

Errata

Gyrus X3ERR/D
Rev. A, 2/2003

Gyrus Microprocessor
Evaluation System
Errata



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1 Introduction

This document describes the known errata and limitations of the Gyrus MPMC card for the Sandpoint reference platform. In all cases, if an errata has a workaround, it is applied to the system before shipped to customers.

The errata revision (“A”, “B”, etc.) is updated every time a new problem is found and systems have been shipped. If your current system is a “Gyrus X3 rev ‘A’”, then it has all rev “A” fixes, but no rev “B” fixes.

The errata should be applied to the published schematics to determine the correct wiring of the MPMC+Sandpoint system (i.e. after changes are applied).

Lastly, note that some errata are not true errors but requests for minor modifications to improve the system. These errata are not performed but may be rolled into possible future revisions of the system, if any.

ERRATA

Table 1: Summary of Gyrus Errata

#	Type	Problem	Impact	Work-Around	Affects	Rev
1	ENG	No connection between CSense and VDD for non-MPC7447A.	Non MPC7447A devices do not regulate properly.	1. Hold PMC with bottom visible and PMC header to the left. 2. Add wire from C22 (near center of CPU (U6) lower pad to R23 right pad (near PMC header). X4: Review geometry.	X3	
2	CAD	No labels for the mictors.	Hard to configure/connect logic analyzer.	Use the names on page 19 of the schematics. X4: Add silkscreen.	X3	-
3	ENG	Some PMCs were created with 22 ohm series resistors instead of 33 ohms in the memory path.	Memory timing problems.	FOR SERIALNO's <= GY15: 1. Examine RN8-RN17 (between the 54-pin SDRAM memory and the '107). If label is '330' , stop. 2. Change RN8-RN15 to 33 ohm RPAKs. 3. Change R77-R83 to 33 ohm resistors. X4: None.	X3	-

Version	Date	Changes
A	2002 Jan 24	Initial Errata

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