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	Revisions	
Rev	Description	Date
A	Release	12-06-2016
В	Update U2 from FXLS8962AF to FXLS8972CF and using footprint DFN_10_0P4_2X2_NSP_A	10-16-2017

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FRDM-STBC-AGMP03



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Designer:	Drawing Title:			
Aswin S	FRDM-STBC-AGMP03			
Drawn by:	Page Title: TITLE PAGE			
Approved: Team	Size Document Number Rev B SCH-29427 PDF: SPF-29427 B			
	Date: Monday, October 16, 2017 Sheet 1 of 4			

1. Unless Otherwise Specified: All resistors are in ohms, 5%, 1/8 Watt All capacitors are in uF, 20%, 50V All voltages are DC All polarized capacitors are aluminum electrolytic 2. Interrupted lines coded with the same letter or letter combinations are electrically connected. 3. Device type number is for reference only. The number varies with the manufacturer. 4. Special signal usage: _B Denotes - Active-Low Signal <> or [] Denotes - Vectored Signals 5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology. MAG3110 MPL3115 FXAS21002C FXLS8972CF I2C/SPI I2C/SPI Selectable I2C I2C Selectable through switch through switch One-wire interface Selectable through switch

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NOTES

Document Number

Date: Monday, October 16, 2017

FRDM-STBC-AGMP03

IUO: X

Sheet 2

SCH-29427 PDF: SPF-29427

PUBI:

Rev

В

MCU



SHIELD BOARD ARDUINO HEADER

J3 SKT_1X8

INT1_8962_A RST 21002 C INT1_3110_ INT2_8962_A INT1_3110_C INT2_8962_C INT1 3115 VDD INT1_3115_C V_8962_C INT1_21002 VDD

RST 21002 A

SHIELD BOARD ARDUINO HEADER

U4

