

NOTES (UNLESS OTHERWISE SPECIFIED):

1. THIS DRAWING SPECIFIES THE REQUIREMENTS FOR A PRINTED WIRING BOARD IN ACCORDANCE WITH SPECIFICATION IPC-A-600 CLASS 2 (LATEST REVISION).

2. THE PWB MUST BE LEAD FREE ASSEMBLY PROCESS COMPATIBLE AND MUST BE ABLE TO HANDLE A MINIMUM OF 5 CYCLES AT 260 DEGREES CELSIUS FOR 10 SECONDS.

3. BASE MATERIAL - LAMINATE AND PREPREG SHALL MEET IPC-4101B-26, 83 or 98  
T<sub>g</sub> - MUST BE GREATER THAN OR EQUAL TO 150 DEGREES CELSIUS.  
T<sub>d</sub> - MUST BE GREATER THAN OR EQUAL TO 330 DEGREES CELSIUS.

4. COPPER FOIL WEIGHT - SEE STACKUP DETAIL 'A'

5. CHARACTERISTIC IMPEDANCE - NONE

6. MINIMUM CONDUCTIVE WIDTH/SPACING TO BE .009-.006

7. PLATING FINISH - BOTH SIDES ENIG (ELECTROLESS NICKEL IMMERSION GOLD):  
.05080-.232 MICRON (2-8 MICROINCH) OF GOLD OVER  
2.540-5.080 MICRON (100-200 MICROINCH) OF NICKEL.

8. ALL THROUGH HOLE VIAS MAY BE PLATED SHUT.

9. SOLDERMASK - GREEN COLOR (TAIYO OR EQUIVALENT), BOTH SIDES.  
MODIFICATION OF SOLDERMASK IS NOT ALLOWED WITHOUT WRITTEN PERMISSION FROM FREESCALE.

10. SILKSCREEN - WHITE EPOXY INK, BOTH SIDES. NO SILK ON PADS.

1. ELECTRICAL TEST - 100% IPCD356.

2. PRINTED WIRING BOARD IS TO BE INDIVIDUALLY BAGGED.

3. DRC'S MUST BE RUN ON THE GERBER BEFORE BUILDING BOARDS, UNLESS PRIOR APPROVAL IS GIVEN IN WRITING BY FREESCALE.

4. TEARDROPS MAYBE ADDED AT THE FAB HOUSE TO ALL SIGNAL LAYERS.

15. TWO SOLDER SAMPLES TO BE PROVIDED.

6. BASIC GRID INCREMENT AT 1:1 IS .0001.

7. SUPPLIER MARKINGS - ON SOLDER SIDE ONLY, WHERE SHOWN.

8. THE PWB WILL BE MARKED AS LEAD FREE BY USE OF AN INK STAMP (Pb)

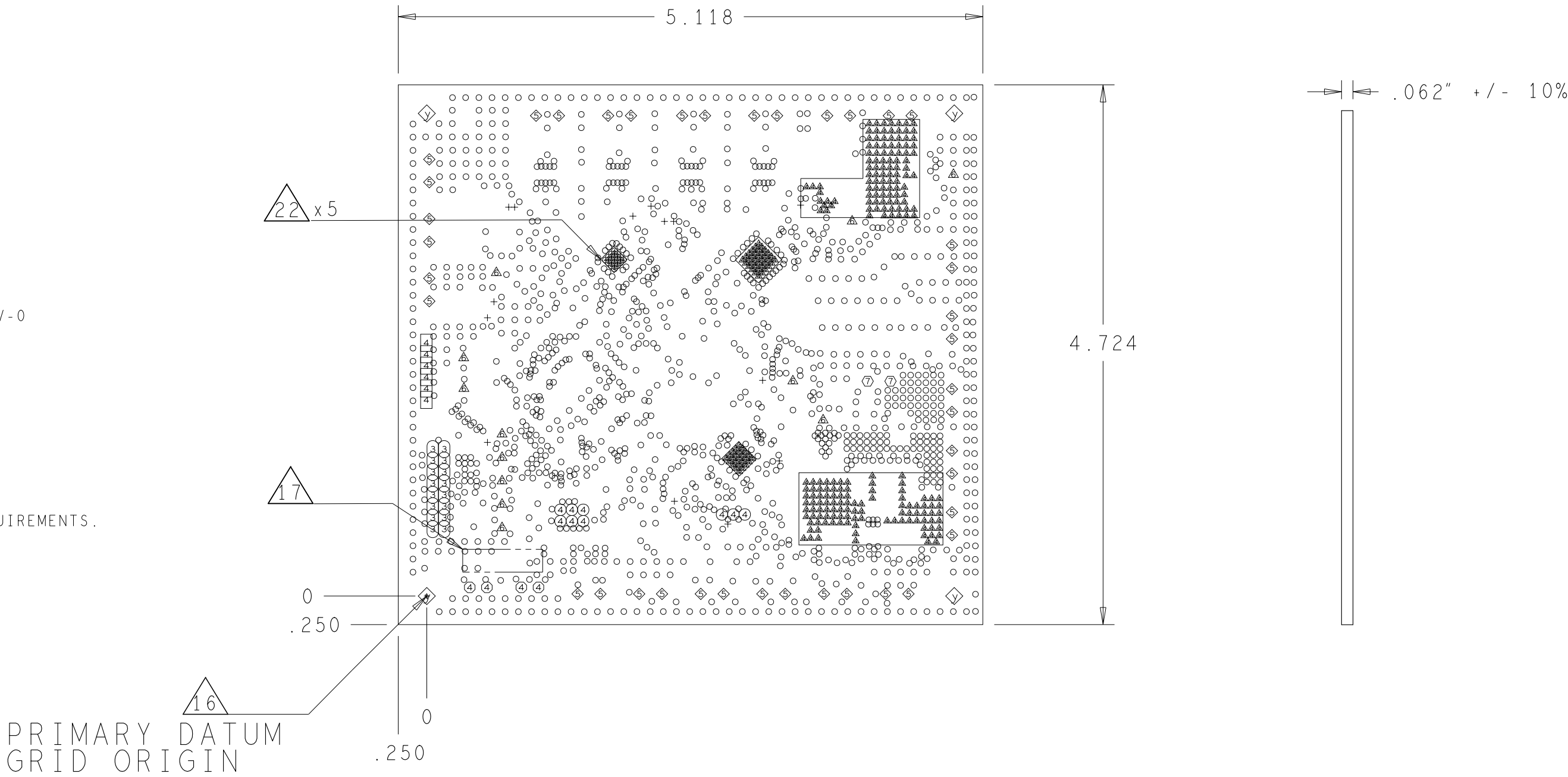
9. THE PWB WILL BE MARKED AS LEAD FREE PROCESS COMPATIBLE BY USE OF AN INK STAMP (260°C)

20. ALL PLATED AND NON-PLATED THROUGH HOLES ARE TO BE DRILLED AT PRIMARY DRILL STEP.  
ALL HOLE LOCATION TOLERANCES ARE TO BE  $\pm .002$  IN REFERENCE TO THE PRIMARY DATUM.





21. FINISHED PCB MUST BE PANELIZED FOR ASSEMBLY ACCORDING TO CONTRACT MANUFACTURERS REQUIREMENTS. THE ADDITION OF RAILS AND .125" NON-PLATED TOOLING HOLES ARE AT THE DISCRETION OF CONTRACT MANUFACTURER. PANELIZATION MUST BE APPROVED BY CONTRACT MANUFACTURER.

2. THIS BOARD USE VIA IN PAD:

- A. VIA IN PAD TO BE FILLED WITH NON-CONDUCTIVE VIA FILL  
LACKWERKE-PETERS PP2795 OR EQUIVALENT AND MADE PLANNAR TO THE PADS.  
B. OVERPLATE THE FILLED VIA AND APPLY FINISH METAL TREATMENT.



DRILL CHART: TOP to BOTTOM				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
•	8.0	+0.0/-8.0	PLATED	16
◦	10.0	+0.0/-10.0	PLATED	1458
▲	12.0	+0.0/-12.0	PLATED	263
+	20.0	+0.0/-10.0	PLATED	14
⊖	39.0	+3.0/-3.0	PLATED	16
⊙	40.0	+3.0/-3.0	PLATED	9
⊞	41.0	+3.0/-3.0	PLATED	6
⊗	48.0	+3.0/-3.0	PLATED	4
◆	55.0	+3.0/-3.0	PLATED	40
▲	63.0	+3.0/-3.0	PLATED	12
⊖	76.0	+2.0/-2.0	PLATED	2
◇	125.0	+2.0/-2.0	NON-PLATED	4

		FINISHED Cu WEIGHT
	LAYER 1 COMPONENT SIDE	2 oz.
	LAYER 2 GROUND PLANE	2 oz.
	LAYER 3 SIGNAL 1	2 oz.
	LAYER 4 GROUND PLANE	2 oz.

DETAIL A  
LAYER STACKUP

SCALE: NONE

PART NO.		170-28642		PART NO.							
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UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: 											