

1. THIS DRAWING SPECIFIES THE REQUIREMENTS FOR A PRINTED WIRING BOARD IN ACCORDANCE WITH SPECIFICATION IPC-A-600 CLASS 2 (LATEST REVISION).
2. THE PWB MUST BE LEAD FREE ASSEMBLY PROCESS COMPATIBLE AND MUST BE ABLE TO HANDLE A MINIMUM OF 5 CYCLES AT 160 DEGREES CELSIUS FOR 10 SECONDS.
3. BASE MATERIAL - LAMINATE AND PREPREG SHALL MEET IPC-4101B-26, #3 or #8.
T_g - MUST BE GREATER THAN OR EQUAL TO 150 DEGREES CELSIUS.
T_d - MUST BE GREATER THAN OR EQUAL TO 330 DEGREES CELSIUS.
4. COPPER FOIL WEIGHT - SEE STACKUP DETAIL 'A'
5. CHARACTERISTIC IMPEDANCE - NOT APPLICABLE
6. MINIMUM CONDUCTIVE WIDTH/SPACING TO BE .005"/.005"
7. PLATING FINISH - BOTH SIDES ENDS (ELECTROLESS NICKEL IMMERSION GOLD):
.05080 - .232 MICRON (2.48 MICRON) OF GOLD OVER
2.540-6.350 MICRON (100-250 MICRON) OF NICKEL.

9. SOLDERMASK - ORANGE COLOR (TAIYO OR EQUIVALENT), BOTH SIDES.
MODIFICATION OF SOLDERMASK IS NOT ALLOWED WITHOUT WRITTEN PERMISSION FROM FREESCALE.

10. SILKSCREEN - WHITE EPOXY INK, BOTH SIDES. NO SILK ON PADS.

11. ELECTRICAL TEST - 100% IPCD356.

12. PRINTED WIRING BOARD IS TO BE INDIVIDUALLY BAGGED.

13. DRC'S MUST BE RUN ON THE GERBER BEFORE BUILDING BOARDS.

14. TEARDROPS MAYBE ADDED AT THE FAB HOUSE TO ALL SIGNAL LAYERS.

15. TWO SOLDER SAMPLES TO BE PROVIDED.

16 BASIC GRID INCREMENT AT 1:1 IS .0001.

17. SUPPLIER MARKINGS - ON SOLDER SIDE ONLY, WHERE SHOWN.
- MUST BE UL RECOGNIZED AND MUST HAVE AN ID THAT CONFORMS TO UL94V-0

18. THE PWB WILL BE MARKED AS LEAD FREE BY USE OF AN INK STAMP ~~Pb~~

19. THE PWB WILL BE MARKED AS LEAD FREE PROCESS COMPATIBLE BY USE OF AN INK STAMP (260°C)

20. ALL PLATED AND NON-PLATED THROUGH HOLES ARE TO BE DRILLED AT PRIMARY DRILL STEP.
ALL HOLE LOCATION TOLERANCES ARE TO BE $\pm .002$ IN REFERENCE TO THE PRIMARY DATUM.

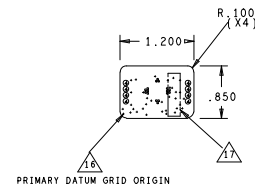
21. FINISHED PCB MUST BE PANELIZED FOR ASSEMBLY ACCORDING TO CONTRACT MANUFACTURERS REQUIREMENTS. THE ADDITION OF RAILS AND .125"NON-PLATED TOOLING HOLES ARE AT THE DISCRETION OF CONTRACT MANUFACTURER. PANELIZATION MUST BE APPROVED BY CONTRACT MANUFACTURER.

22. INTENTIONAL SHORTS AT

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22. INTENTIONAL SHORTS AT
RefDes  Net1      Net2
SH1      INT1_C    INT1
SH2      INT2_C    INT2
SH3      SCL_C     SCL_BRKT
SH4      SDA_C     SDA_BRKT
SH5      VDD_8451_C VDD
SH6      VDDIO_8451_C VDD
SH7      GND_C     GND
SH8      SA0_C     SA0_A



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—▶|◀— 0.062" THICK +/-10%

DRILL CHART: TOP to BOTTOM				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
•	8.0	+0.0/-8.0	PLATED	35
•	12.0	+2.0/-2.0	PLATED	16
⊙	40.0	+3.0/-3.0	PLATED	8

FINISHED Cu WEIGHT

	LAYER 1	COMPONENT SIDE	1 oz.
	LAYER 2	SOLDER SIDE	1 oz.

DETAIL A
LAYER STACKUP
SCALE: NONE

PART <u>170-29412</u> THIS DOCUMENT CONTAINS INFORMATION PROPRIETARY TO PRECASTALC AND SHALL NOT BE USED FOR ENGINEERING DESIGN OR CONSTRUCTION OR MANUFACTURE IN WHOLE OR IN PART WITHOUT THE CONSENT OF PRECASTALC.		NXP 6501 WILLIAM CANNON DRIVE WEST AUSTIN, TEXAS 78735 USA	
PURI (PUBLIC INFORMATION) FPD (FREESCALE INTERNAL USE ONLY) FCP (FREESCALE COMMERCIAL PROPRIETARY)			
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: DECIMALS .010 ANGLES .5° HOLE DIA .005 HOLE POS .005 ✓ RRS ALL UNNOTED SURFACES BREAK ALL SHARP EDGES AND CORNERS. REMOVE BURRS. DIMENSIONS SHOWN UNLESS OTHERWISE NOTED. DIMENSIONS ARE TO SCALE, UNLESS ANGLE OR GRAPHIC PROJECTION IS USED.		TITLE PRINTED WIRING BOARD BRKTSTBC-A8451	
APPROVALS DRAWN ASWIN S CHECKED HALEN X DESIGN ASWIN S		DATE 09-30-16 09-30-16 09-30-16	
SIZE 0		CDF FILE NAME DNG. LINK FAB-29412 SCALE 1/1 DO NOT SCALE DRAWING	
SHEET 1 OF 1		REV A	