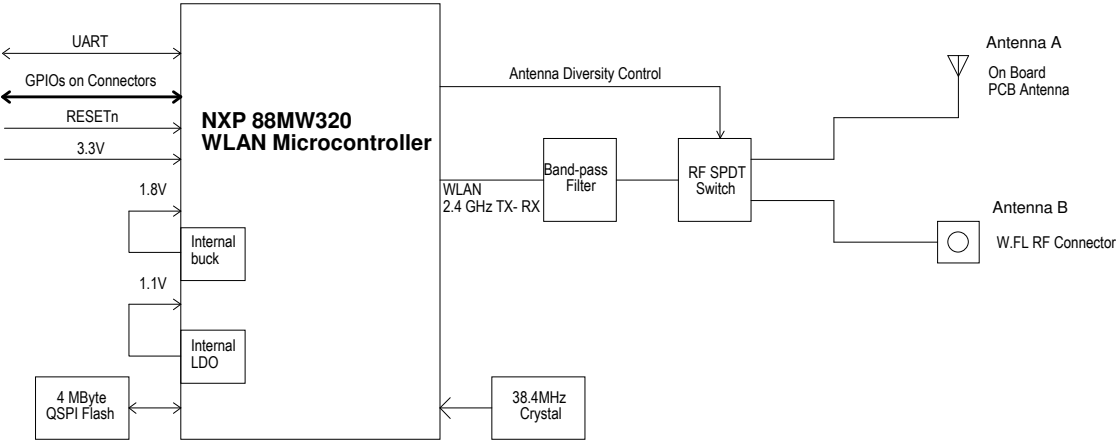


# NXP SEMICONDUCTORS

## Block Diagram



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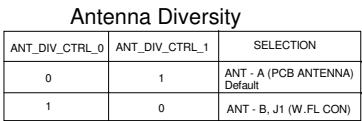
- 1. Block Diagram
- 2. 88MW320 QFN-68 SoC , Power, RF
- 3. Revision History

## WLAN Microcontroller, IEEE 802.11n/g/b RD-88MW320-QFN-1B-2A v1.0 Schematic

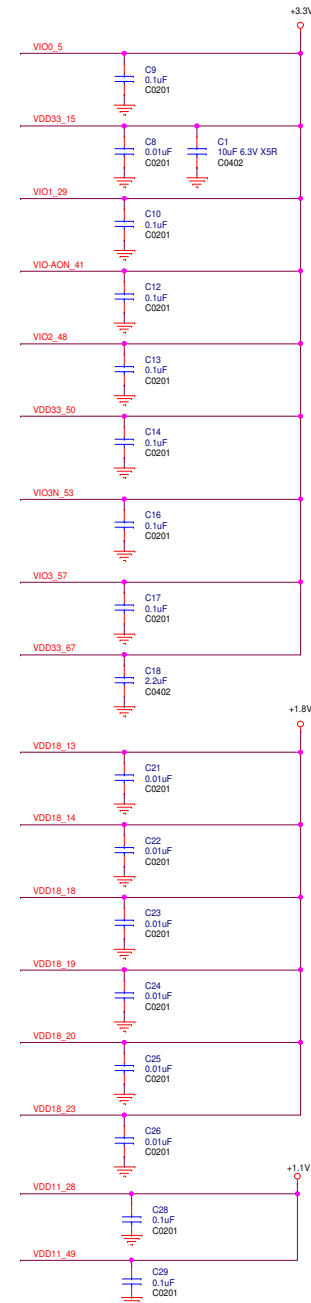
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## WLAN RF Front End



Pinout diagram for the RF Antenna Portion of the MODULE\_50\_50\_21X37.5. The diagram shows a central blue box labeled "RF Antenna Portion" with pins 1 through 31. Pins 1-18 are on the left, and pins 19-31 are on the bottom. A 3.3V supply is connected to pin 1. Various signals are connected to other pins, including UART1\_CTS, UART1\_RTS, UART1\_TXD, UART1\_RXD, GPIO16, GPIO17, GPIO18, GPIO19, GPIO20, GPIO21, GPIO22, GPIO23, GPIO24, GPIO25, GPIO26, GPIO27, GPIO28, GPIO29, GPIO30, GPIO31, GPIO32, GPIO33, GPIO34, GPIO35, GPIO36, GPIO37, GPIO38, GPIO39, GPIO40, GPIO41, GPIO42, GPIO43, GPIO44, GPIO45, GPIO46, GPIO47, GPIO48, GPIO49, GPIO50, GPIO51, GPIO52, GPIO53, GPIO54, GPIO55, GPIO56, GPIO57, GPIO58, GPIO59, GPIO60, GPIO61, GPIO62, GPIO63, GPIO64, GPIO65, GPIO66, GPIO67, GPIO68, GPIO69, GPIO70, GPIO71, GPIO72, GPIO73, GPIO74, GPIO75, GPIO76, GPIO77, GPIO78, GPIO79, GPIO80, GPIO81, GPIO82, GPIO83, GPIO84, GPIO85, GPIO86, GPIO87, GPIO88, GPIO89, GPIO90, GPIO91, GPIO92, GPIO93, GPIO94, GPIO95, GPIO96, GPIO97, GPIO98, GPIO99, GPIO100. The diagram also shows a 3.3V supply connected to pin 1 and a ground connection for pin 19.



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Revision History		
Design Version	Document Revision	Change List
V1.0	Rev 1	Initial release

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