Installation manual for the BFU5XX family ADS design kit

Rev. 2 — 18 April 2014

Installation manual

Document information

Info	Content	
Keywords	Installation manual BFU5XX ADS design kit	
Abstract	This document describes the installation of the NXP BFU5XX wideband family design kit for ADS	



Revision history

Rev	Date	Description
1	20131204	Initial document
2	20140418	Added BFU580G,BFU590G,BFU580Q and BFU590Q models

Contact information

For more information, please visit: http://www.nxp.com

For sales office addresses, please send an email to: salesaddresses@nxp.com

1. Introduction

The BFU5xxX transistor family is designed to meet the latest requirements on high frequency applications (up to approximately 1.5 GHz) such as communication, automotive and industrial equipment.

As soon as fast, low noise analogue signal processing is required, combined with medium to high voltage swings the BFU5xxX transistors are the perfect choice. Due to the high gain at low supply current those types can also be applied very well in battery fed equipment.

Compared to previous Philips / NXP transistor generations and competitor products improvements on gain, noise and thermal properties are realized. BFU5xxX transistors will be available in various packages.

To enhance the design cycle speed, accurate models have been extracted, and combined in an ADS design kit. For this, the most accurate bipolar model, MEXTRAM is used, which is supported by the Agilent ADS simulator. Also the parasitic and package has been modeled, to cover up to 6 GHz, over the entire bias range.

A summary of the available models in the design kit is shown in Table. 1

Transistor	Package	
BFU520W, BFU530W, BFU550W	SOT323	
BFU520A, BFU530A, BFU550A	SOT23	
BFU520, BFU530, BFU550	SOT143	
BFU520X, BFU530X, BFU550X	SOT143X	
BFU520XR, BFU530XR, BFU550XR	SOT143XR	
BFU580G, BFU590G	SOT223	
BFU580Q, BFU590Q	SOT89	

Table 1. Devices includes in the ADS design kit

2. Installation procedure

This section describes step-by-step the installation of the design kit.

- 1. Copy the file in the ADS home directory
- 2. Close all ADS schematics
- 3. In the main window of ADS, select Design Kit -→ Unzip Design Kit, browse to the location where you have copied the BFU5XX_family_ADS_design_kit _v2.zip.
- 4. After unzipping the design kit, select Design Kit -→ Manage Favorite Design Kits, and mark the check box "In Current Workspace" associated with the design kit. After this, all devices will be available in your design environment, named as ads_nym_ bfu5xx.
- 5. This will complete the installation.

3. Legal information

3.1 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

3.2 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Evaluation products — This product is provided on an "as is" and "with all faults" basis for evaluation purposes only. NXP Semiconductors, its affiliates and their suppliers expressly disclaim all warranties, whether express, implied or statutory, including but not limited to the implied warranties of non-infringement, merchantability and fitness for a particular purpose. The entire risk as to the quality, or arising out of the use or performance, of this product remains with customer.

In no event shall NXP Semiconductors, its affiliates or their suppliers be liable to customer for any special, indirect, consequential, punitive or incidental damages (including without limitation damages for loss of business, business interruption, loss of use, loss of data or information, and the like) arising out the use of or inability to use the product, whether or not based on tort (including negligence), strict liability, breach of contract, breach of warranty or any other theory, even if advised of the possibility of such damages.

Notwithstanding any damages that customer might incur for any reason whatsoever (including without limitation, all damages referenced above and all direct or general damages), the entire liability of NXP Semiconductors, its affiliates and their suppliers and customer's exclusive remedy for all of the foregoing shall be limited to actual damages incurred by customer based on reasonable reliance up to the greater of the amount actually paid by customer for the product or five dollars (US\$5.00). The foregoing limitations, exclusions and disclaimers shall apply to the maximum extent permitted by applicable law, even if any remedy fails of its essential purpose.

3.3 Licenses

Purchase of NXP <xxx> components

<License statement text>

3.4 Patents

Notice is herewith given that the subject device uses one or more of the following patents and that each of these patents may have corresponding patents in other jurisdictions.

<Patent ID> — owned by <Company name>

3.5 Trademarks

Notice: All referenced brands, product names, service names and trademarks are property of their respective owners.

<Name> — is a trademark of NXP B.V.