AUD-EXP-42448

Table of Content

Page 1	COVER
Page 2	BLOCK DIAGRAM
Page 3	POWER&INTERFACE
Page 4	AUDIO CODEC
Page 5	AUDIO INPUT
Page 6	AUDIO OUTPUT
Page 7	
Page 8	
Page 9	
Page 10	
Page 11	
Page 12	
Page 13	
Page 14	
Page 15	
Page 16	
Page 17	
Page 18	
Page 19	
Page 20	
Page 21	
Page 22	
Page 23	
Page 24	
Page 25	
Page 26	
Page 26	
Page 27	
Page 28	

Revision History

Rev. Code	Date	By	Description
A	2021-01-06	Shawn Shi	Initial Release
B	2021-03-26	Shawn Shi	1. Add R83(DNP) for 3V3 power option 2. Add P1/P2, R84/R85 for MIC input 3. Populate R82, DNP R28 for DGND/AGND connection

REF DES	JUMPER(DEFAULT)	PAGE NAME
J13,J14,J2	1-2	03 POWER&INTERFACE
J4,J3	2-3	03 POWER&INTERFACE
J5	1-2	04 AUDIO CODEC

1. Unless Otherwise Specified:

   All resistors are in ohms, 1/16 Watt,0402
   All capacitors are in uF,0402
   All voltages are DC
   All polarized capacitors are aluminum electrolytic

2. Interrupted lines coded with the same letter or letter combinations are electrically connected.

3. Device type number is for reference only. The number varies with the manufacturer.

4. Special signal usage:
   _B Denotes - Active-Low Signal
   <> or [] Denotes - Vectored Signals

5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

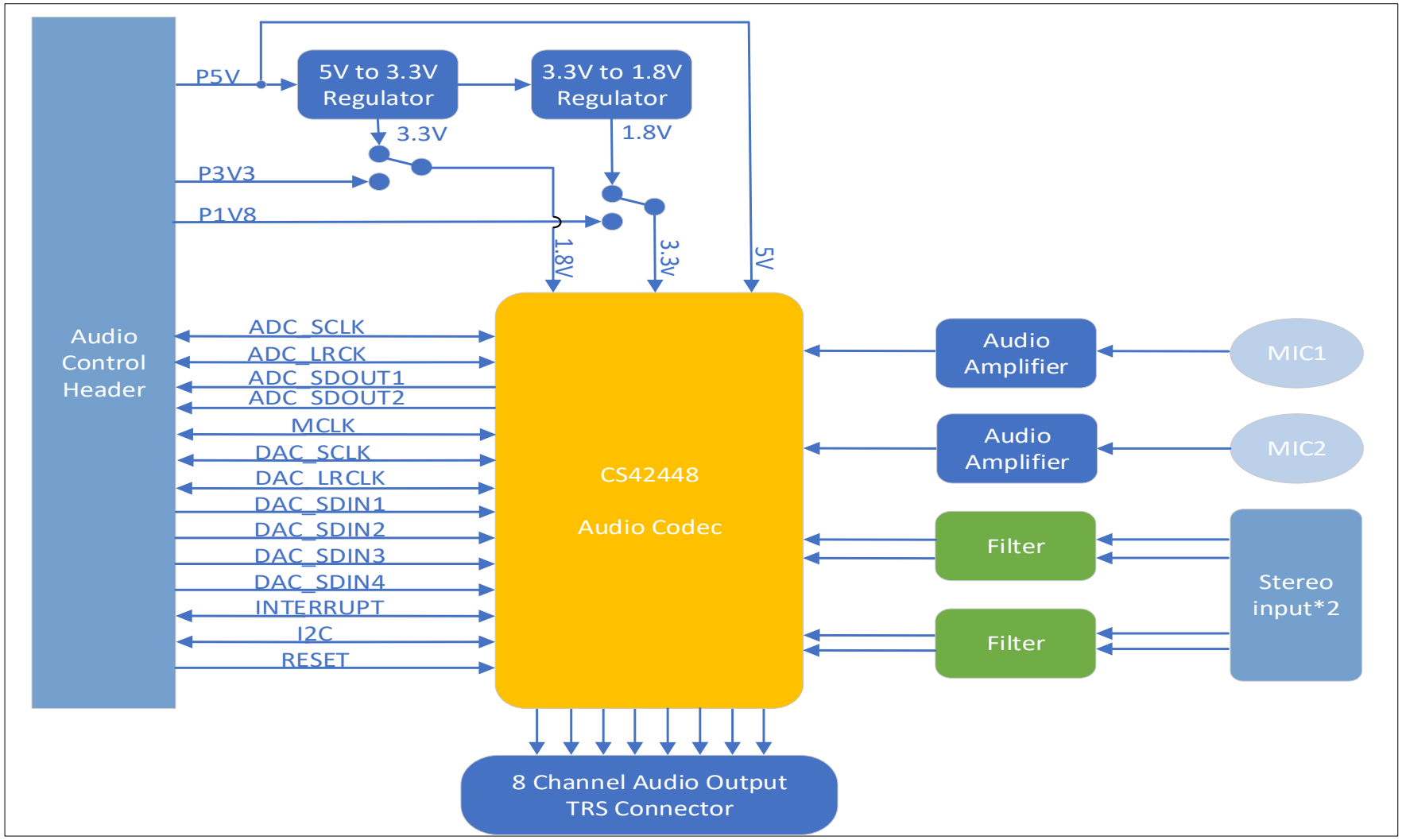
Microcontroller Product Group
6501 William Cannon Drive West
Austin, TX 78735-8598

ICAP Classification: CP: IUO: PUBI:

Designer: Shawn Shi	Drawing Title: AUD-EXP-42448	
Drawn by: Shawn Shi	Page Title: COVER	
Approved: Yes	Size C	Document Number SCH-48012, PDF: SPF-48012 Rev B
	Date: Friday, March 26, 2021	Sheet 1 of 6

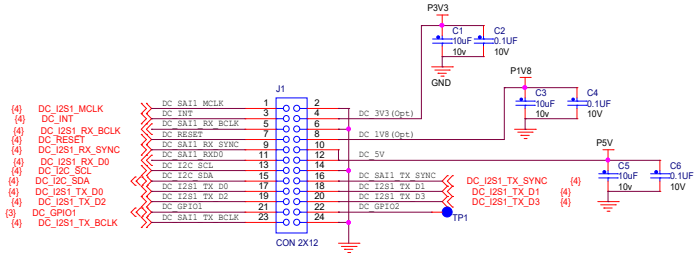
AUD-EXP-42448

Blcok Diagram Rev B#####

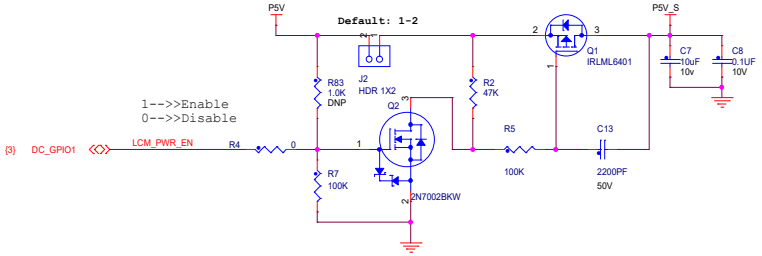


ICAP Classification:	CP: ___	IUO: X	PUBI: ___
Drawing Title:	AUD-EXP-42448		
Page Title:	BLOCK DIAGRAM		
Size C	Document Number SCH-48012, PDF: SPF-48012		Rev B
Date: Monday, March 29, 2021	Sheet 2 of 6		

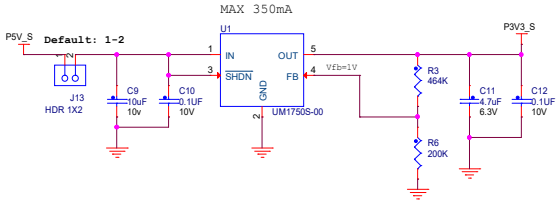
Audio Main CONN



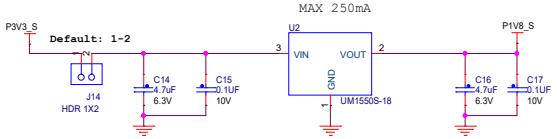
5V Control



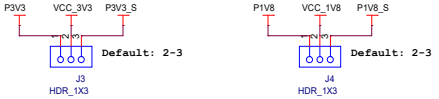
3V3 LDO



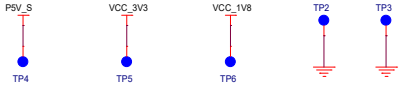
1V8 LDO



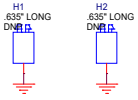
Power Options



Test Points



Standoff Mounting Holes



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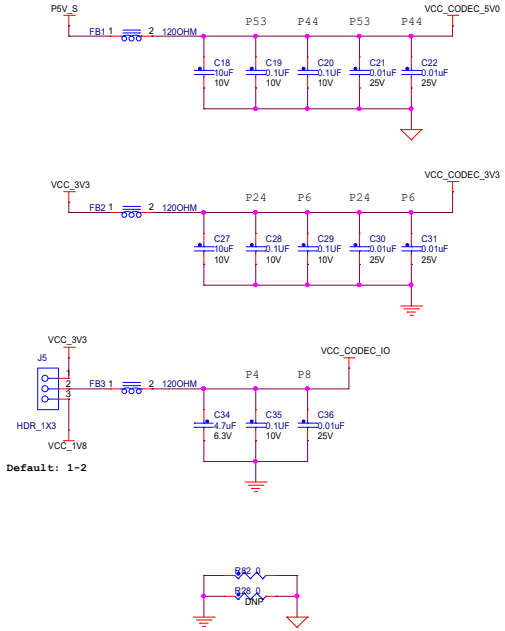
Drawing Title: **AUD-EXP-42448**

Page Title: **POWER&INTERFACE**

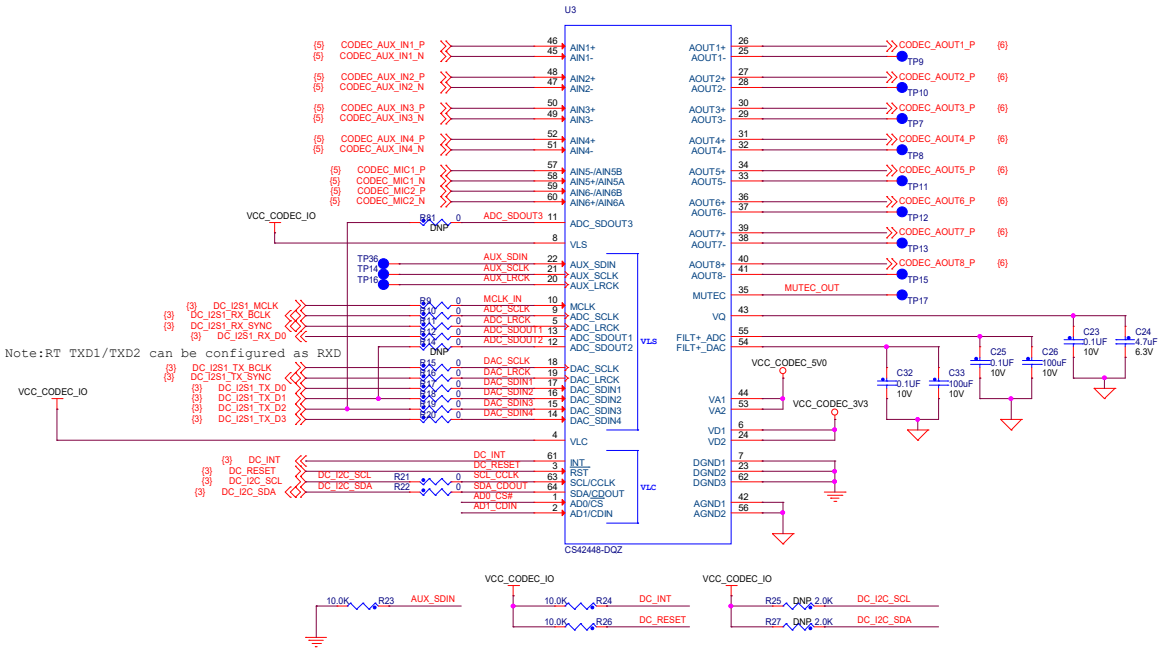
Size	Document Number	Rev
C	SCH-48012, PDF: SPF-48012	B

Date: Friday, March 26, 2021 Sheet 3 of 6

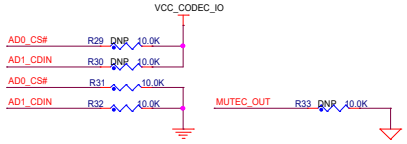
Audio Codec Power&GND



Audio Codec



I2C Address Config



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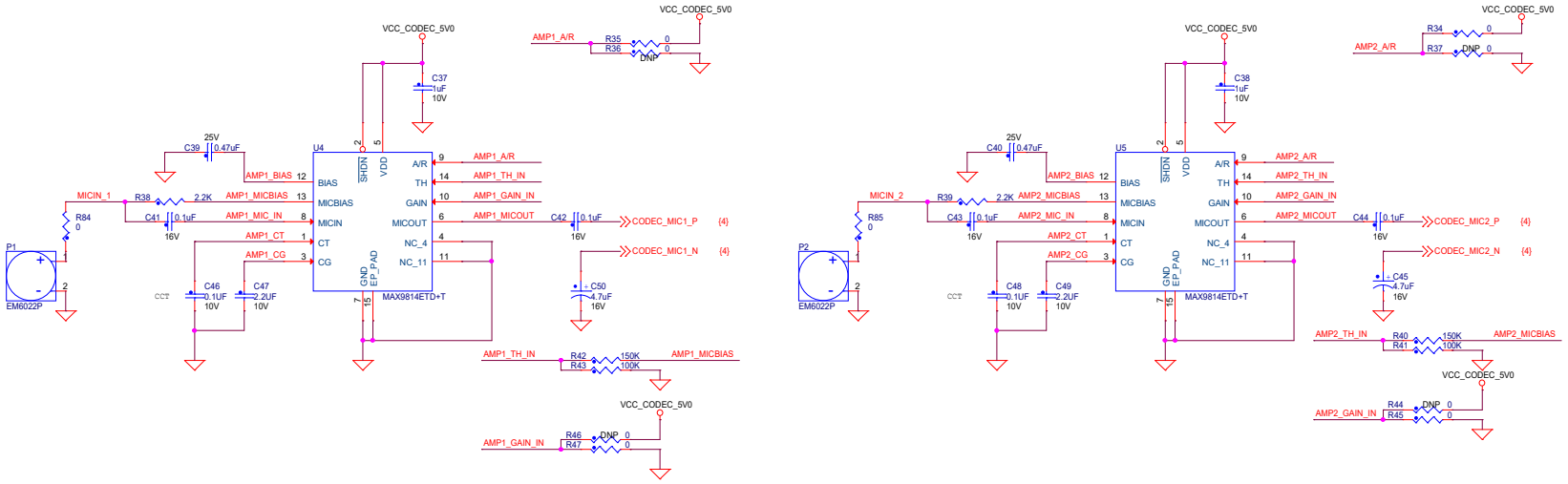
Drawing Title: **AUD-EXP-42448**

Page Title: **AUDIO CODEC**

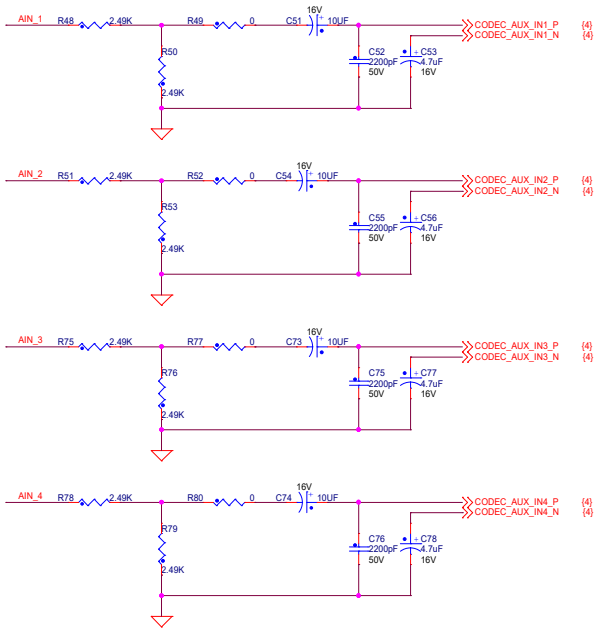
Size	Document Number	Rev
C	SCH-48012, PDF: SPF-48012	B

Date: Friday, March 26, 2021 Sheet 4 of 6

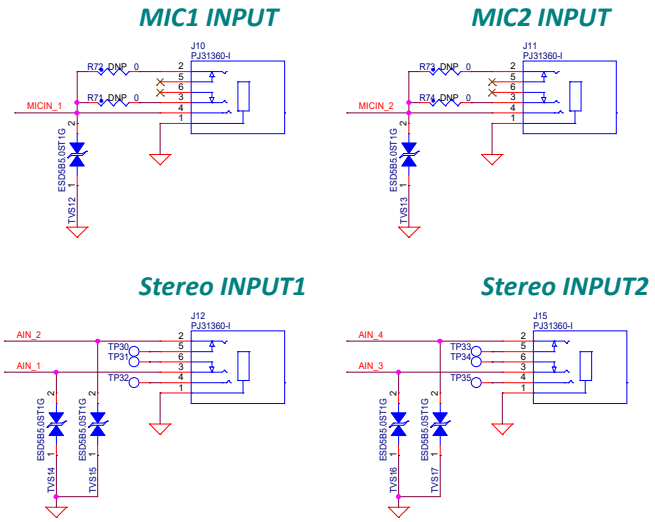
Audio Input Amplifiers



Auxiliary Audio Input Filter



Input TRS Connectors



NXP

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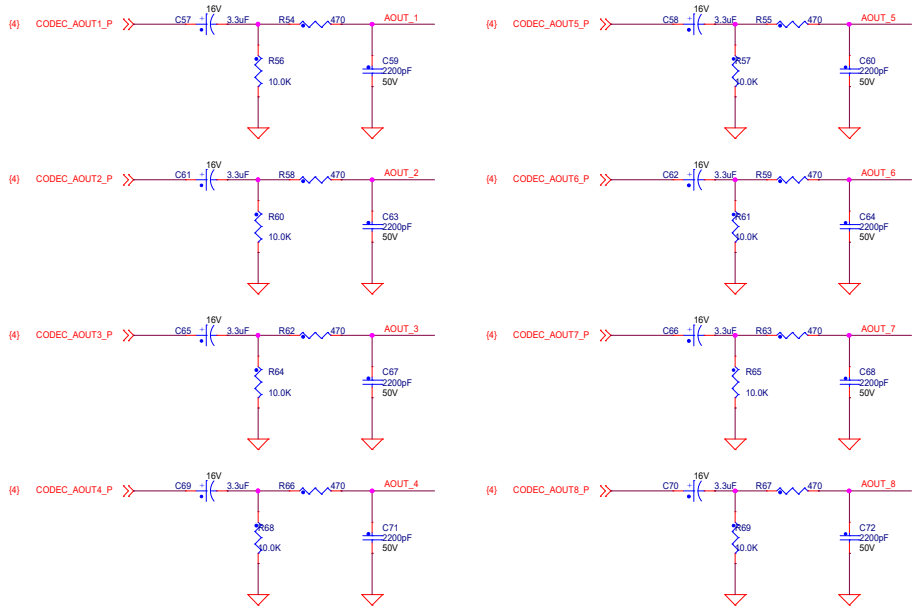
Drawing Title: **AUD-EXP-42448**

Page Title: **AUDIO INPUT**

Size	Document Number	Rev
C	SCH-48012, PDF: SPF-48012	B

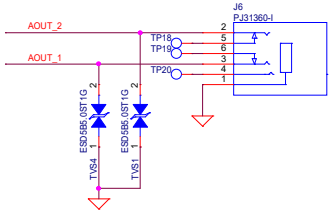
Date: Friday, March 26, 2021 | Sheet 5 of 6

Audio Output Filters

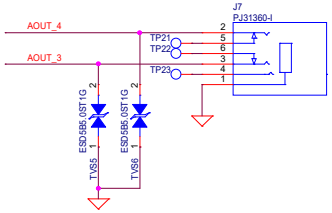


Output TRS Connectors

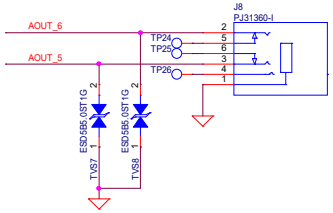
LINE1&LINE2 OUTPUT



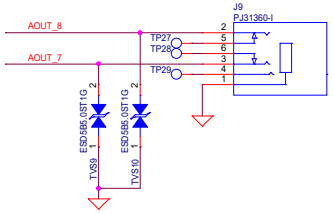
LINE3&LINE4 OUTPUT



LINE5&LINE6 OUTPUT



LINE7&LINE8 OUTPUT



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Drawing Title: **AUD-EXP-42448**		
Page Title: **AUDIO OUTPUT**		
Size C	Document Number SCH-48012, PDF: SPF-48012	Rev B
Date: Friday, March 26, 2021	Sheet 6 of 6	