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i.MX 配置工具的快速入门指南 Rev. 13 — 18 September 2025

User guide

Document information

Information	Content
Keywords	MCUXpresso配置工具,i.MX
Abstract	i.MX 配置工具 MCUXpresso 配置工具的一部分,是一套评估和配置工具,可指导用户完成从首次评估到生产软件开发的整个流程。使用 i.MX 配置工具可轻松简便地配置 i.MX 处理器设备的引脚和 DDR。一般而言,该软件使您能够创建、检查、更改和修改设备的针脚配置和多路复用方面的任何内容。它还允许您配置和验证 DDR 设置。本文档介绍了 i.MX 配置工具的基本组件,并列出了配置和使用它们来配置管脚和 DDR 的步骤。



1 简介

i.MX 配置工具是一套用于配置 NXP i.MX Cortex-A 和 Cortex-M 处理器的工具套件。

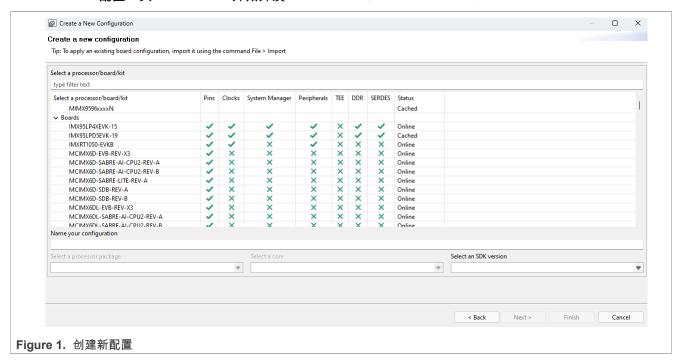
使用引脚工具可以自定义设备的引脚路由配置,包括配置所有相关引脚信号的电气特性,并创建适用于设备初始化的源代码,可以是寄存器的直接初始化或 SDK API 代码和/或设备树代码片段(如果支持)。

使用 DDR 工具可以配置和验证双倍数据速率 RAM 配置。

使用可信执行环境 (TEE) 工具可以配置内存区域、总线主控和外设的安全策略,以隔离和保护应用程序的敏感区域。

2 开始新配置

首次启动 i.MX 配置工具时,您将看到开始开发窗口。您可以使用此窗口创建新配置或加载现有配置。



要为选定的处理器、开发板或套件创建新配置,请执行以下操作:

- 1. 启动工具或选择文件 > 新建。
- 2. 选择选项为处理器、开发板或套件创建新配置。
- 3. 选择下一步。
- 4. 展开树形结构并选择任何处理器、开发板或套件配置。您也可以使用过滤器字段快速查找所需项目。
- 5. 自定义配置名称并选择完成。

使用文件 > 保存将现有配置保存到磁盘。

3 导入现有配置

您还可以使用内置的导入向导导入现有配置,从传统项目格式(IO Mux 工具设计配置 XML 或 i.MX 的 PEx)或另一个已存在的配置文件 (MEX) 或引脚工具生成的包含 YAML 配置详细信息的源文件中获取引脚和 DDR 配置。

要导入现有配置:

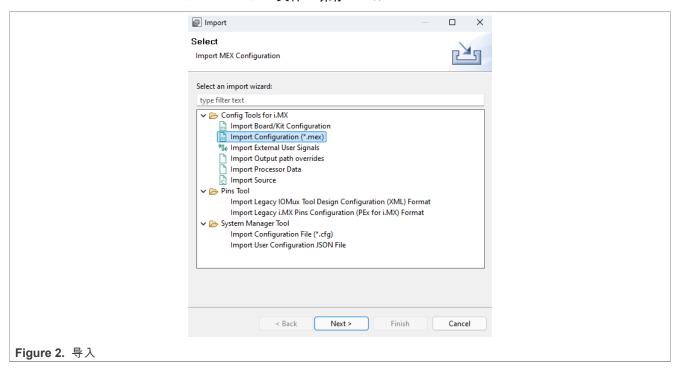
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- 1. 选择文件 > 导入。
- 2. 选择导入向导,选择下一步并按照说明操作。
- 3. 选择浏览, 然后选择所需的输入文件。
- 4. 选择完成以导入文件。

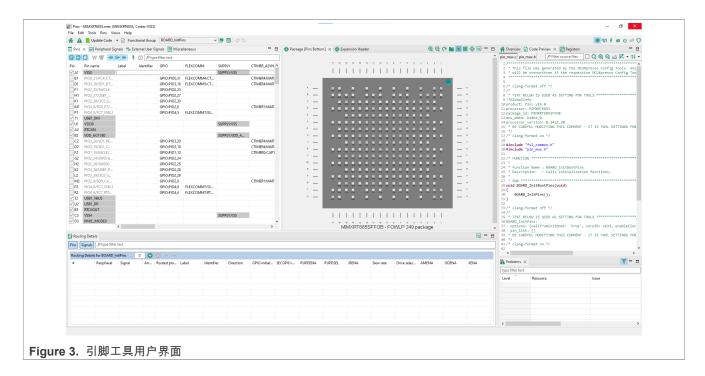
如果导入成功完成,将创建新配置,然后使用文件 > 保存将其保存到磁盘。



4 引脚工具

在引脚工具中,您可以显示和配置所选处理器的引脚。基本配置可以在引脚、外设信号或封装视图中完成。 更高级的设置(引脚电气特性和功能)可以在路由引脚视图中查看和配置。

此外,**i.MX** 配置工具允许您在硬件级别验证给定功能组内来自不同电源轨(如果为给定处理器指定)的引脚配置可能存在的电压电平问题。支持的电源组的各个电压电平可以在电源组视图中为当前配置进行全局配置。



5 DDR 工具

在 DDR 视图中,您可以查看和配置基本 DDR 属性,如内存类型、频率、通道数等。

在验证视图中,您可以将 DDR 配置提交给各种测试。指定连接类型后,您可以选择场景、在这些场景中运行的测试,并查看测试结果、日志和摘要。

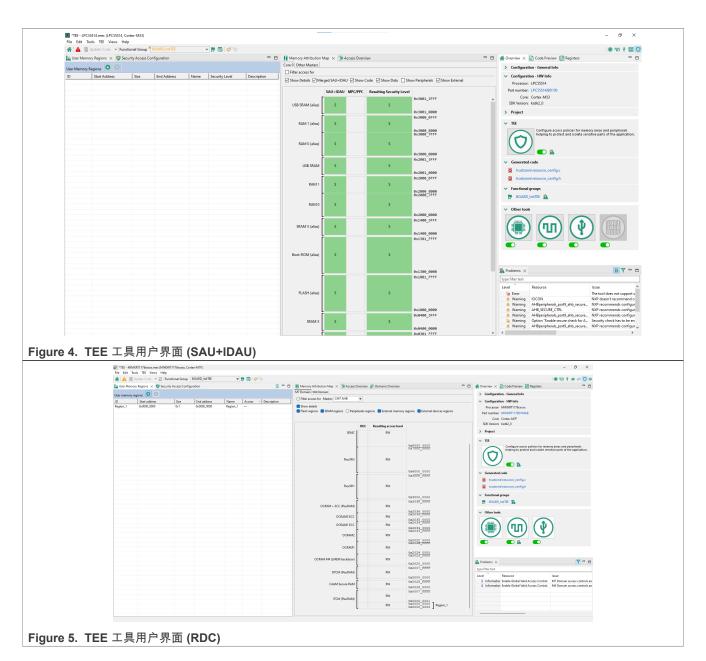
6 可信执行环境工具

在可信执行环境或 TEE 工具中,您可以配置内存区域、总线主控和外设的安全策略,以隔离和保护应用程序的敏感区域。

您可以在安全访问配置及其子视图中设置应用程序不同部分的安全策略,并在内存属性映射、访问概览和域概 览视图中查看这些策略。使用用户内存区域视图可以创建内存区域及其安全级别的便捷概览。

您还可以在寄存器视图中查看 TEE 工具处理的寄存器,并在代码预览工具中检查代码。

注意: 为了使您的配置生效,请确保您已在安全访问配置视图的杂项子视图中启用了相关的启用安全检查选项。



7 生成代码

引脚工具会自动为当前配置生成输出代码。您也可以从主菜单选择引脚 > 刷新来手动更新代码。所有输出代码文件都显示在代码预览视图中。要复制代码,请执行复制/粘贴操作或单击代码预览视图右上角的导出图标。

```
♠ Overview   Code Preview ×  Registers

                                                                       Q @ @ 🖆 🔀 🕶
                                pin_mux.c pin_mux.h
                                   1/***************
                                   2 * This file was generated by the MCUXpresso Confi
                                   * will be overwritten if the respective MCUXpress
                                   6/* clang-format off */
                                   8 * TEXT BELOW IS USED AS SETTING FOR TOOLS *******
                                   9!!GlobalInfo
                                  10 product: Pins v14.0
                                  11 processor: MIMXRT685S
                                  12 package_id: MIMXRT685SFVKB
                                  13 mcu data: ksdk2 0
                                  14 processor version: 0.14.4
                                  15 board: MIMXRT685-EVK
                                  16 * BE CAREFUL MODIFYING THIS COMMENT - IT IS YAML
                                  18 /* clang-format on */
                                  19
                                  20 #include "fsl_common.h"
                                  21 #include "fsl_gpio.h"
22 #include "fsl_iopctl.h"
                                  23 #include "pin mux.h"
                                  25 /* FUNCTION ************************
                                  26 *
                                  27 * Function Name : BOARD InitBootPins
                                  28 * Description : Calls initialization functions.
                                  Figure 6. 生成代码
```

或者,您也可以导出各种类型的生成输出,如源文件、CSV格式的纯引脚配置数据、修改的寄存器内容或HTML格式的引脚配置报告,可通过从主菜单的文件 > 导出中选择特定的导出向导。

8 修订历史

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IMXQSUG_ZH v.5	2023年7月31日	为 v.14 更新
IMXQSUG_ZH v.4	2023年1月2日	截图已更新,小幅更新
IMXQSUG_ZH v.3	2022年9月20日	为 v.12.1 更新
IMXQSUG_ZH v.2	2022年6月30日	为 v.12 更新
IMXQSUG_ZH v.1	2021年12月22日	截图已更新。
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