# AN12969

## 铜排设计应用笔记

第2版 — 2023年5月15日

应用笔记

#### 文档信息

信息	内容
关键词	MC33771x、铜排
摘要	本应用笔记介绍了如何将铜排连接到恩智浦电芯控制器。



铜排设计应用笔记

#### 修订历史

版本号	日期	说明
第2版	2023年5月15日	将安全状态更改为公开
第1版	2021年1月8日	初始版本

铜排设计应用笔记

## 1 介绍

#### 1.1 母线的定义

在电动出行工具的电池组中,铜排是用来连接电池电芯或模块的。在汽车电池组中,铜排是用来连接电池模块的。铜排由铜制成。在原理图中,铜排用很小的电阻来表示。铜排的阻抗通常非常低。



aaa-03992

图1. 母线示例

## 2 铜排位置的确定

#### 2.1 传统设计要求

如果一个电池组的电芯数量少于最大电芯数量,电芯端子引脚CT\_x和电芯平衡引脚CB\_x的使用必须满足一些约束条件。表1列出了堆叠电芯(7到14个电芯)的排列方式。

#### 铜排设计应用笔记

表1. MC33771B/C电池组的电芯数少于最大电芯数时的排列方式

	性 <b>登</b> 电芯							
	14	13	12	11	10	9	8	7
1	CT_REF/CT_1	CT_REF/CT_1	CT_REF/CT_1	CT_REF/CT_1	CT_REF/CT_1	CT_REF/CT_1	CT_REF/CT_1	CT_REF/CT_1
2	CT_1/CT_2	CT_1/CT_2	CT_1/CT_2	CT_1/CT_2	CT_1/CT_2	CT_1/CT_2	CT_1/CT_2	CT_1/CT_2
3	CT_2/CT_3	CT_2/CT_3	CT_2/CT_3	CT_2/CT_3	CT_2/CT_3	CT_2/CT_3	CT_2/CT_3	CT_2/CT_3
4	CT_3/CT_4	CT_3/CT_4	CT_3/CT_4	CT_3/CT_4	CT_3/CT_4	CT_3/CT_4	CT_3/CT_4	CT_3/CT_4
5	CT_4/CT_5	CT_5/CT_6	CT_6/CT_7	CT_7/CT_8	CT_8/CT_9	CT_9/CT_10	CT_10/CT_11	CT_11/CT_12
6	CT_5/CT_6	CT_6/CT_7	CT_7/CT_8	CT_8/CT_9	CT_9/CT_10	CT_10/CT_11	CT_11/CT_12	CT_12/CT_13
7	CT_6/CT_7	CT_7/CT_8	CT_8/CT_9	CT_9/CT_10	CT_10/CT_11	CT_11/CT_12	CT_12/CT_13	CT_13/CT_14
8	CT_7/CT_8	CT_8/CT_9	CT_9/CT_10	CT_10/CT_11	CT_11/CT_12	CT_12/CT_13	CT_13/CT_14	
9	CT_8/CT_9	CT_9/CT_10	CT_10/CT_11	CT_11/CT_12	CT_12/CT_13	CT_13/CT_14		
10	CT_9/CT_10	CT_10/CT_11	CT_11/CT_12	CT_12/CT_13	CT_13/CT_14			
11	CT_10/CT_11	CT_11/CT_12	CT_12/CT_13	CT_13/CT_14				
12	CT_11/CT_12	CT_12/CT_13	CT_13/CT_14					
13	CT_12/CT_13	CT_13/CT_14						
14	CT_13/CT_14							

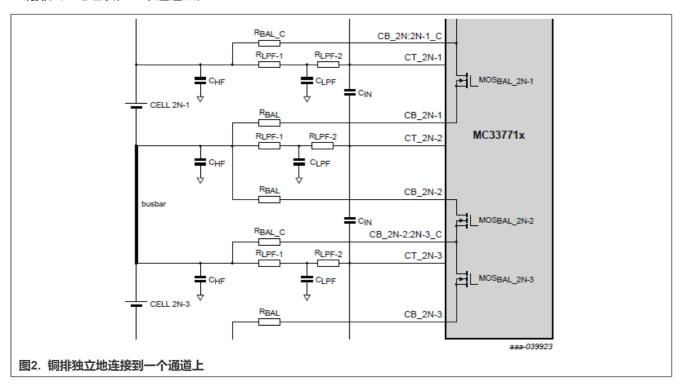
总之, 当系统需要的电芯较少时, 通道5至通道11可能较短。因此, 铜排可将这些通道组装起来。

铜排设计应用笔记

#### 2.2 铜排排列方式

添加铜排有两种可能的方式。

• 铜排独立地组装在一个通道上。



优点:铜排只连接到一个通道上,不会影响相邻电芯的精度。

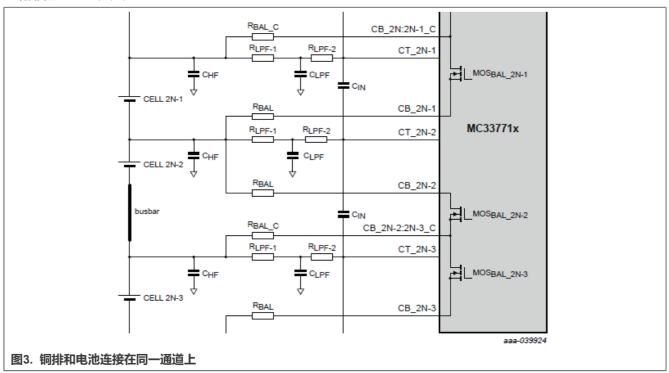
缺点:铜排占用一个通道。

注意: 电池放电时可能会产生负电压。每个通道的负电压限制为-0.3V。如果超过这个电压,相邻通道的测量精度将受到影响。最坏的情况是可能损坏设备。用户必须控制铜排电阻,确保电流条件最差时,压降不高于0.3V。如果无法实现,则不能使用这个选项。

注意:如图2所示,如果母线占用一个通道,则必须禁用CT\_x OV/UV功能验证(SM01),否则会触发误报。

铜排设计应用笔记

#### • 铜排和电芯组装在同一通道上。



优点:铜排不占用任何通道。

缺点: 大电流通过铜排时,铜排上的压降影响通道的精度。

#### 2.3 特殊设计要求

根据系统要求,一些用户可能会在通道5至通道11之外的通道上添加铜排。对于这种应用,应该详细列出添加铜排的条件。铜排位置最重要的限制是每个CT\_x引脚的电压要求。如果电压不能满足要求,CT\_x的精度可能受到影响。表2列出了电压要求。

表2. 每个MC33771x引脚的电压要求

MC33771x引脚	到AGND的最小电压(V)	到VPWR的最小绝对电压(V)
CT_REF	-	-
CT_1	-	-
CT_2	1.9	-
CT_3	1.9	-
CT_4	4.8	-
CT_5	-	-
CT_6	-	-
CT_7	-	-
CT_8	-	-
CT_9	-	-

AN12969 本文件中提供的所有信息均受法律免责声明的约束。 © 2023 NXP B.V. 版权所有。

铜排设计应用笔记

表2. 每个MC33771x引脚的电压要求(续)

MC33771x引脚	到AGND的最小电压(V)	到VPWR的最小绝对电压(V)
CT_10	-	-
CT_11	-	6
CT_12	-	4
CT_13	-	-
CT_14	-	-

用户应根据应用电芯的电压配置铜排,确保在任何情况下都符合<u>表2</u>中的条件。典型的电池工作电压范围如<u>表3</u> 所示。

表3. 电池电压范围

技术	缩略语	额定电压(V)	满电电压(V)	放电结束电压(V)
LiNiMnCoO <sub>2</sub>	NMC	3.6 至 3.7	4.2	2.5 至 3.0
LiNiCoAlO <sub>2</sub>	NCA	3.6	4.2	2.7
LiFePO <sub>4</sub>	LFP	3.2	3.6	2.0
LiCoO <sub>2</sub>	LCO	3.7	4.2	2.4
Li <sub>2</sub> TiO <sub>3</sub>	LTO	2.4	3至3.9	1.5
LiMn <sub>2</sub> O <sub>4</sub>	LMO	3.0	3.5	2.0

## 3 电池组装配过程中的铜排安装

为了避免在安装过程中损坏电芯控制器 (BCC) ,必须首先连接电池的所有铜排,然后再插入MC33771x。

表4. 母线配置示例

MC33771x	3 × 3个电芯	3 × 4个电芯	2×6个电芯	13个电芯
CT_14引脚和CT_13引脚之间的位置	3个模块X3个电芯	3个模块X4个电芯	2个模块X6个电芯	1个模块X13个电芯
CT_13引脚和CT_12引脚之间的位置	3个模块X2个电芯	3个模块X3个电芯	2个模块X5个电芯	1个模块X12个电芯
CT_12引脚和CT_11引脚之间的位置	3个模块X1个电芯	3个模块X2个电芯	2个模块X4个电芯	1个模块X11个电芯
CT_11引脚和CT_10引脚之间的位置	母线	3个模块X1个电芯	2个模块X3个电芯	1个模块X10个电芯
CT_10引脚和CT_9引脚之间的位置	2个模块X3个电芯	母线	2个模块X2个电芯	1个模块X9个电芯
CT_9引脚和CT_8引脚之间的位置	2个模块X2个电芯	2个模块X4个电芯	2个模块X1个电芯	1个模块X8个电芯
CT_8引脚和CT_7引脚之间的位置	2个模块X1个电芯	2个模块X3个电芯	母线	1个模块X7个电芯
CT_7引脚和CT_6引脚之间的位置	0 Ω	2个模块X2个电芯	1个模块X6个电芯	1个模块X6个电芯
CT_6引脚和CT_5引脚之间的位置	0 Ω	2个模块X1个电芯	1个模块X5个电芯	1个模块X5个电芯
CT_5引脚和CT_4引脚之间的位置	0 Ω	母线	0 Ω	0 Ω
CT_4引脚和CT_3引脚之间的位置	母线	1个模块X4个电芯	1个模块X4个电芯	1个模块X4个电芯
CT_3引脚和CT_2引脚之间的位置	1个模块X3个电芯	1个模块X3个电芯	1个模块X3个电芯	1个模块X3个电芯
CT_2引脚和CT_1引脚之间的位置	1个模块X2个电芯	1个模块X2个电芯	1个模块X2个电芯	1个模块X2个电芯
CT_1引脚和CT_REF引脚之间的位置	1个模块X1个电芯	1个模块X1个电芯	1个模块X1个电芯	1个模块X1个电芯

2969 本文件中提供的所有信息均受法律免责声明的约束。

© 2023 NXP B.V. 版权所有。

铜排设计应用笔记

### 4 Legal information

#### 4.1 Definitions

**Draft** — A draft status on a document indicates that the content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included in a draft version of a document and shall have no liability for the consequences of use of such information.

#### 4.2 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Terms and conditions of commercial sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nxp.com.cn/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

Suitability for use in automotive applications — This NXP product has been qualified for use in automotive applications. If this product is used by customer in the development of, or for incorporation into, products or services (a) used in safety critical applications or (b) in which failure could lead to death, personal injury, or severe physical or environmental damage (such products and services hereinafter referred to as "Critical Applications"), then customer makes the ultimate design decisions regarding its products and is solely responsible for compliance with all legal, regulatory, safety, and security related requirements concerning its products, regardless of any information or support that may be provided by NXP. As such, customer assumes all risk related to use of any products in Critical Applications and NXP and its suppliers shall not be liable for any such use by customer. Accordingly, customer will indemnify and hold NXP harmless from any claims, liabilities, damages and associated costs and expenses (including attorneys' fees) that NXP may incur related to customer's incorporation of any product in a Critical Application.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

**Evaluation products** — This product is provided on an "as is" and "with all faults" basis for evaluation purposes only. NXP Semiconductors, its affiliates and their suppliers expressly disclaim all warranties, whether express, implied or statutory, including but not limited to the implied warranties of non-infringement, merchantability and fitness for a particular purpose. The entire risk as to the quality, or arising out of the use or performance, of this product remains with customer.

In no event shall NXP Semiconductors, its affiliates or their suppliers be liable to customer for any special, indirect, consequential, punitive or incidental damages (including without limitation damages for loss of business, business interruption, loss of use, loss of data or information, and the like) arising out the use of or inability to use the product, whether or not based on tort (including negligence), strict liability, breach of contract, breach of warranty or any other theory, even if advised of the possibility of such damages.

Notwithstanding any damages that customer might incur for any reason whatsoever (including without limitation, all damages referenced above and all direct or general damages), the entire liability of NXP Semiconductors, its affiliates and their suppliers and customer's exclusive remedy for all of the foregoing shall be limited to actual damages incurred by customer based on reasonable reliance up to the greater of the amount actually paid by customer for the product or five dollars (US\$5.00). The foregoing limitations, exclusions and disclaimers shall apply to the maximum extent permitted by applicable law, even if any remedy fails of its essential purpose.

**Translations** — A non-English (translated) version of a document, including the legal information in that document, is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

Security — Customer understands that all NXP products may be subject to unidentified vulnerabilities or may support established security standards or specifications with known limitations. Customer is responsible for the design and operation of its applications and products throughout their lifecycles to reduce the effect of these vulnerabilities on customer's applications and products. Customer's responsibility also extends to other open and/or proprietary technologies supported by NXP products for use in customer's applications. NXP accepts no liability for any vulnerability. Customer should regularly check security updates from NXP and follow up appropriately. Customer shall select products with security features that best meet rules, regulations, and standards of the intended application and make the ultimate design decisions regarding its products and is solely responsible for compliance with all legal, regulatory, and security related requirements concerning its products, regardless of any information or support that may be provided by NXP.

NXP has a Product Security Incident Response Team (PSIRT) (reachable at <a href="mailto:PSIRT@nxp.com">PSIRT@nxp.com</a>) that manages the investigation, reporting, and solution release to security vulnerabilities of NXP products.

**NXP B.V.** - NXP B.V. is not an operating company and it does not distribute or sell products.

AN12969 本文件中提供的所有信息均受法律免责声明的约束。 © 2023 NXP B.V. 版权所有。

铜排设计应用笔记

NXP — wordmark and logo are trademarks of NXP B.V.

#### 4.3 Trademarks

Notice: All referenced brands, product names, service names, and trademarks are the property of their respective owners.

#### 铜排设计应用笔记

## 目录

1	介绍	3
1.1		3
2	铜排位置的确定	3
2.1	传统设计要求	3
2.2	铜排排列方式	5
2.3	特殊设计要求	6
3	电池组装配过程中的铜排安装	7
4	法律声明	8

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.