## UM11556

PCA9450 evaluation board user manual
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## 1 Introduction

The PCA9450 is a single chip Power Management IC (PMIC) specifically designed to support the i.MX 8 M family processors in both 1 cell Li-Ion and Li-polymer battery portable application and 5 V adapter non-portable applications.

The device provides six high efficiency step-down regulators, five LDOs, one 400 mA load switch, 2-channel level translator and 32.768 kHz crystal oscillator driver. Three buck regulators support Dynamic Voltage Scaling (DVS) feature along with programmable ramping up and down time, and those buck regulators support remote sense to compensate IR drop to load. This device is characterized across $-40^{\circ} \mathrm{C}$ to $105^{\circ} \mathrm{C}$ ambient temperature range.

Six step-down regulators are designed to provide power for the i.MX 8M application processor and DRAM memory. Two LDOs, LDO1 and LDO2, feature very low quiescent current to provide power for Secure Non-Volatile Storage (SNVS) since these LDOs are always ON when input voltage is valid.

PCA9450 integrates a 2-bit logic translator and dual supply translating transceiver with auto direction sensing. It enables bidirectional voltage level translation and can be used as an $I^{2} C$ level translator. 400 mA load switch is to supply 3.3 V power supply to SD card, which has internal discharge resistor.

PCA9450 has three versions: PCA9450AA is companion PMIC for i.MX 8M Mini, PCA9450B is companion PMIC for i.MX 8M Nano and PCA9450C is companion PMIC for i.MX 8M Plus.

The PCA9450 is offered in 56 -pin HVQFN package, $7 \mathrm{~mm} \times 7 \mathrm{~mm}, 0.4 \mathrm{~mm}$ pitch.

## 2 Key Features

- Six high efficiency step-down regulators
- Three 3 A Buck regulators with DVS feature and remote sense PCA9450AA - Three 3 A buck regulators PCA9450B - Two 3 A buck regulators PCA9450C - 6 A dual-phase buck regulator and 3 A buck regulator
- One 3 A buck regulator
- Two 2 A buck regulators
- Five linear regulators
- Two 10 mA LDOs
- One 150 mA LDO
- One 200 mA LDO
- One 300 mA LDO
- Support various memory types: DDR4/LPDDR4/DDR3L via system UBOOT configuration, no hardware change required
- 400 mA load switch with active discharge
- 32.768 kHz crystal oscillator buffer output
- 2-channel level translator
- Power control IOs
- Power ON/OFF control
- Standby/run mode control
- $\mathrm{Fm}+1 \mathrm{MHz} \mathrm{I}^{2} \mathrm{C}$-bus interface
- ESD protection
- Human Body Model (HBM) : +/- 2000 V
- Charged Device Model (CDM) : +/-500 V
- $7 \mathrm{~mm} \times 7 \mathrm{~mm}, 56$ pin HVQFN with 0.4 mm pitch


## 3 Applications

- loT Devices
- Tablet
- Electronic Point of Sale (ePOS)
- Industrial application


## 4 Evaluation kit components

There are three evaluation boards for the PCA9450 family device: PCA9450AA-EVK, PCA9450B-EVK and PCA9450C-EVK. When evaluating the device, the kits below are required:

- 1x PCA9450 evaluation board, which allows easy evaluation on function and features
- 1x Interface (FTDI C232HM-EDHSL-0) cable, serves as a USB to I ${ }^{2}$ C-bus interface between the computer and the PCA9450 evaluation board
- A Windows based Graphic User Interface (GUI) provides a user friendly interface to program on-chip ${ }^{2}$ C registers to exercise the feature on PCA9450


## 5 PCA9450 GUI software

- Unzip the provided PCA9450 Evaluation Kit GUI file into selected folder. No need to install. If password is asked during unzip, type "NXP"
- Install the FTDI cable driver from website https://www.ftdichip.com/Drivers/D2XX.htm.
- Run the file PCA9450.exe. The interface is shown in Figure 1.


Figure 1. PCA9450 GUI overview

## 6 Test setup and evaluation board connections

### 6.1 Test equipment

- PC/laptop with Windows 7/Windows 10 OS
- Power supply (5 V typical)
- Electronic load
- Oscilloscope/multimeter
- FTDI cable C232HM-DDHSL-0


### 6.2 Test setup

Figure 2 shows test setup block diagram.


Figure 2. Test setup

### 6.3 Evaluation board connection

Figure 3 shows the evaluation board test pin assignment.


Figure 3. PCA9450 board connection
Below is evaluation board jumper/switch settings.
Table 1. Jumper/switch settings

| Jumpers/ <br> Switch | Settings | Description |
| :--- | :--- | :--- |
| S1 | ON (default) | PMIC_ON_REQ =High |

Table 1. Jumper/switch settings...continued

| Jumpers/ <br> Switch | Settings | Description |
| :--- | :--- | :--- |
| S2 | OFF (default) | PMIC_STBY_REQ =Low |
| J6 | 1-2 (default) | VSYS Enablement |
| J7 | No connection (default) | PMIC_RST_B |
| J10 | 1-2 (default) | Pull up of WDOG_B of SDA,SCL, IRQ_B |
| J11 | 1-2 (default) | Pull up of SCLL,SDAL |
| J12 | 1-2 (default) | Pull up of SCLH,SDAH |
| J13 | 1-2 (default) | INL1 Enablement |
| J14 | 1-2 (default) | SD_VSEL = Low |
| J20 | 2-3 (default) | BUCK2 Feedback connection: <br> R_SNSP2 |
| J27 | 1-2 (default) | BUCK1 Feedback connection: <br> R_SNSP1 |
| J29 | 1-2 (default) | BUCK3 Feedback connection: <br> R_SNSP3 |
| J30 | PCA9450AA-EVK: 2-3 (default) <br> PCA9450B-EVK: 2-4 (default) <br> PCA9450C-EVK: 2-5 (default) | SWIN Connect to BUCK4 |
| 1-2 (default) | SW_EN connect to BUCK4 |  |
| 1-2 (default) | BUCK1\&BUCK3 Single/Dual <br> Phase |  |
| J36 | PCA9450AA-EVK: No connection (default) <br> PCA9450B-EVK: No connection (default) <br> PCA9450C-EVK: 1-2 (default) |  |

### 6.4 Connection

Connect wires on the following pins as shown in Figure 3 and make sure the power supply is turned off during the wiring stage:

- VIN and GND - Connect to external power supply.
- SDA/SCL and GND - Connect to FTDI USB to $I^{2} \mathrm{C}$ cable. See Figure 4.
- It can use either external power or buck 5 output for $I^{2} \mathrm{C}$ pull up.
- Each power rail output can be connected to e-load for testing.
- Use the test pin to measure the voltage signal by oscilloscope/multimeter.


Figure 4. $I^{2} \mathrm{C}$ connection

## 7 PCA9450 GUI quick guide

As shown in Figure 5, the GUI is a user friendly tool to access the on-chip registers to perform write/read commands manually or automatically (depending on different setting chosen from the GUI). Below is a quick guide of the key blocks that the GUI provides.


Figure 5. PCA9450 GUI summary

### 7.1 Connection

After plugging in FTDI cable, GUI will detect cable automatically. Select the cable type (FT2TP3HO) from drop-down menu, and then click "Connect" button. Turn on the input power supply; it will show "connected" with green light in the right bottom, "Device connection status".

### 7.2 Register table selection

The registers are categorized for easy use.

- Interrupt/Status - Interrupt, buck/LDO output voltage, fault information, power on source.
- Configuration - Reset behavior, debounce time, UVLO threshold and buck/LDO output status.
- Buck123 - Buck1, 2, 3 configuration.
- Buck456 - Buck4, 5, 6 configuration.
- LDO - LDOs and load switch configuration.


## $\left.7.3\right|^{2} C$ read and write

PCA9450 GUI provides three ways to read and write:

- On register table, click the "read" "write" button for the whole table, or click "回" for write and " $\triangle$ " for read.
- Command: Read or write the Hex value to specific register.
- Script: Run the script to read or write a series of registers. User guide can be found in help menu.


## 8 PCA9450 evaluation steps

Connect the 3.3 V power to $\mathrm{I}^{2} \mathrm{C}$ pull up if using external pull up
2. Connect the jumper as default which is marked with green in schematic.
3. Connect the board to power and PC accordingly.
4. Turn on power supply ( 5 V ) on Vin.
5. Turn on the loading.

## 9 Evaluation board schematic



Figure 6. PCA9450AA-EVK schematic


Figure 7. PCA9450B-EVK schematic


Figure 8. PCA9450C-EVK schematic


Figure 9. PCA9450 evaluation board placement

## 11 BOM

| Qty | ASSY_OPT | Value | Part Reference | DESCRIPTION | MFG_NAME01 | MFG_PN01 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 8 |  | 1uF | C1,C2, C6, C7, C9, C10,C26,C27 | CAP CER 1uF 10V 10\% X7S 0402 | MURATA | GRM155C71A105KE11D |
| 2 |  | 18pF | C3,C4 | CAP CER 18pF 50V 5\% C0G AEC-Q200 0402 | MURATA | GCM1555C1H180JA16 |
| 3 |  | 4.7uF | C5,C22,C24 | CAP CER 4.7uF 10V 10\% X7S 0603 | MURATA | GRM188C71A475KE11D |
| 1 |  | 2.2uF | C8 | CAP CER 2.2uF 16V 10\% X5R 0402 | MURATA | GRM155R61C225KE44 |
| 4 |  | 10uF | C11,C14,C17,C20 | CAP CER 10UF 10V 10\% X5R 0603 | MURATA | GRM188R61A106KE69D |
| 4 |  | 22uF | C12,C15,C18,C21 | CAP CER 22uF 10V 20\% X5R 0603 | TDK | C1608X5R1A226M080AC |
| 3 |  | 47uF | C13,C16, C19 | CAP CER 47UF 6.3V 20\% X5R 0603 | MURATA | GRM188R60J476ME15D |
| 2 |  | 10UF | C23,C25 | CAP CER 10UF 6.3V 10\% X5R 0603 | MURATA | GRM188R60J106KE47D |
| 2 |  | 47uF | C28,C29 | CAP CER 47uF 10V 20\% X5R 0805 | TDK | C2012X5R1A476M125AC |
| 12 | DNP | PMEG4030ER | D1-D12 | $\begin{aligned} & \text { DIODE SCH RECT 3A 40V AEC-Q101 } \\ & \text { SOD123W } \end{aligned}$ | NEXPERIA | PMEG4030ER,115 |
| 1 |  | HDR 2X3 | J3 | HDR 2X3 TH 100MIL CTR 344H AU 118L | WURTH ELEKTRONIK EISOS GMBH \& CO. KG (ELECTRONIC \& ELECTROMEHANICAL COMP) | 61300621121 |
| 2 |  | HDR 2X8 | J4, J5 | HDR 2X8 TH 100MIL CTR 344H AU 118L | WURTH ELEKTRONIK EISOS GMBH \& CO. KG (ELECTRONIC \& ELECTROMEHANICAL COMP) | 61301621121 |
| 9 |  | HDR1X2 | J6, J7, J11-J14, J33, J34, J36 | HDR 1X2 TH 100MIL SP 342H AU 118L | WURTH ELEKTRONIK EISOS GMBH \& CO. KG (ELECTRONIC \& ELECTROMEHANICAL COMP) | 61300211121 |
| 2 |  | SW DIP 1 POS | S1,S2 | SW DIP 1 POS 0.025A@24VDC SMT | WURTH ELEKTRONIK EISOS GMBH \& CO. KG (ELECTRONIC\& ELECTROMEHANICAL COMP) | 416131160801 |


| Qty | ASSY_OPT | Value | Part Reference | DESCRIPTION | MFG_NAME01 | MFG_PN01 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 6 |  | HDR 1X3 | J10,J20,J27,J29 | HDR 1X3 TH 2.54MM SP 344H AU 118L | WURTH ELEKTRONIK EISOS GMBH \& CO. KG (ELECTRONIC \& ELECTROMEHANICAL COMP) | 61300311121 |
| 1 |  | HDR 5 CROSS | J30 | SUBASSEMBLY HDR 1X3 TH 2.54MM SP 344H AU 118L + HDR 1X1 TH -- 344H AU 118L | SUBASSEMBLY | 210-80732,210-81266 |
| 4 |  | 0.47uH | L1-L4 | IND PWR 0.47uH@1MHZ 4.7A 20\% SMT | Shenzhen Sunlord Electronics Co., Ltd | WPN252012ER47MT |
| 2 |  | 0.47uH | L5,L6 | IND WW 0.47uH@1MHz 3.4A 20\% 2016 | MURATA | 1286AS-H-R47M=P2 |
| 5 |  | JUMPER_200MIL | LK1-LK5 | CON 2 JUMPER PLUG SHORTING TH 200MIL SP 300 H -- | KEYSTONE ELECTRONICS | 5026 |
| 6 |  | 100K | R1,R4,R12-R15 | RES MF 100K 1/10W 5\% AEC-Q200 0402 | KOA SPEER | RK73B1ETTP104J |
| 6 |  | 4.7K | R2,R3,R5-R8 | RES MF 4.7K 1/10W 5\% AEC-Q200 0402 | KOA SPEER | RK73B1ETTP472J |
| 3 |  | 0.01 | R9-R11 | RES MF 0.01 OHM 1/10W 1\% AEC-Q200 0603 | Yageo | RL0603FR-070R01L |
| 1 | DNP | 0 | R16 | RES MF ZERO OHM -- AEC-Q200 0603 | KOA SPEER | RK73Z1JTTD |
| 3 |  | 0 | R17-R19 | RES MF ZERO OHM 1/16W 5\% 0402 | ROHM | MCR01MZPJ000 |
| 5 |  | TEST POINT | TP1-TP5 | TEST POINT ORANGE 70X220 MIL TH | KEYSTONE ELECTRONICS | 5008 |
| 1 |  | 5007 | TP6 | TEST POINT WHITE 70X220 MIL TH | KEYSTONE ELECTRONICS | 5007 |
| 6 |  | TESTLOOP_RED | TP7-TP12 | TEST POINT PC MULTI PURPOSE RED TH | KEYSTONE ELECTRONICS | 5010 |
| 1 |  | PCA9450AA/ PCA9450B/ PCA9450C | U1 | IC POWER MANAGEMENT 2.7-5.5V HVQFN56 | NXP SEMICONDUCTORS | PCA9450AA/ PCA9450B/ PCA9450C |
| 1 |  | 32.768 KHZ | Y1 | XTAL 32.768KHZ 12.5PF 20PPM 3.2X1.5MM SMT | Abracon Corporation | ABS07-32.768KHZ-T |

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