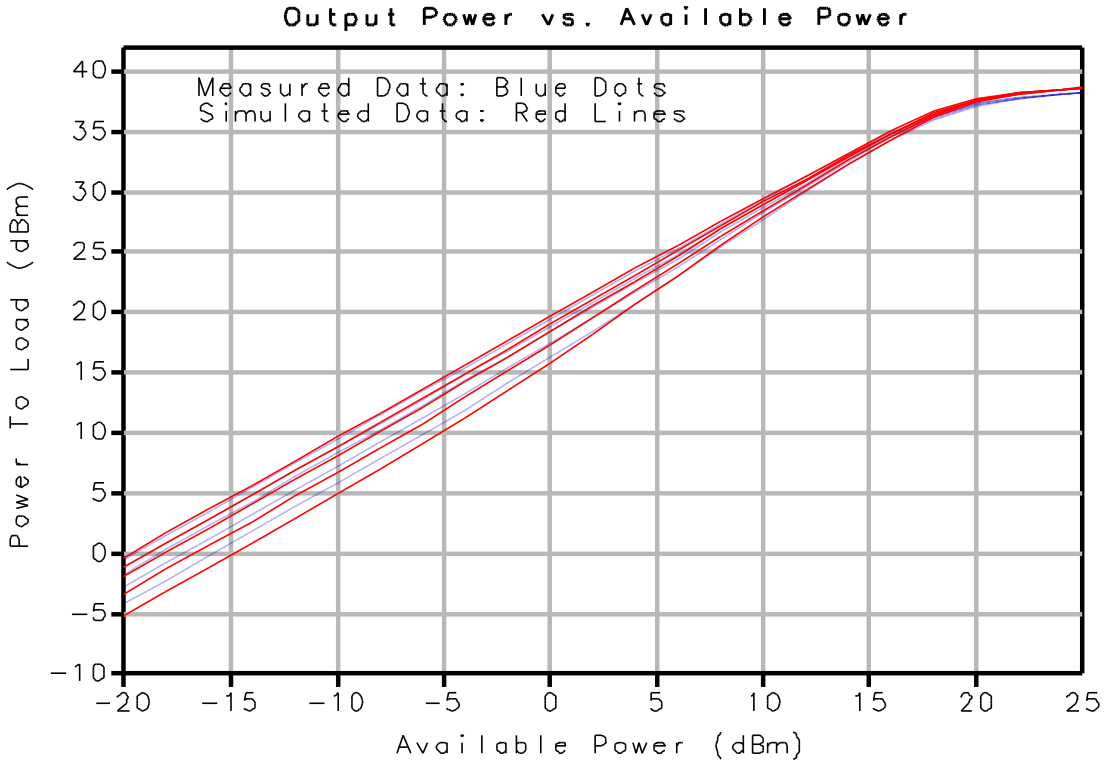
MET LDMOS Model vs. Measured Data

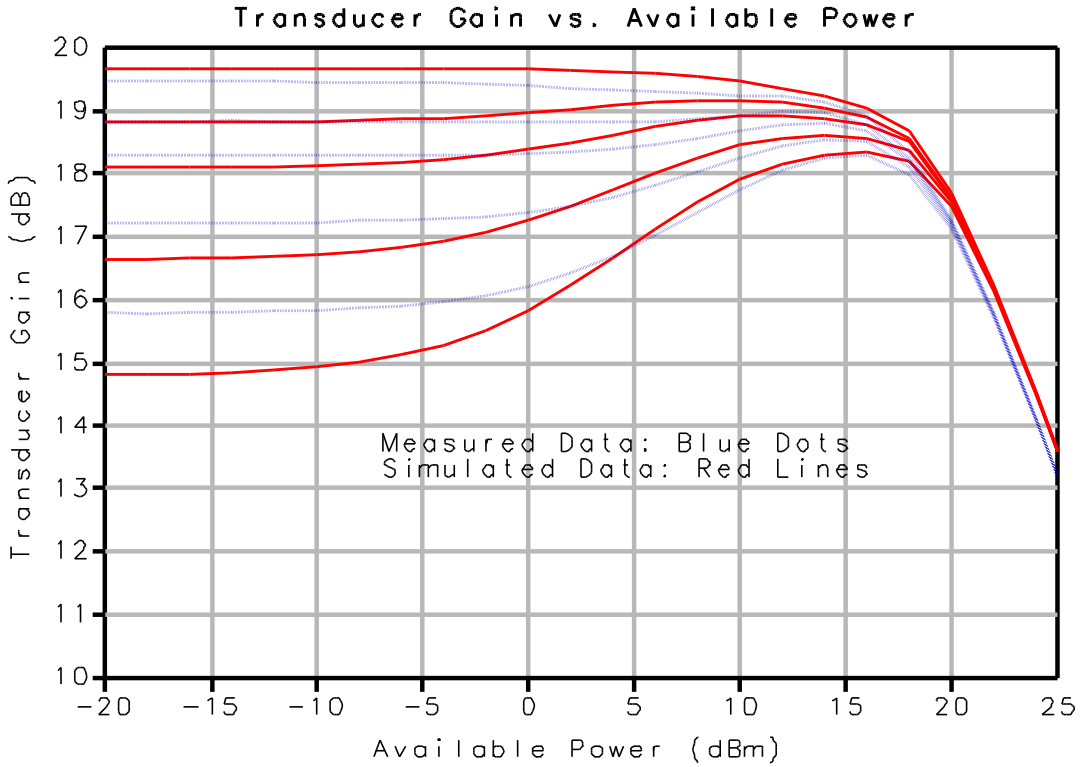
Under single and two tone excitation for 5 different bias conditions of a 10 mm device used in the MRF18090A, MRF18090AS, MRF18090B, MRF18090BS, MRF19090, MRF19090S, MRF18060A, MRF18060AS, MRF18060B, MRF18060BS, MRF19060, MRF19060S and MRF21060 products.

Devices were tuned for best compromise between output power and efficiency.

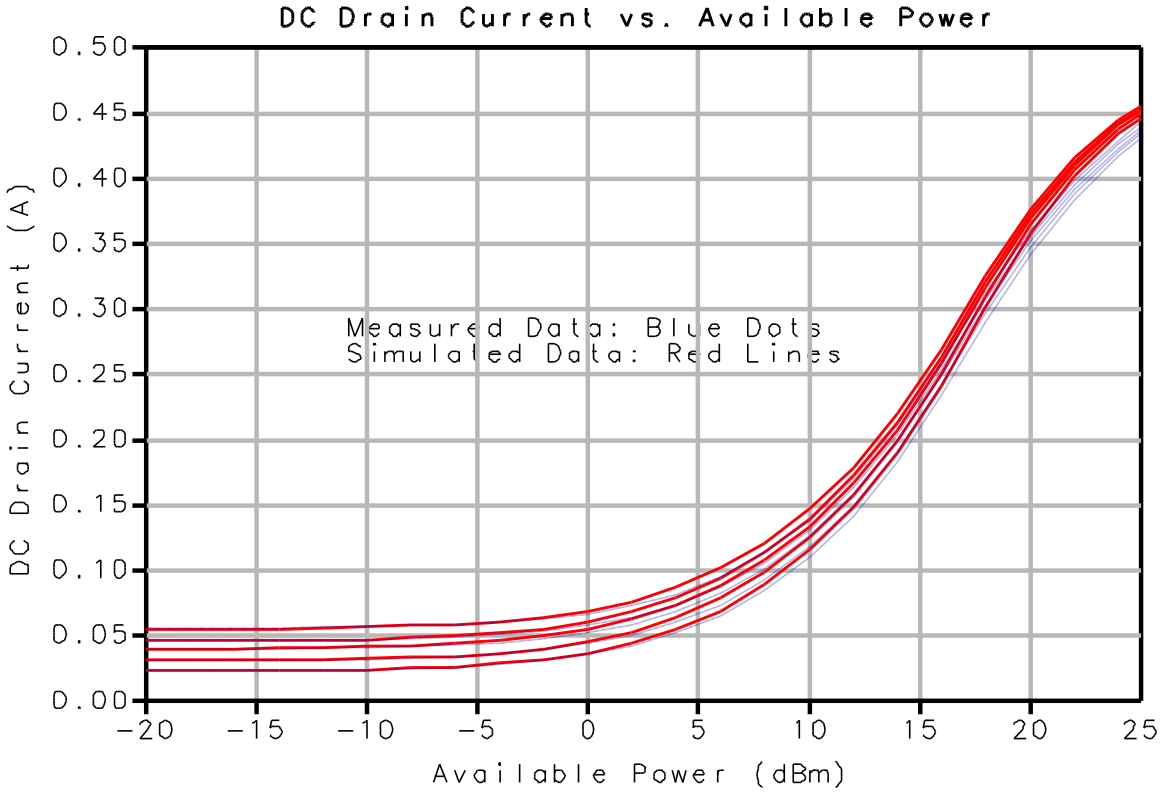
Single Tone (1.96 GHz) Simulated LDMOS MET Model versus Measured Data for 5 Different Bias Conditions of a 10 mm Device Used in the MRF18090A, MRF18090AS, MRF18090B, MRF18090BS, MRF19090, MRF19090S, MRF18060A, MRF18060AS, MRF18060B, MRF18060BS, MRF19060, MRF19060S and MRF21060. Tuned for Best Compromise Between Output Power and Efficiency



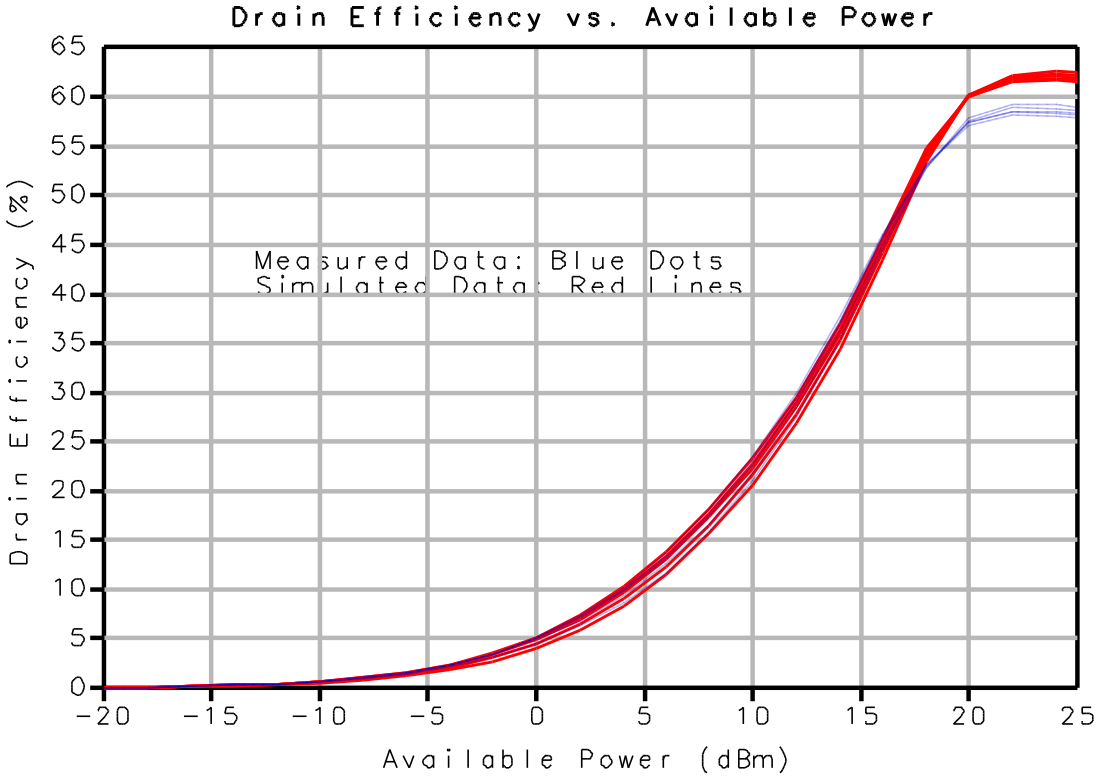
Single Tone (1.96 GHz) Simulated LDMOS MET Model versus Measured Data for 5 Different Bias Conditions of a 10 mm Device Used in the MRF18090A, MRF18090AS, MRF18090B, MRF18090BS, MRF19090, MRF19090S, MRF18060A, MRF18060AS, MRF18060B, MRF18060BS, MRF19060, MRF19060S and MRF21060. Tuned for Best Compromise Between Output Power and Efficiency



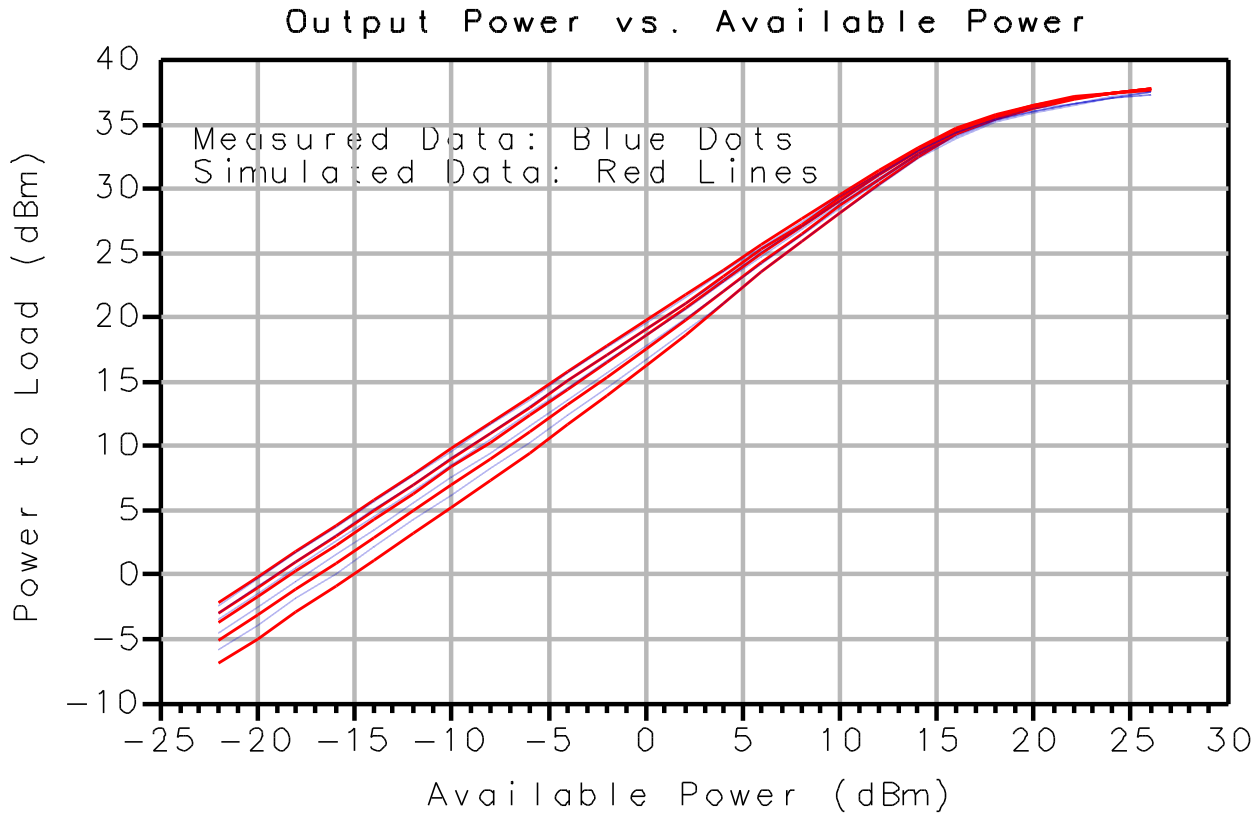
Single Tone (1.96 GHz) Simulated LDMOS MET Model versus Measured Data for 5 Different Bias Conditions of a 10 mm Device Used in the MRF18090A, MRF18090AS, MRF18090B, MRF18090BS, MRF19090, MRF19090S, MRF18060A, MRF18060AS, MRF18060B, MRF18060BS, MRF19060, MRF19060S and MRF21060. Tuned for Best Compromise Between Output Power and Efficiency



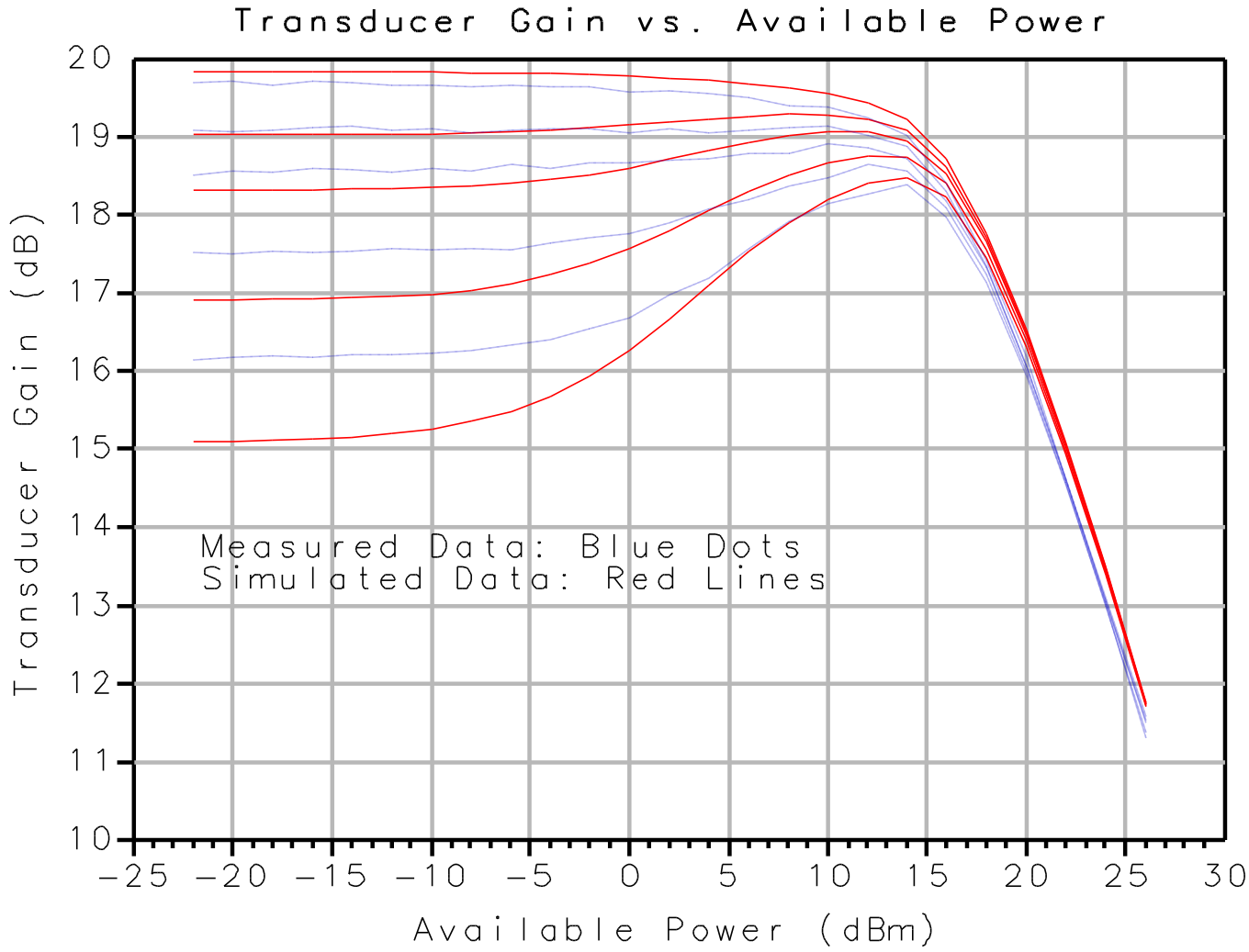
Single Tone (1.96 GHz) Simulated LDMOS MET Model versus Measured Data for 5 Different Bias Conditions of a 10 mm Device Used in the MRF18090A, MRF18090AS, MRF18090B, MRF18090BS, MRF19090, MRF19090S, MRF18060A, MRF18060AS, MRF18060B, MRF18060BS, MRF19060, MRF19060S and MRF21060. Tuned for Best Compromise Between Output Power and Efficiency



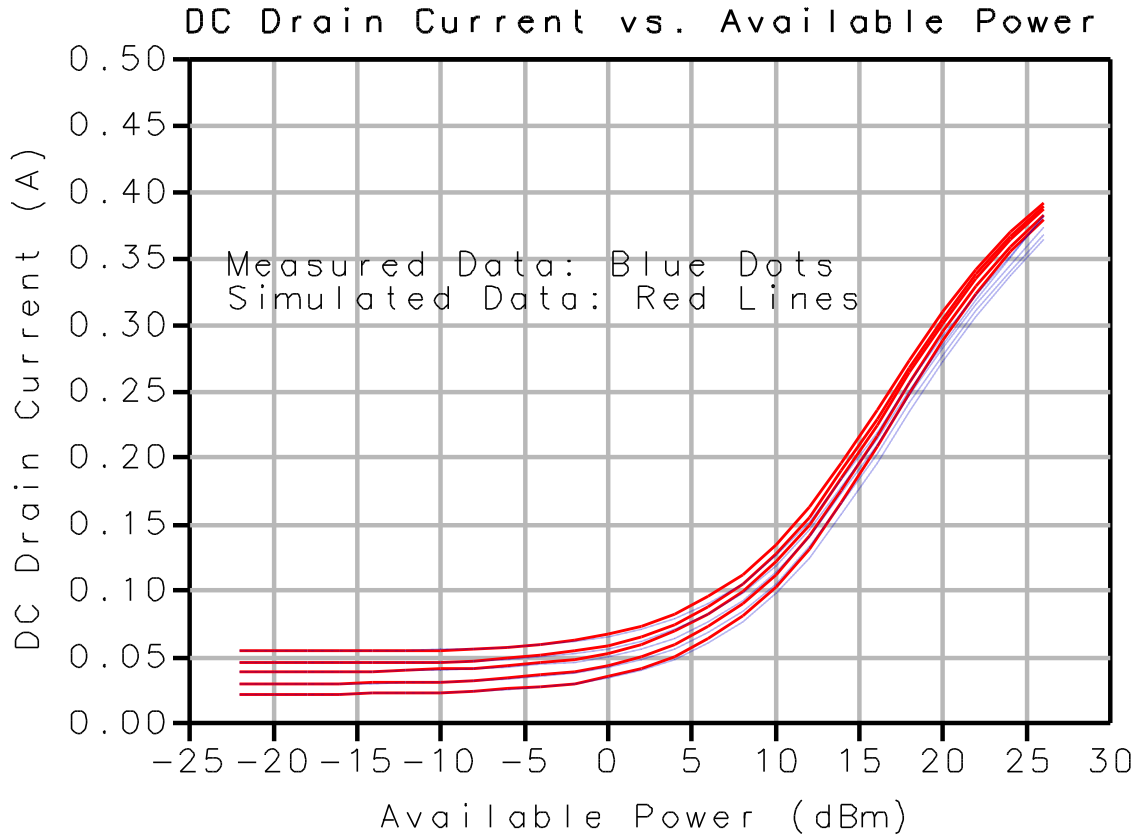
Two Tone (1.96 and 1.97 GHz) Simulated LDMOS MET Model versus Measured Data for 5 Different Bias Conditions of a 10 mm Device Used in the MRF18090A, MRF18090AS, MRF18090B, MRF18090BS, MRF19090, MRF19090S, MRF18060A, MRF18060AS, MRF18060B, MRF18060BS, MRF19060, MRF19060S and MRF21060. Tuned for Best Compromise Between Output Power, Efficiency and Linearity



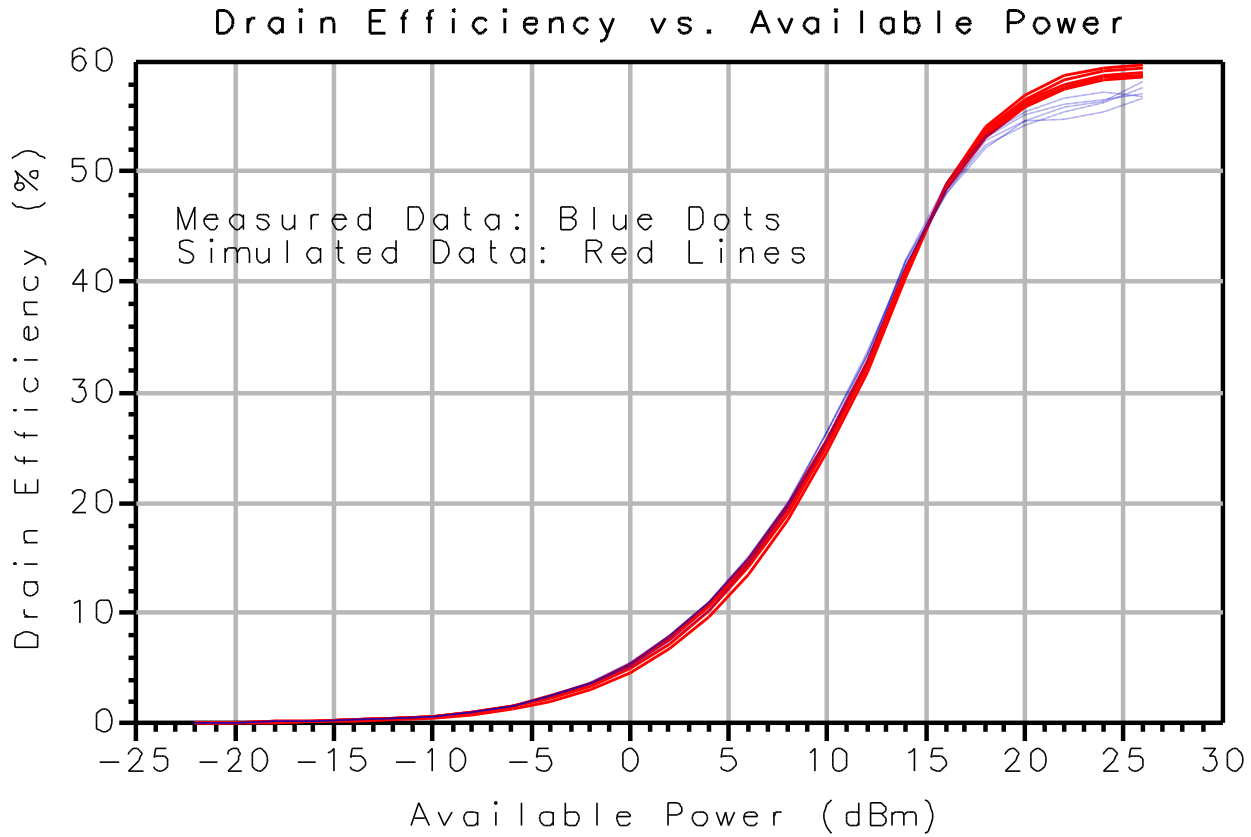
Two Tone (1.96 and 1.97 GHz) Simulated LDMOS MET Model versus Measured Data for 5 Different Bias Conditions of a 10 mm Device Used in the MRF18090A, MRF18090AS, MRF18090B, MRF18090BS, MRF19090, MRF19090S, MRF18060A, MRF18060AS, MRF18060B, MRF18060BS, MRF19060, MRF19060S and MRF21060. Tuned for Best Compromise Between Output Power, Efficiency and Linearity



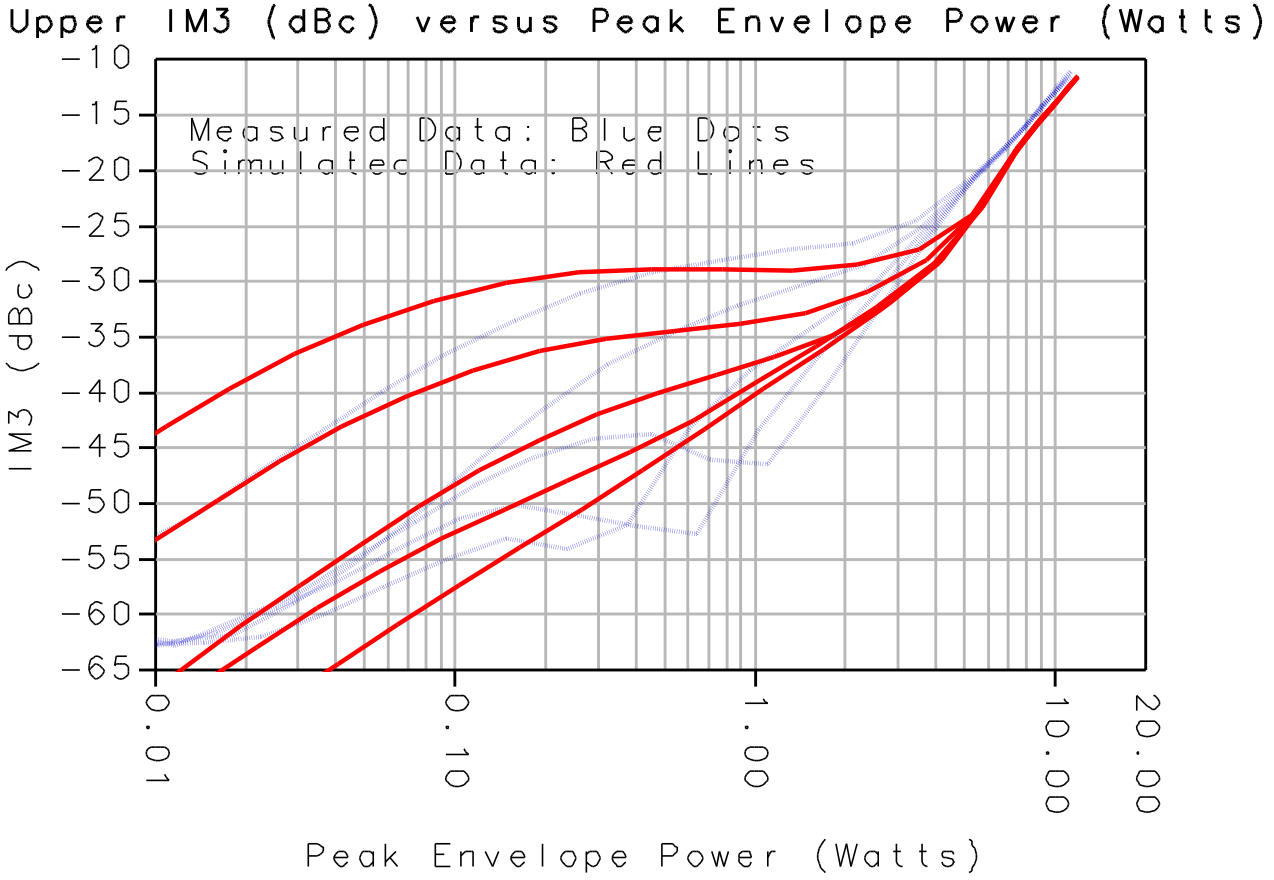
Two Tone (1.96 and 1.97 GHz) Simulated LDMOS MET Model versus Measured Data for 5 Different Bias Conditions of a 10 mm Device Used in the MRF18090A, MRF18090AS, MRF18090B, MRF18090BS, MRF19090, MRF19090S, MRF18060A, MRF18060AS, MRF18060B, MRF18060BS, MRF19060, MRF19060S and MRF21060. Tuned for Best Compromise Between Output Power, Efficiency and Linearity



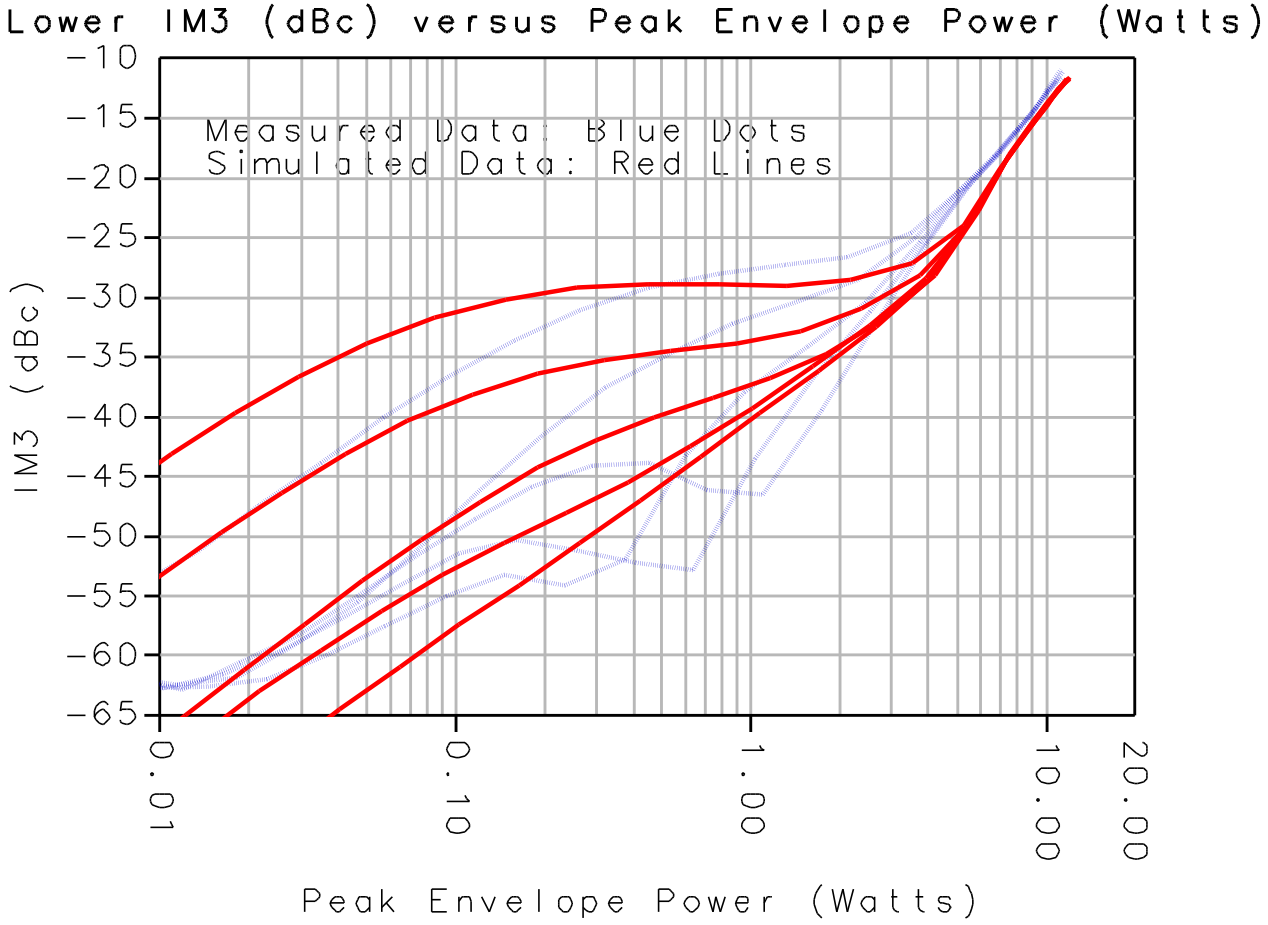
Two Tone (1.96 and 1.97 GHz) Simulated LDMOS MET Model versus Measured Data for 5 Different Bias Conditions of a 10 mm Device Used in the MRF18090A, MRF18090AS, MRF18090B, MRF18090BS, MRF19090, MRF19090S, MRF18060A, MRF18060AS, MRF18060B, MRF18060BS, MRF19060, MRF19060S and MRF21060. Tuned for Best Compromise Between Output Power, Efficiency and Linearity



Two Tone (1.96 and 1.97 GHz) Simulated LDMOS MET Model versus Measured Data for 5 Different Bias Conditions of a 10 mm Device Used in the MRF18090A, MRF18090AS, MRF18090B, MRF18090BS, MRF19090, MRF19090S, MRF18060A, MRF18060AS, MRF18060B, MRF18060BS, MRF19060, MRF19060S and MRF21060. Tuned for Best Compromise Between Output Power, Efficiency and Linearity



Two Tone (1.96 and 1.97 GHz) Simulated LDMOS MET Model versus Measured Data for 5 Different Bias Conditions of a 10 mm Device Used in the MRF18090A, MRF18090AS, MRF18090B, MRF18090BS, MRF19090, MRF19090S, MRF18060A, MRF18060AS, MRF18060B, MRF18060BS, MRF19060, MRF19060S and MRF21060. Tuned for Best Compromise Between Output Power, Efficiency and Linearity

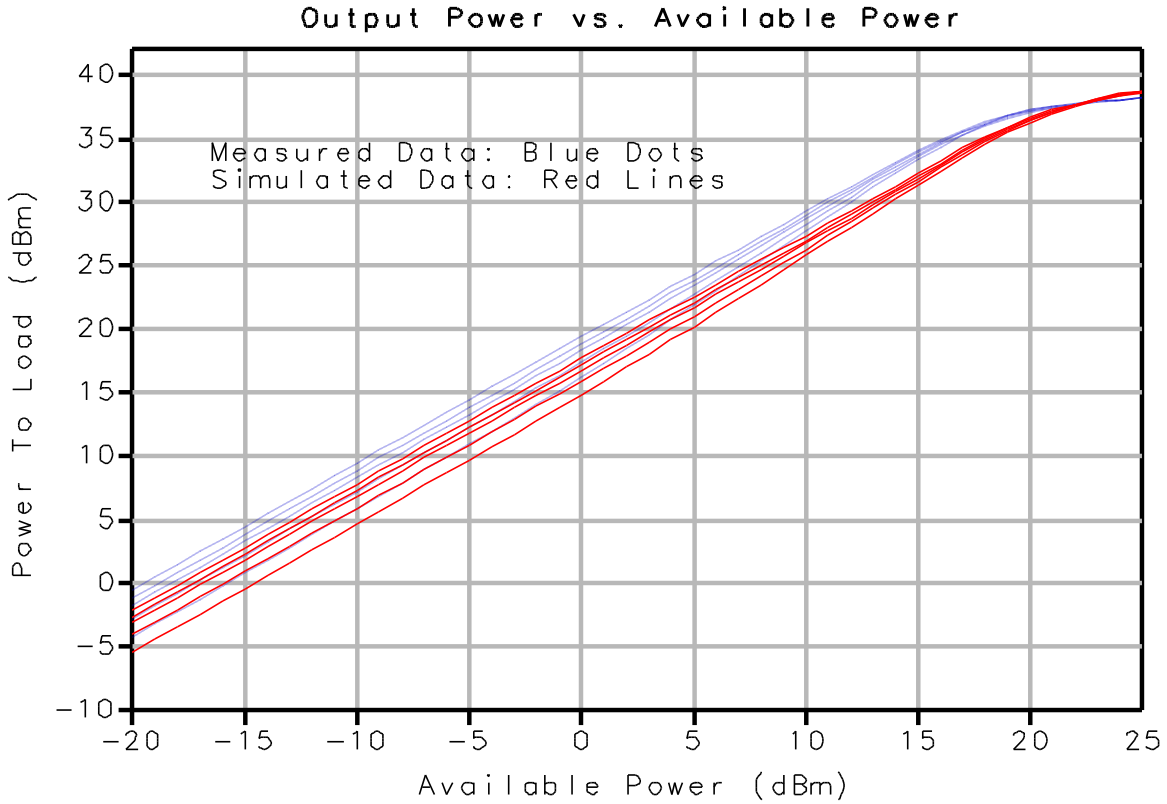


Root LDMOS Model vs. Measured Data

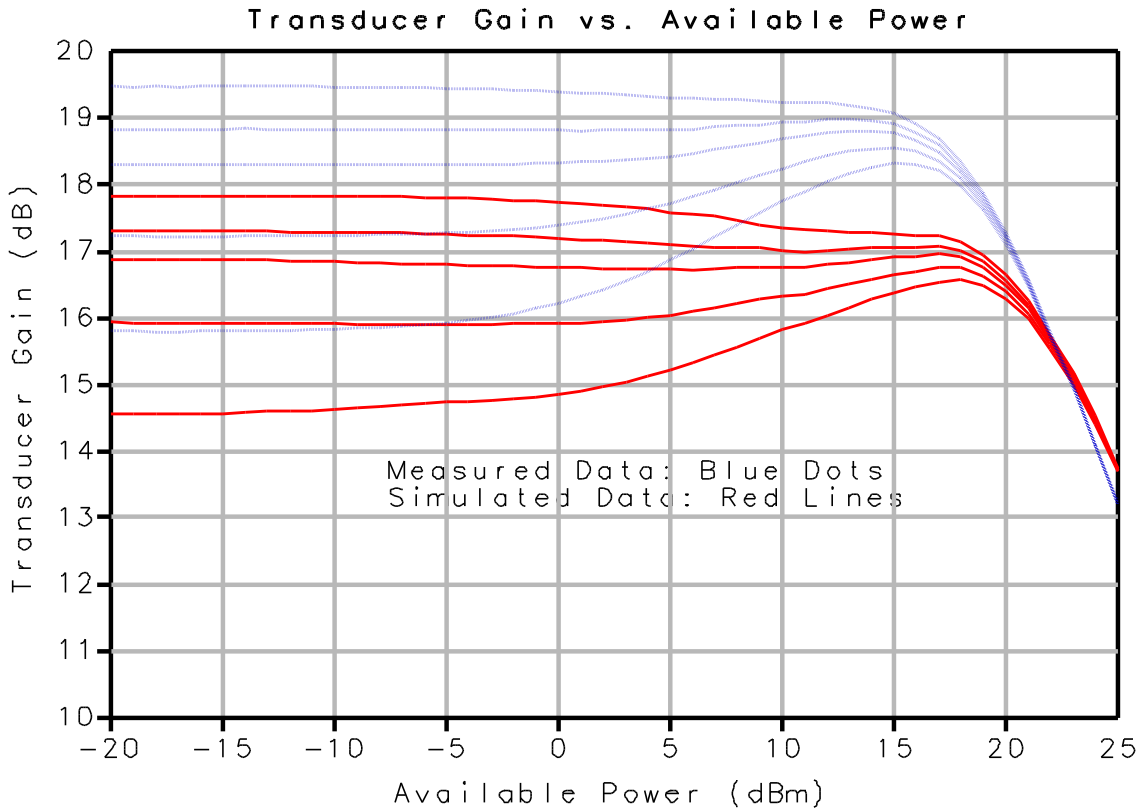
Under single and two tone excitation for 5 different bias conditions of a 10 mm device used in the MRF18090A, MRF18090AS, MRF18090B, MRF18090BS, MRF19090, MRF19090S, MRF18060A, MRF18060AS, MRF18060B, MRF18060BS, MRF19060, MRF19060S and MRF21060 products.

Devices were tuned for best compromise between output power and efficiency.

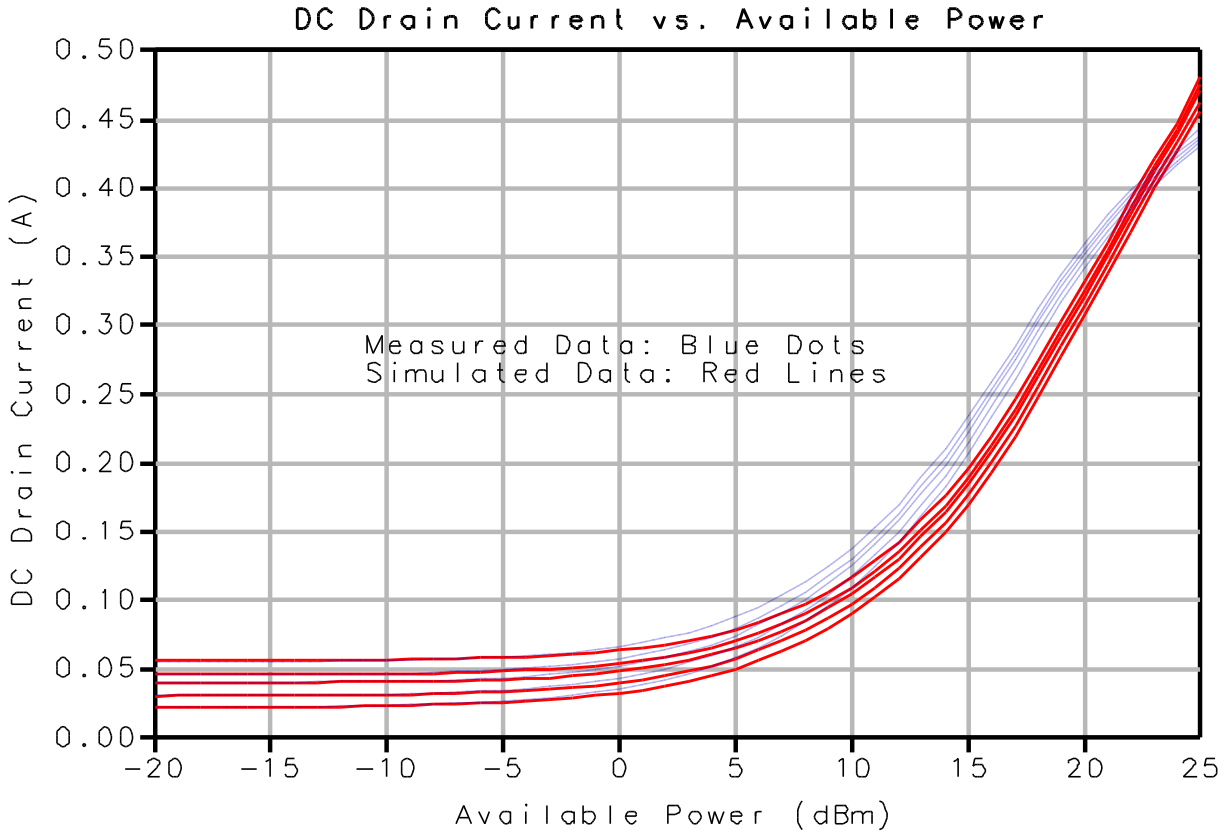
Single Tone (1.96 GHz) Simulated LDMOS Root Model versus Measured Data for 5 Different Bias Conditions of a 10 mm Device Used in the MRF18090A, MRF18090AS, MRF18090B, MRF18090BS, MRF19090, MRF19090S, MRF18060A, MRF18060AS, MRF18060B, MRF18060BS, MRF19060, MRF19060S and MRF21060. Tuned for Best Compromise Between Output Power and Efficiency



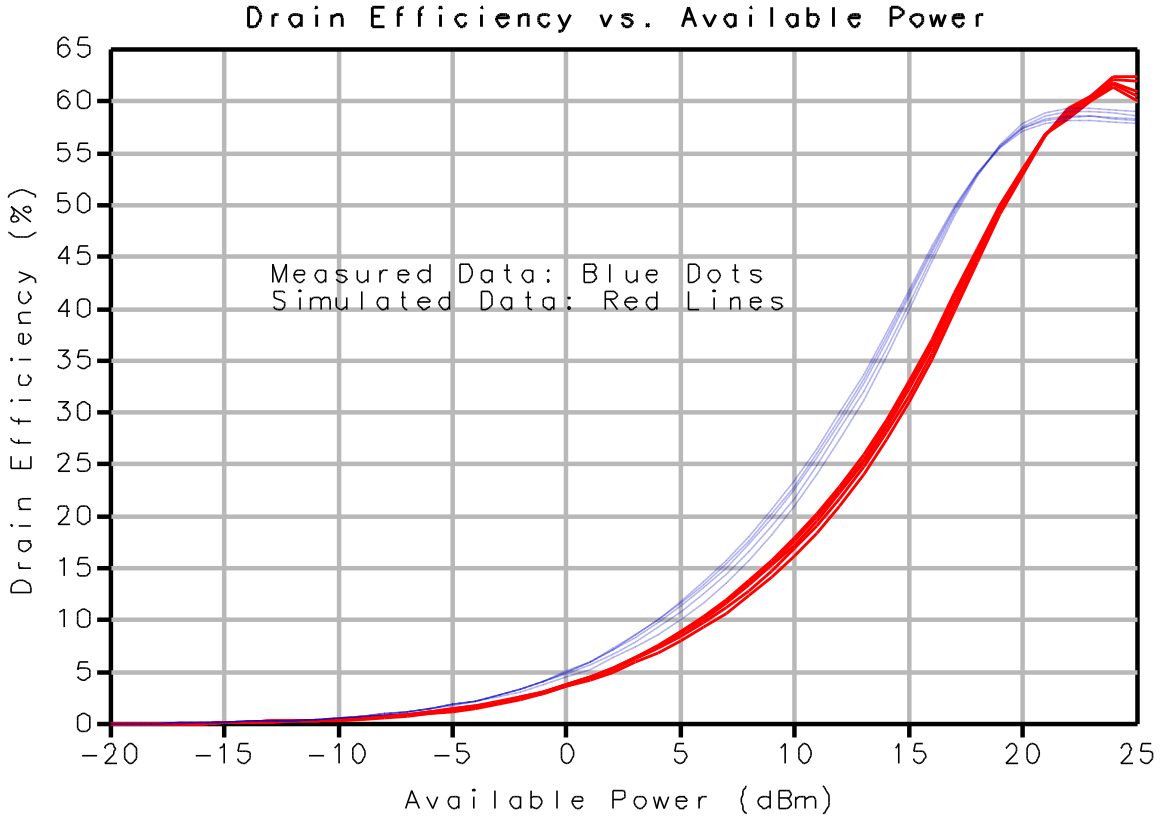
Single Tone (1.96 GHz) Simulated LDMOS Root Model versus Measured Data for 5 Different Bias Conditions of a 10 mm Device Used in the MRF18090A, MRF18090AS, MRF18090B, MRF18090BS, MRF19090, MRF19090S, MRF18060A, MRF18060AS, MRF18060B, MRF18060BS, MRF19060, MRF19060S and MRF21060. Tuned for Best Compromise Between Output Power and Efficiency



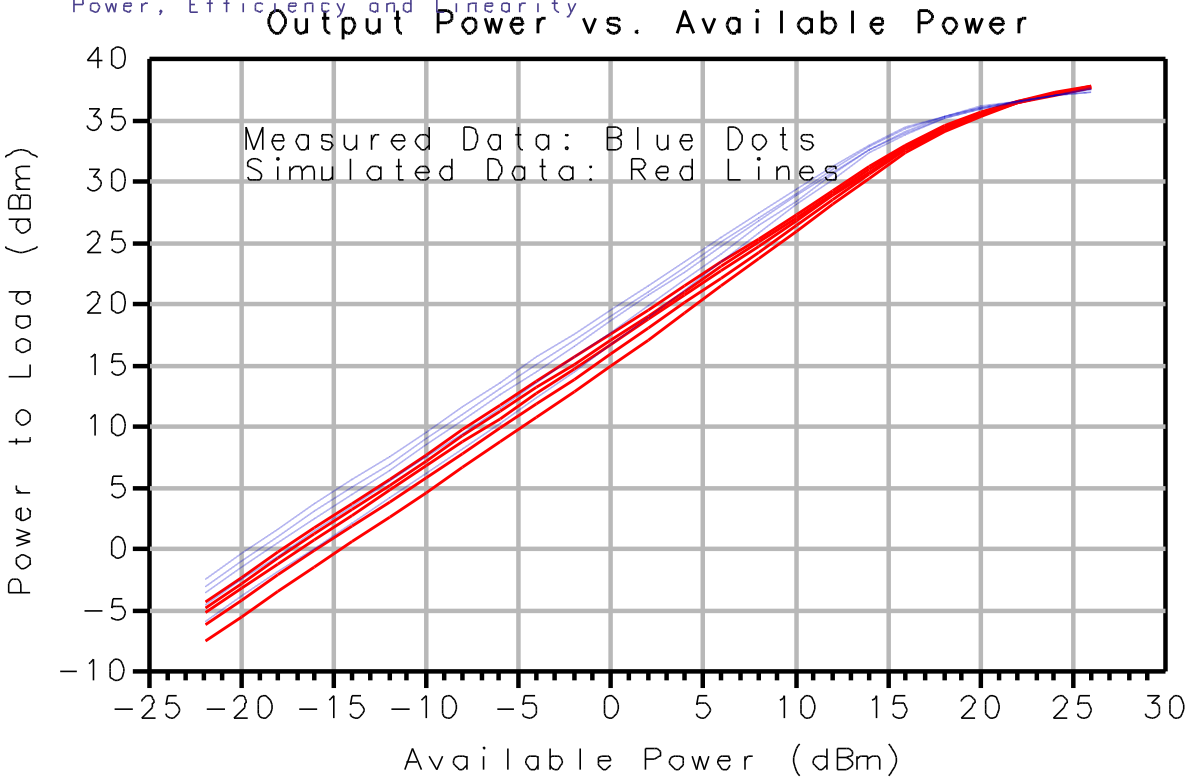
Single Tone (1.96 GHz) Simulated LDMOS Root Model versus Measured Data for 5 Different Bias Conditions of a 10 mm Device Used in the MRF18090A, MRF18090AS, MRF18090B, MRF18090BS, MRF19090, MRF19090S, MRF18060A, MRF18060AS, MRF18060B, MRF18060BS, MRF19060, MRF19060S and MRF21060. Tuned for Best Compromise Between Output Power and Efficiency



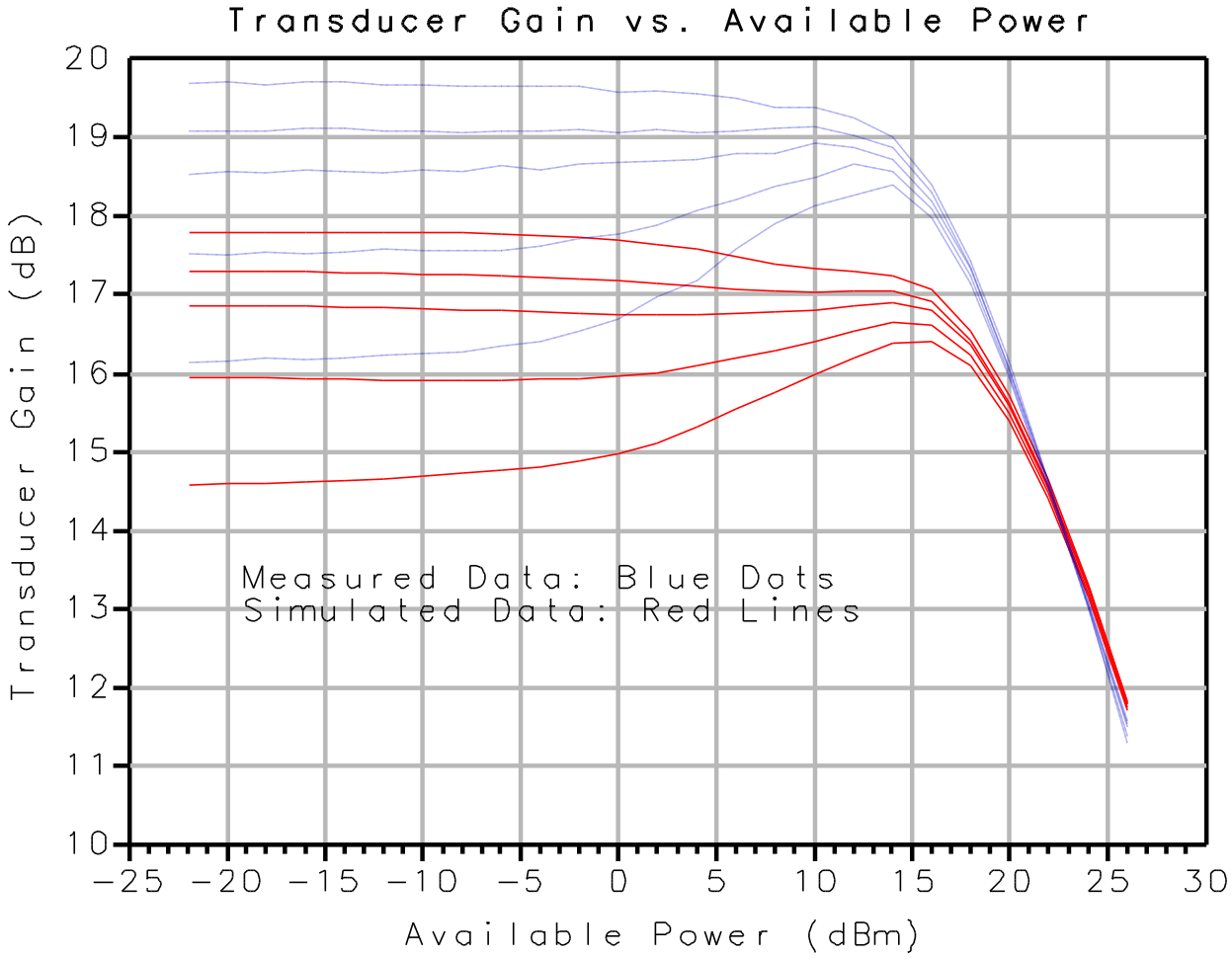
Single Tone (1.96 GHz) Simulated LDMOS Root Model versus Measured Data for 5 Different Bias Conditions of a 10 mm Device Used in the MRF18090A, MRF18090AS, MRF18090B, MRF18090BS, MRF19090, MRF19090S, MRF18060A, MRF18060AS, MRF18060B, MRF18060BS, MRF19060, MRF19060S and MRF21060. Tuned for Best Compromise Between Output Power and Efficiency



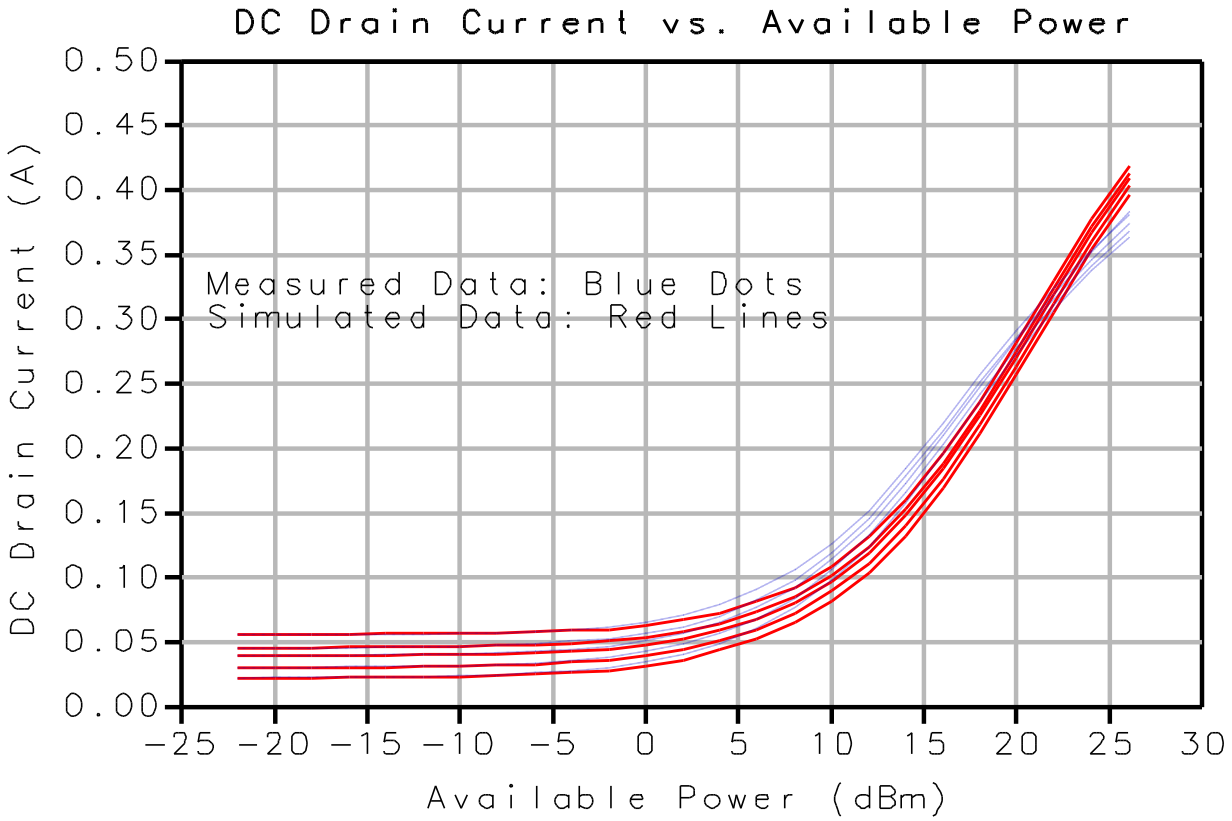
Two Tone (1.96 and 1.97 GHz) Simulated LDMOS Root Model versus Measured Data for 5 Different Bias Conditions of a 10 mm Device Used in the MRF18090A, MRF18090AS, MRF18090B, MRF18090BS, MRF19090, MRF19090S, MRF18060A, MRF18060AS, MRF18060B, MRF18060BS, MRF19060, MRF19060S and MRF21060. Tuned for Best Compromise Between Output Power, Efficiency and Linearity



Two Tone (1.96 and 1.97 GHz) Simulated LDMOS Root Model versus Measured Data for 5 Different Bias Conditions of a 10 mm Device Used in the MRF18090A, MRF18090AS, MRF18090B, MRF18090BS, MRF19090, MRF19090S, MRF18060A, MRF18060AS, MRF18060B, MRF18060BS, MRF19060, MRF19060S and MRF21060. Tuned for Best Compromise Between Output Power, Efficiency and Linearity



Two Tone (1.96 and 1.97 GHz) Simulated LDMOS Root Model versus Measured Data for 5 Different Bias Conditions of a 10 mm Device Used in the MRF18090A, MRF18090AS, MRF18090B, MRF18090BS, MRF19090, MRF19090S, MRF18060A, MRF18060AS, MRF18060B, MRF18060BS, MRF19060, MRF19060S and MRF21060. Tuned for Best Compromise Between Output Power, Efficiency and Linearity



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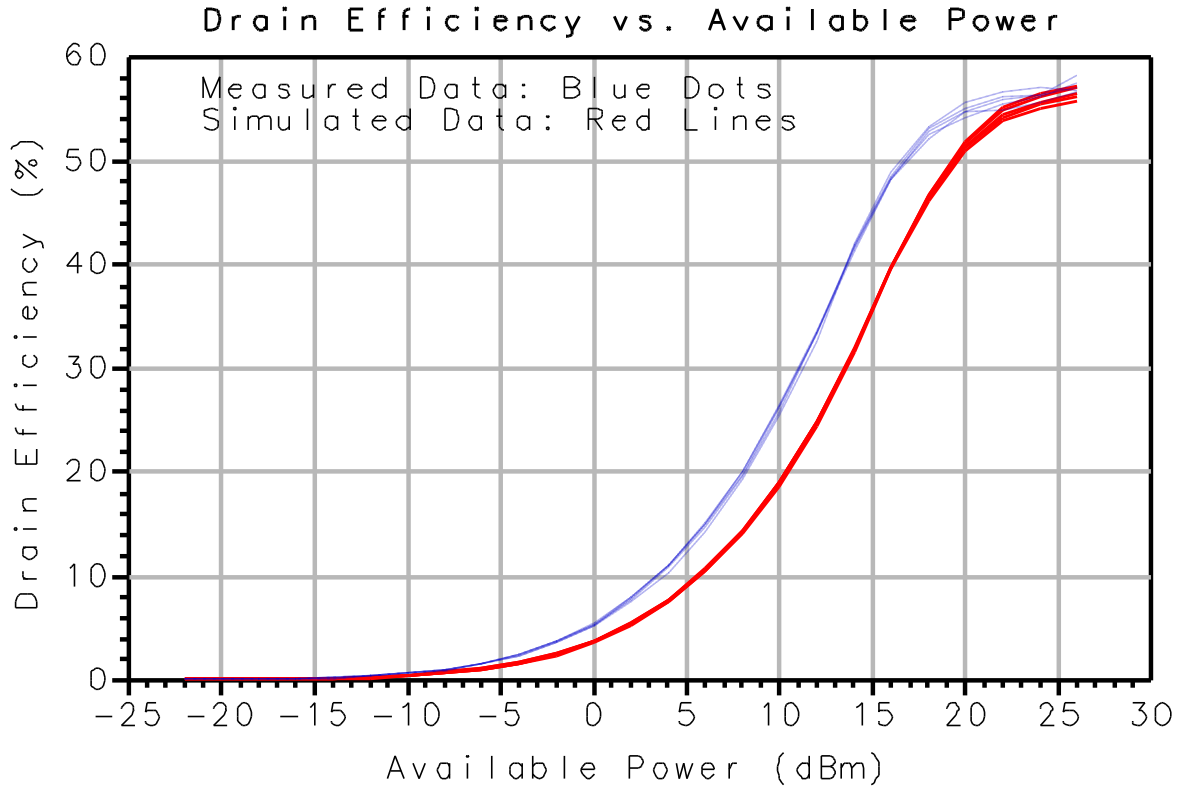

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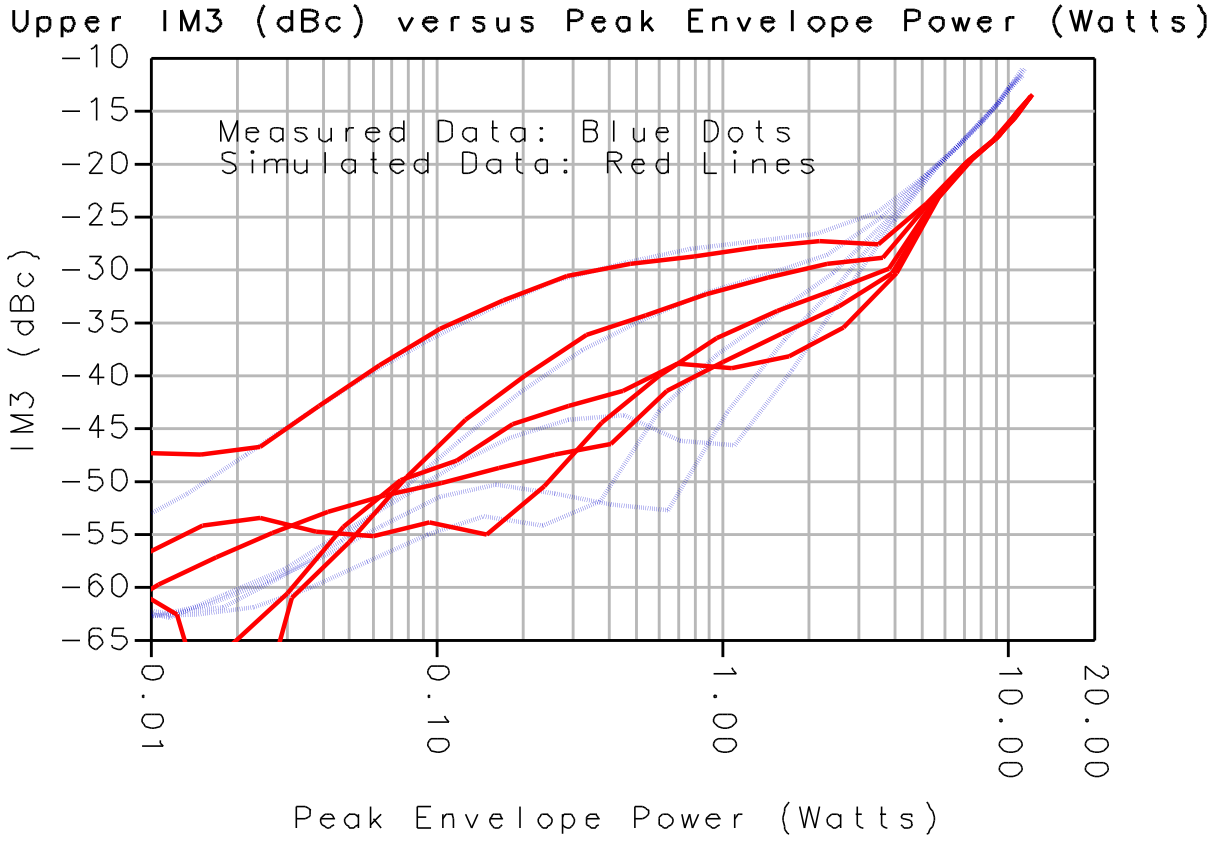

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