

S32 Design Studio for Vision v.1.1

1. Release description

Freescale, a subsidiary of NXP Semiconductors, is pleased to announce the release of the S32 Design Studio for Vision v.1.1 for Automotive and Ultra-Reliable MCUs. The S32 Design Studio is based on the Eclipse open development platform and integrates the Eclipse IDE, GNU Compiler Collection (GCC), GNU Debugger (GDB), and other open-source software to offer designers a straightforward development tool with no code-size limitations.

1.1. Release content

- Eclipse Neon 4.6 Framework
- GNU Tools for ARM® Embedded Processors (Launchpad) build (4.9.3 20150529)
- ARM64: Linaro GCC 4.9-2015.05
- Libraries included: newlib, newlib-nano and ewl2 (ewl and ewlnano)
- NXP APU compiler
- P&E Multilink/Cyclone/OpenSDA (with P&E GDB Server)
- Fully integrated Vision SDK release v.0.9.7. For the details on the feature set of SDK please refer to Reference Manuals
- New Project wizard to create application and library projects for supported devices
- Visual Graph tools to support ISP and APU/APEX2 program development

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- Peripherals Register View
- DDR configuration and validation tools
- Lauterbach¹ debugger support by new project wizard
- Kernel Aware debugging for FreeRTOS, OSEK.
- Devices supported:
 - S32V234

2. What's New

S32 Design Studio for Vision v.1.1 addresses a number of defects and new features:

2.1. New Features

- Moved to Eclipse Neon 4.6 Framework
- APU Compiler update – see details in compiler release notes in S32DS\APUC\doc
- Vision SDK release v.0.9.7 integrated
- Simplified user experience for ISP and APEX2 Visual Graph Tools
- Improved Wizard to import ISP graphs from the Vision SDK
- Supported “Reassign target project” feature for ISP and APEX2 source code emitters
- Restyled S32 Design Studio examples support
- Added Welcome page to enable navigation through the S32 Design Studio ecosystem
- Cheat sheets have been added to guide users through project creation
- RESET functionality added, the hardware reset² is executed for main/boot core and individual core reset for secondary cores.
- Improved DDR configuration and validation tool
- GUI version of DDR stress test tool added
- Updated P&E debug plugins and drivers to the latest and greatest versions

2.2. Bug Fixes

- Project Explorer issue with incorrect display of the resources excluded in some build configuration fixed
- Updated header and register description files to fix issues and synchronize with latest Ref Manual versions
- The Rename Project functionality is improved to cover referenced projects and launch

¹ The plugins to support 3rd party compilers or debuggers are not included and have to be installed from corresponding update site or installation

² As the hardware reset clear RAM, the executable code will be lost for RAM configuration.

configuration

3. System Requirements for Windows Host

3.1. Recommended Configuration

- PC with 2.6 GHz Intel® Pentium® compatible processor or better
- 4 GB of RAM
- 7 GB of disk space (when installing full product or updates)
- 5 GB on Windows system disk
- USB port for communications with target hardware
- Ethernet port for communications with target hardware (optional)

3.2. Operational Minimum Configuration

- PC with 1.8 GHz Intel® Pentium® compatible processor
- 2 GB of RAM
- 7 GB of disk space (when installing full product or updates)
- 5 GB on Windows system disk
- USB port for communications with target hardware

3.3. Host Operating System Support

- Microsoft Windows 7 32-bit and 64-bit (Home Premium Edition and Professional Edition)
- Microsoft Windows 8 32-bit and 64-bit (Home Premium Edition and Professional Edition)
- Microsoft Windows 8.1 32-bit and 64-bit (Home Premium Edition and Professional Edition)
- Microsoft Windows 10 32-bit and 64-bit (Home and Pro Editions)

4. System Requirements for Linux Host

4.1. Recommended Configuration

- PC with 2.6 GHz Intel® Pentium® compatible processor or better
- 4 GB of RAM
- 6.7 GB of disk space (when installing full product or updates)
- 4.8 GB in /tmp directory
- Java 1.8
- USB port for communications with target hardware
- Ethernet port for communications with target hardware (optional)

4.2. Operational Minimum Configuration

- PC with 1.8 GHz Intel® Pentium® compatible processor
- 2 GB of RAM
- 6.7 GB of disk space (when installing full product or updates)
- 4.8 GB in /tmp directory
- Java 1.8
- USB port for communications with target hardware

4.3. Host Operating System Support

- Ubuntu 14.04 64-bit
- Debian 8 64-bit
- CentOS 7 64-bit

5. Product WEB page

S32 Design Studio for Vision v.1.1 product page is <http://www.nxp.com/S32DS>. It contains general information about this product, updates, and download links.

6. Installation and Licensing

To install S32 Design Studio for Vision v.1.1, choose the download option that meets your needs.

The installer package contains the complete S32 Design Studio for Vision v.1.1 suite and an installer. All data needed by the installer will be downloaded and no other download will be performed³. Run the installation package and a wizard will guide you through the installation process.

During installation the license activation will be requested. The following types of activation supported:

- Online activation, requires the internet access and request for license will be sent automatically
- Offline activation, no internet access required, the offline activation request is generated and should be passed to licensing site to get the activation response, which should be loaded back to installer.

New functionality including support for new devices can be added to S32 Design Studio for Vision v.1.1 with service packs, updates and patches. Service packs add specific support for new devices. Updates

³ The plugins to support 3rd party compilers or debuggers are not included and have to be installed from corresponding update site or installation

and patches correct software defects and add general functionality affecting more than one device family.

New support can be added directly from the Internet or from a downloaded archive. If your computer is connected to the Internet, select Install New Software in the Help Menu and all available updates will be displayed. If your computer does not have Internet access, you can download the archive that contains the service pack, update or patch you need from product page and follow the Service Pack Updater procedure posted on the site.

7. Technical Support

S32 Design Studio for Vision specific issues are tracked through the S32DS restricted NXP Community space:

<https://community.nxp.com/groups/s32ds-vision-controlled>

S32 Design Studio general issues are tracked through the S32DS Public NXP Community space:

<https://community.nxp.com/community/s32/s32ds>

For confidential cases and cases which cannot be publicly shared on NXP Community please follow the steps described here:

<https://community.nxp.com/docs/DOC-329745>

Appendix A. Known issues and Workarounds

- Conditional watchpoints and breakpoints:** Conditional breakpoints and watchpoints, including those using ignore counts, do not work always.

Workaround: do not use conditions for breakpoints and watchpoints, instead check for condition in the code and set a normal breakpoint.
- Uninstallation of P&E drivers:** The P&E Device Drivers item will remain in the Control Panel/ Programs and Features after uninstallation of S32 Design Studio, if user will try to use this item to uninstall – error message appeared, the user should ignore it. But the drivers will remain in the system.

Workaround: P&E drivers should be uninstalled before the product uninstallation. But one should be careful if several products are using P&E drivers.
- Disassembly view** content might be destroyed occasionally.

Workaround: Close disassembly view and open it again using menu Window → Show View → Disassembly.
- USB driver conflict** between P&E and Xilinx software: P&E Drivers for Windows utilize the 3rd party service Jungo WinDriver to enable USB support under windows XP/7/8.x/10. Xilinx also uses WinDriver for their driver packages, but older versions. Starting with the P&E Driver package 11.7, a conflict between P&E Drivers and software IDEs that rely on older Jungo driver packages might occur. This is because both the old and new driver packages utilize the same .sys file (windrvr6.sys). When the new P&E Drivers are installed, Xilinx tools stop working after 30 days because the new sys file requires a new license that the older Xilinx software does not have.

Workaround: Please follow the steps in P&E's FAQ 255 for installing an older version of P&E Drivers (11.1) to resolve Xilinx conflict under Windows XP/7/8.x.

http://www.pemicro.com/website_management/faq_view.cfm?id=225

If drivers issue persists, please submit a support request to P&E Microcomputer Systems: <http://www.pemicro.com/support/index.cfm>
- Missing license** could be reported on IDE start.

Workaround: same activation ID should be entered.
- Hyperlinks** might not work correctly under Windows 10 when Microsoft Edge is default browser

Workaround: Set another browser as default.
- Welcome page** might be seen as empty under Linux

Workaround: Webkit1 for GTK2 needs to be installed with the following command
`sudo apt-get install libwebkitgtk-1.0-0`
- When reopening** an imported project created with the previous release, a stack overflow message might be received with a request to exit the workbench.

Workaround: the problem might be caused by the links between projects. At first please ensure that setting for “Open referenced projects when a project is opened” not in “Always” state (in the Windows->Preferences->General->Workspace). When you open project select “No” in “Should referenced projects also be opened where applicable?” dialog. Open project properties for dependent project(s) – could be design or graph project – and remove reference to main host

project (the Project References tab)

NOTE

There are some inherited issues which are introduced by the Eclipse CDT and thus, also present in NXP's S32 Design Studio. When those issues are fixed in a newer version of Eclipse CDT and the S32 Design Studio migrates to that newer version, only then will they be resolved.

Appendix B. Performance Considerations

The following suggestions will help keep the S32 Design Studio tools running at a respectable performance level.

1. To maximize performance, the S32 Design Studio tools should be installed on a computer with the recommended system configuration. While the tools will operate on a computer with the minimum configuration, the limited hardware will restrict its ability to function at desired performance levels.
 2. Close unused projects. Eclipse caches files for all open projects in the workspace. If you need multiple projects open, try to limit the number of projects to no more than 10.
 3. The Eclipse IDE provides several options that provide user assistance tools. These options, however, use memory and cpu bandwidth. If performance is slow and you do not need these options, turn them off.
- Scalability options configure how eclipse deals with large source files.
 - Scalability options:
 - Editor live parsing: impacts parsing while typing, Outline View, semantic highlighting, folding, etc.
 - Semantic highlighting: C/C++ identifiers are colored
 - Syntax coloring: coloring of keywords, comments and literals
 - Parsing–based content assist proposals: content assist proposals which require parsing the file
 - Content assist auto activation: content assist activated automatically on trigger sequences, like '!', '::' or '->'.
 - To disable
 - Click menu 'Window' -> 'Preference'
 - Expand 'C/C++' -> 'Editor' -> 'Scalability'
 - Uncheck 'Enable all scalability mode options'
 - Content Assist Auto Activation can reduce the number of keystrokes a developer must type to create code. The Content Assist plug-in consists of components that predict what a developer will type, based on the current context, scope and prefix.
 - To disable:

- Click menu 'Window' -> 'Preference'
- Expand 'C/C++' -> 'Editor' -> 'Content Assist'
- Uncheck all the options for 'Auto-Activation'



How to Reach Us:

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nxp.com

Web Support:
nxp.com/support

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