

MCF51EM256 Reference Manual Addendum

by: Microcontroller Solutions Group

This errata document describes corrections or updates to the *MCF51EM256 Reference Manual*, file named as MCF51EM256RM. For convenience, the addenda items are grouped by revision. Please check our website at <http://www.freescale.com/coldfire>, for the latest updates.

The current version available of the *MCF51EM256 Reference Manual* is Revision 8.0.

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1 Addendum for Revision 8.0

Table 1. MCF51EM256RM Rev 8.0 Addendum

Location	Description																																				
Figure 3-1, "MCF51EM256 Series Memory Maps" in Chapter 3, "Memory"	<p>Updated the memory map, to reflect the actual EM128 silicon. The updated section of the figure is depicted below:</p> <table border="1" data-bbox="771 499 1200 1346"> <thead> <tr> <th data-bbox="771 510 967 558">Address Range</th> <th data-bbox="974 510 1200 579">MCF51EM128 Memory Usage</th> </tr> </thead> <tbody> <tr> <td data-bbox="808 585 967 611">0x(00)00_0000</td> <td data-bbox="974 585 1200 655">64 KB Flash Memory Array 1</td> </tr> <tr> <td data-bbox="808 617 967 642">0x(00)00_FFFF</td> <td data-bbox="974 661 1200 730">Unimplemented</td> </tr> <tr> <td data-bbox="808 653 967 678">0x(00)01_0000</td> <td data-bbox="974 716 1200 785">64 KB Flash Memory Array 2</td> </tr> <tr> <td data-bbox="808 684 967 709">0x(00)01_FFFF</td> <td data-bbox="974 791 1200 861">Unimplemented</td> </tr> <tr> <td data-bbox="808 716 967 741">0x(00)02_0000</td> <td data-bbox="974 867 1200 936">8 KB RAM memory</td> </tr> <tr> <td data-bbox="808 747 967 772">0x(00)02_FFFF</td> <td data-bbox="974 942 1200 1012">Unimplemented</td> </tr> <tr> <td data-bbox="808 779 967 804">0x(00)7F_FFFF</td> <td data-bbox="974 1018 1200 1087">ColdFire Rapid GPIO</td> </tr> <tr> <td data-bbox="808 810 967 835">0x(00)80_0000</td> <td data-bbox="974 1094 1200 1163">Unimplemented</td> </tr> <tr> <td data-bbox="808 842 967 867">0x(00)80_1FFF</td> <td data-bbox="974 1169 1200 1239">Unimplemented</td> </tr> <tr> <td data-bbox="808 873 967 898">0x(00)80_2000</td> <td data-bbox="974 1245 1200 1314">Slave Peripherals</td> </tr> <tr> <td data-bbox="808 905 967 930">0x(00)BF_FFFF</td> <td data-bbox="974 1320 1200 1390">Unimplemented</td> </tr> <tr> <td data-bbox="808 936 967 961">0x(00)C0_0000</td> <td data-bbox="974 1396 1200 1465">Unimplemented</td> </tr> <tr> <td data-bbox="808 968 967 993">0x(00)C0_000F</td> <td data-bbox="974 1472 1200 1541">Unimplemented</td> </tr> <tr> <td data-bbox="808 999 967 1024">0x(00)C0_0010</td> <td data-bbox="974 1547 1200 1617">Unimplemented</td> </tr> <tr> <td data-bbox="808 1031 967 1056">0x(FF)FF_7FFF</td> <td data-bbox="974 1623 1200 1692">Unimplemented</td> </tr> <tr> <td data-bbox="808 1062 967 1087">0x(FF)FF_8000</td> <td data-bbox="974 1698 1200 1768">Unimplemented</td> </tr> <tr> <td data-bbox="808 1094 967 1119">0x(FF)FF_FFFF</td> <td data-bbox="974 1774 1200 1843">Unimplemented</td> </tr> </tbody> </table>	Address Range	MCF51EM128 Memory Usage	0x(00)00_0000	64 KB Flash Memory Array 1	0x(00)00_FFFF	Unimplemented	0x(00)01_0000	64 KB Flash Memory Array 2	0x(00)01_FFFF	Unimplemented	0x(00)02_0000	8 KB RAM memory	0x(00)02_FFFF	Unimplemented	0x(00)7F_FFFF	ColdFire Rapid GPIO	0x(00)80_0000	Unimplemented	0x(00)80_1FFF	Unimplemented	0x(00)80_2000	Slave Peripherals	0x(00)BF_FFFF	Unimplemented	0x(00)C0_0000	Unimplemented	0x(00)C0_000F	Unimplemented	0x(00)C0_0010	Unimplemented	0x(FF)FF_7FFF	Unimplemented	0x(FF)FF_8000	Unimplemented	0x(FF)FF_FFFF	Unimplemented
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0x(00)C0_0010	Unimplemented																																				
0x(FF)FF_7FFF	Unimplemented																																				
0x(FF)FF_8000	Unimplemented																																				
0x(FF)FF_FFFF	Unimplemented																																				

2 Revision History

Table 2 provides a revision history for this document.

Table 2. Revision History Table

Rev. Number	Substantive Changes	Date of Release
1.0	Initial release. Updated chapter 3, "Memory."	01/2012

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