S32K118 EVB

QUICK START GUIDE

APPLIES FOR: S32K118 EVB (SCH_29945 REV B)





SECURE CONNECTIONS FOR A SMARTER WORLD

EXTERNAL USE

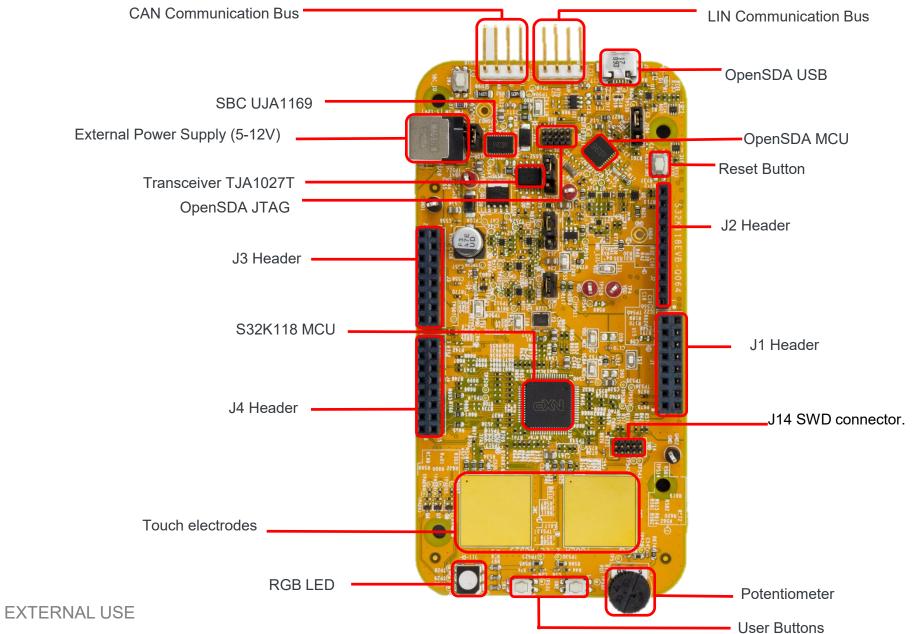
Contents:

- Get to Know S32K118 EVB
- Out of the Box Setup
- Introduction to OpenSDA
- Creating a new S32DS project for S32K118:
 - Download
 - Create a project
 - Create a project from SDK example
- S32DS Debug basics
- Create a P&E debug configuration



Get to know S32K118-EVB

2





S32K118 EVB Features:

- Supports 64LQFP and 48LQFP packages
- Small form factor size 4.5" x 2.3"
- Arduino[™] UNO footprint-compatible with expansion "shield" support
- Integrated open-standard serial and debug adapter (OpenSDA) with support for several industry-standard debug interfaces
- Easy access to the MCU I/O header pins for prototyping
- On-chip connectivity for CAN, LIN, UART/SCI.
- SBC UJA1169 and LIN phy TJA1027
- Potentiometer for precise voltage and analog measurement
- RGB LED
- Two push-button switches (SW2 and SW3) and two touch electrodes
- Flexible power supply options
 - microUSB
 - external 12V power supply



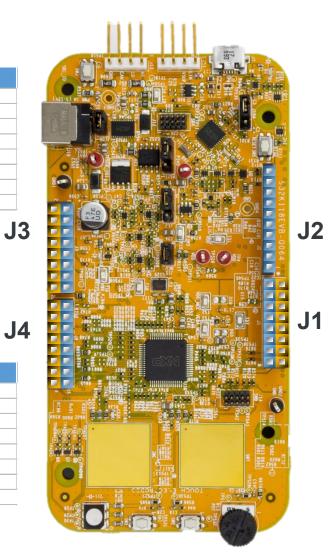


Header/Pinout Mapping for S32K118

PIN	PORT	FUNCTION	J3	PIN	PORT	FUNCTION
J3-02	PTB0	GPIO		J3-01	-	VBAT
J3-04	PTB1	GPIO		J3-03	-	VDD_PERH
J3-06	PTB6	GPIO		J3-05	PTA5	RESET
J3-08	PTB7	GPIO		J3-07	-	3.3V
J3-10	PTE4	GPIO		J3-09	-	5V
J3-12	PTE5	GPIO		J3-11	-	GND
J3-14	PTA11	GPIO		J3-13	-	GND
J3-16	PTB12	GPIO		J3-15	-	VBAT

J3

PIN	PORT	FUNCTION	J4	PIN	PORT	FUNCTION
J4-02	PTC6	GPIO		J4-01	PTA6	ADC0_SE2
J4-04	PTC7	GPIO		J4-03	PTC0	ADC0_SE8
J4-06	PTC8	GPIO		J4-05	PTC1	ADC0_SE9
J4-08	PTC9	GPIO		J4-07	PTC14	ADC0_SE12
J4-10	PTD4	GPIO		J4-09	PTC15	ADC0_SE13
J4-12	PTD15	GPIO		J4-11	PTC16	ADC0_SE14
J4-14	PTD16	GPIO		J4-13	PTC17	ADC0_SE15
J4-16	PTE8	GPIO		J4-15	PTB13	GPIO



J2	PIN	PORT	FUNCTION
	J2-01	PTC2	FTM0_CH2
	J2-02	PTC3	FTM0_CH3
	J2-03	PTB5	LPSPI0_PCS
	J2-04	PTB4	LPSPI0_SOUT
	J2-05	PTB3	LPSPI0_SIN
	J2-06	PTB2	LPSPI0_SCK
	J2-07	-	GND
	J2-08	-	AREF
	J2-09	PTA1	LPI2C0_SDA
	J2-10	PTA0	LPI2C0_SCL

PIN	PORT	FUNCTION	J1	PIN	PORT	FUNCTION
J1-02	PTE10	GPIO		J1-01	PTA2	LPUART0_RX
J1-04	PTE11	GPIO		J1-03	PTA3	LPUART0_TX
J1-06	PTE0	GPIO		J1-05	PTA13	FTM1_CH7
J1-08	PTE1	GPIO		J1-07	PTA12	FTM1_CH6
J1-10	PTE6	GPIO		J1-09	PTD0	FTM0_CH2
J1-12	PTE7	GPIO		J1-11	PTD1	FTM0_CH3
J1-14	PTD3	GPIO		J1-13	PTD2	FXIO_D4
J1-16	PTD5	GPIO		J1-15	PTE9	FTM0_CH7



EXTERNAL USE 4

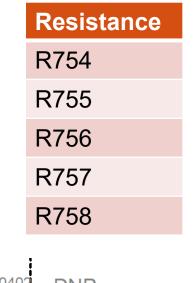
Arduino compatible pins NXP pins

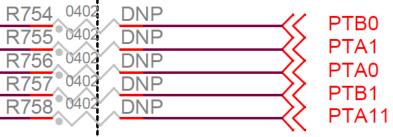
*0ohm resistor is not connected

Jumper Settings

Jumper	Configuration	Description
J104	1-2	Reset signal to OpenSDA, use to enter into OpenSDA Bootloader mode
	2-3 (Default)	Reset signal direct to the MCU, use to reset S32K118.
J107	1-2 (Default)	S32K118 powered by 12V power source.
	2-3	S32K118 powered by USB micro connector.
J10	2-3 (Default)	MCU voltage 5v
	1-2	MCU voltage 3.3v
J108	1-2 (Default)	Select LIN master option
J15	1-2 (Default)	Used for current measurement

Note: The resistances below are populated in RevB







5 EXTERNAL USE

HMI mapping

Component	S32K118
Red LED	PTD16 (FTM0 CH1)
Blue LED	PTE8 (FTM0 CH6)
Green LED	PTD15 (FTM0 CH0)
Potentiometer	PTA7 (ADC0_SE3)
SW2	PTD3
SW3	PTD5
OpenSDA UART TX	PTB1(LPUART0_TX)
OpenSDA UART RX	PTB0(LPUART0_RX)
CAN TX	PTE5(CAN0_TX)
CAN RX	PTE4 (CAN0_RX)
LIN TX	PTC7(LPUART1_TX)
LIN RX	PTC6 (LPUART1_RX)
SBC_SCK	PTB2 (LPSPI0_SCK)
SBC_MISO	PTB3(LPSPI0_SIN)
SBC_MOSI	PTB4(LPSPI0_SOUT)
SBC_CS	PTB5(LPSPI0_PCS1)



6 EXTERNAL USE

S32K118 EVB OUT OF THE BOX



Step 1: Power up the Board – EVB Power Supplies

- The S32K118-EVB evaluation board powers from a USB or external 12V power supply. By default 12V power is enabled with J107 (check slide 5)
- Connect the USB cable to a PC using supplied USB cable .
- Connect other end of USB cable (microUSB) to mini-B port on S32K118-EVB at J7
- Allow the PC to automatically configure the USB drivers if needed
- Debug is done using OpenSDA through J7





Step 2: Power up the Board – Is it powered on correctly?

- When powered through USB, LEDs D2 and D3 should light green
- Once the board is recognized, it should appear as a mass storage device in your PC with the name S32K118EVB.





Step 3: Power up the Board – Is it powered on correctly?

 Board is preloaded with a software, in which the red, blue and green leds will toggle at different rates.



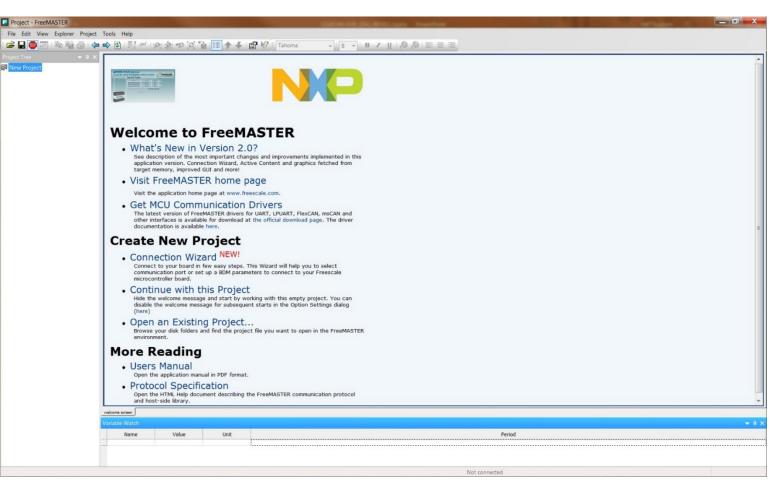


S32K118 JUMPSTART EXPERIENCE **BASED ON THE** FREEMASTER TOOL



Install the FreeMASTER tool

- Download and install the FreeMASTER PC application <u>www.nxp.com/FreeMASTER</u>.
- Open the FreeMASTER application on your PC. You should see Welcome page:





Power up the EVB board

- Powers the S32K118EVB evaluation board from a USB. By default, the USB power is enabled by J07 jumper.
- Connect the USB cable to a PC and connect micro USB connector of the USB cable to micro-B port J7 on the S32K118EVB.
- Allow the PC to automatically configure the USB drivers if needed.
- When EVB is powered from USB, LEDs D2 and D3 should light green.
- The EVB board is preloaded with a software toggling the RGB LED colors periodically between RED-GREEN-BLUE.







Setup serial connection in the FreeMASTER tool

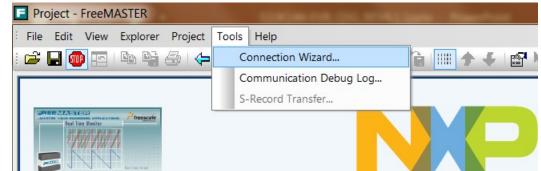
Setup communication port to "OpenSDA" and speed to 115200 b/s:

 Setup communication manualy: Go to: "Project > Options > Comm"

om MAP Files	Pack	Dir HTML Pages Demo Mode Views & Bars
© RS232:	Port [opensda OpenSDA - CDC Serial Port (http://www.per
S	Speed:	115200 Timeouts
C Plug-in Mo	dule:	_
Connect str	ing:	drv=4;ptype=3;pnum=1;devid=PE5011560;devl Configure
Save settin	igs to pro	roject file Save settings to registry, use it as default.

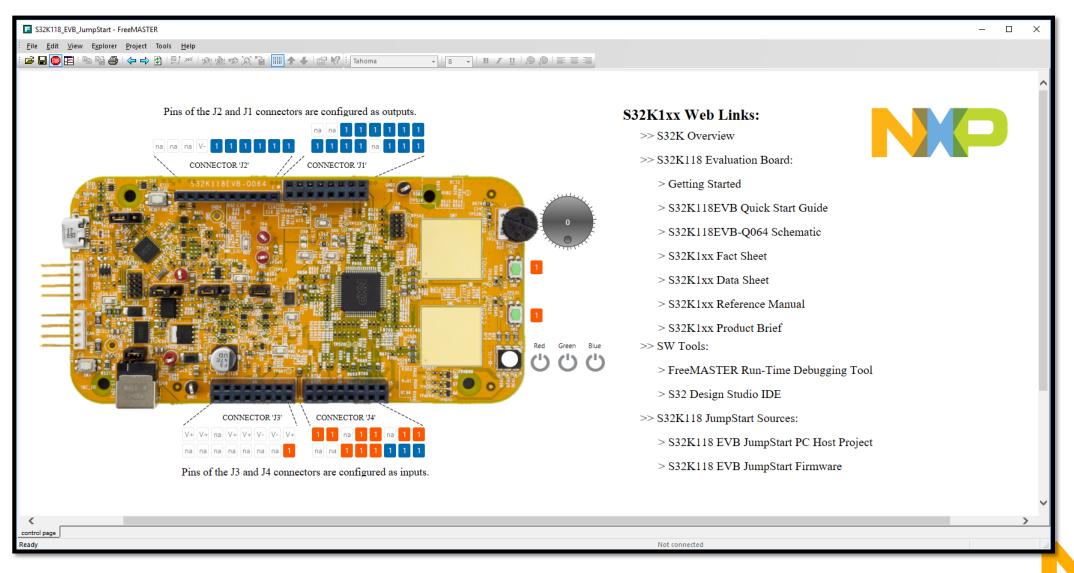
OR

 Setup communication automatically: Go to "Tools > Connection Wizard"

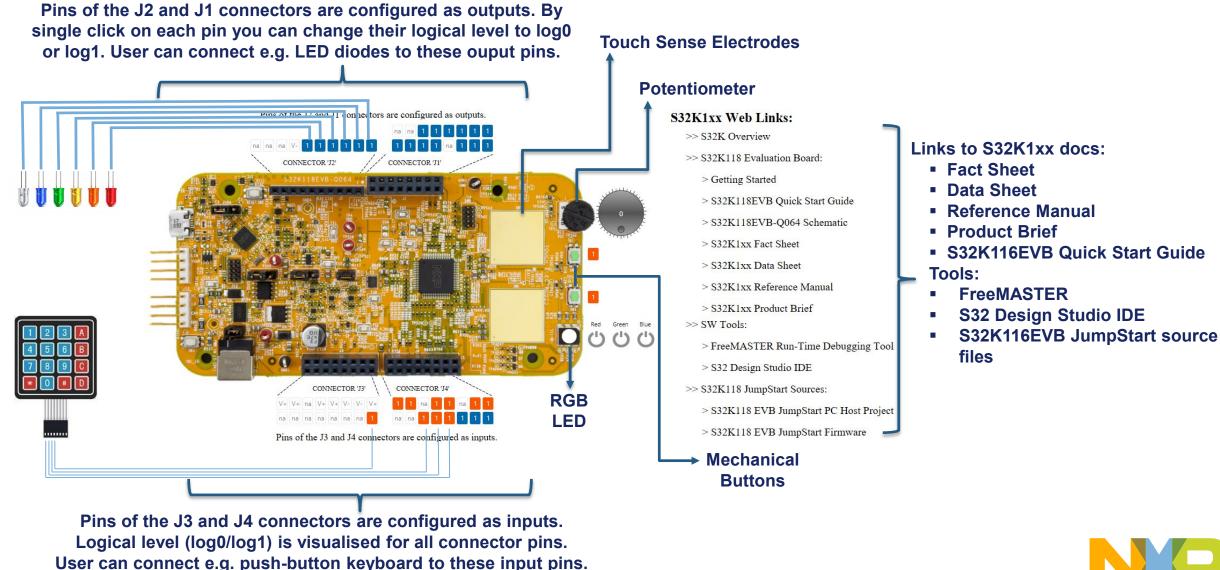




The FreeMASTER JumpStart project is loaded



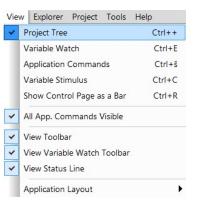
The FreeMASTER JumpStart project description



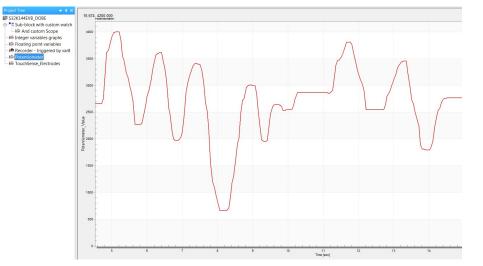
16 **EXTERNAL USE**

The FreeMASTER JumpStart oscilloscope feature examples

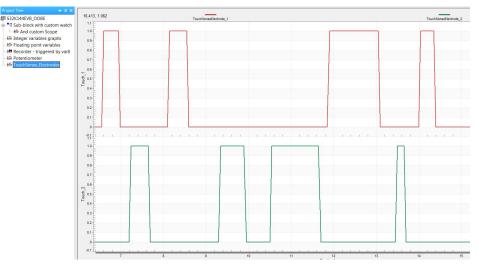
Display main project panel "View > Project Tree".



Display real-time oscilloscope graph examples such as "Potentiometer" or "Touch Sense Electrodes".



Analog values from potentiometer.



Responses from touch sense electrodes.



INTRODUCTION TO OPENSDA



Introduction to OpenSDA: 1 of 2

OpenSDA is an open-standard serial and debug adapter. It bridges serial and debug communications between a USB host and an embedded target processor. OpenSDA software includes a flash-resident USB mass-storage device (MSD) bootloader and a collection of OpenSDA Applications. S32K118 EVB comes with the MSD Flash Programmer OpenSDA Application preinstalled. Follow these instructions to run the OpenSDA Bootloader and update or change the installed OpenSDA Application.

Enter OpenSDA Bootloader Mode

- 1. Unplug the USB cable if attached
- 2. Set J104 on position 1-2.
- 3. Press and hold the Reset button (SW5)
- 4. Plug in a USB cable (not included) between a USB host and the OpenSDA USB connector (labeled "SDA")
- 5. Release the Reset button

A removable drive should now be visible in the host file system with a volume label of BOOTLOADER. You are now in OpenSDA Bootloader mode.

IMPORTANT NOTE: Follow the "Load an OpenSDA Application" instructions to update the MSD Flash Programmer on your S32K118 EVB to the latest version.

Load an OpenSDA Application

- While in OpenSDA Bootloader mode, double-click SDA_INFO.HTML in the BOOTLOADER drive. A web browser will open the OpenSDA homepage containing the name and version of the installed Application. This information can also be read as text directly from SDA_INFO.HTML
- 2. Locate the OpenSDA Applications
- 3. Copy & paste or drag & drop the MSD Flash Programmer Application *to the BOOTLOADER drive*
- Unplug the USB cable and plug it in again. The new OpenSDA Application should now be running and a S32K118 EVB drive should be visible in the host file system

You are now running the latest version of the MSD Flash Programmer. Use this same procedure to load other OpenSDA Applications.



Introduction to OpenSDA: 2 of 2

The MSD Flash Programmer is a composite USB application that provides a virtual serial port and an easy and convenient way to program applications into the S32K118 MCU. It emulates a FAT file system, appearing as a removable drive in the host file system with a volume label of S32K118EVB. Raw binary and Motorola S-record files that are copied to the drive are programmed directly into the flash of the S32K118 and executed automatically. The virtual serial port enumerates as a standard serial port device that can be opened with standard serial terminal applications.

Using the MSD Flash Programmer

- 1. Locate the .srec file of your project , file is under the Debug folder of the S32DS project.
- 2. Copy & paste or drag & drop one of the .srec files to the S32K118EVB drive

The new application should now be running on the S32K118 EVB. Starting with v1.03 of the MSD Flash Programmer, you can program repeatedly without the need to unplug and reattach the USB cable before reprogramming.

Drag one of the .srec code for the S32K118 EVB board over USB to reprogram the preloaded code example to another example.

NOTE: Flash programming with the MSD Flash Programmer is currently only supported on Windows operating systems. However, the virtual serial port has been successfully tested on Windows, Linux and Mac operating systems.

Using the Virtual Serial Port

- Determine the symbolic name assigned to the S32K118EVB virtual serial port. In Windows open Device Manager and look for the COM port named "PEMicro/Freescale – CDC Serial Port".
- 2. Open the serial terminal emulation program of your choice. Examples for Windows include <u>Tera Term</u>, <u>PuTTY</u>, and <u>HyperTerminal</u>
- 3. Press and release the Reset button (SW5) at anytime to restart the example application. Resetting the embedded application will not affect the connection of the virtual serial port to the terminal program.
- 4. It is possible to debug and communicate with the serial port at the same time, no need to stop the debug.

NOTE: Refer to the OpenSDA User's Guide for a description of a known Windows issue when disconnecting a virtual serial port while the COM port is in use.



INSTALLING S32DS





Download S32DS from:





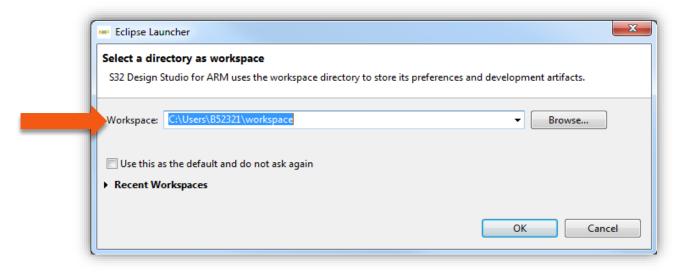
22 EXTERNAL USE

CREATE A NEW PROJECT IN S32 DESIGN STUDIO



Create New Project: First Time – Select a Workspace

- Start program: Click on "S32 Design Studio for ARM v2.0" icon
- Select workspace:
 - Choose default (see below example) or specify new one
 - Suggestion: Uncheck the box "Use this as the default and do not ask again"
 - Click OK





Create New Project: Top Menu Selection

• File – New – Project

workspace - C/C++ - S32 Design Studio for ARM File Edit Source Refactor Navigate Search Project Run Processor Expert Window Help New Alt+Shift+N * C* S32DS Application Project * * * * * * * * * * * * * * * * * * *	ick Access
New Alt+Shift+N * C S32DS Application Project Image: Size in the system in the	ick Access 🛛 😭 🔂 🏘
Open File Image: Signed	
Open Projects from File System S32DS Project from Example Makefile Project from Example Close Ctrl+W Makefile Project with Existing Code An outline is not availal Close All Ctrl+Shift+W C ++ Project Save Ctrl+S C /C++ Project Save As C /C++ Project	
Close All Ctrl+Shift+W C C++ Project Save Ctrl-S C Project Save As C/C++ Project C C/C++ Project	-
Close All Ctrl+Shift+W C+ Project Save Ctrl-S C Save As C/C++ Project	
Save As Save As	
Save As C/C++ Project S32DS Application Project	
S2DS Application Project	
Revert S32DS Library Project	
Move Project	
Rename F2 Convert to a C/C++ Project (Adds C/C++ Nature)	
Refresh F5 6 Source Folder	
Convert Line Delimiters To	
Print Ctrl+P image: Ctrl+P image: Header File	
Switch Workspace File from Template	
Restart G Class	
import 😁 Example	
Export Ctrl+N	
Properties Alt+Enter	
1 main.c [S32K144_Rev2_EVB_CAN_FD_LCD]	
2 lcd_func.h [S32K144_Rev2_EVB_CAN_FD]	
Exit Console 🕄 🔲 Properties 🙀 Debugger Console	🛃 📮 🕶 📑 🗖 🗖
Settings FreeRTOS Task Aware Debugger Console	
kite Project settings	4
🐅 Build settings	
No. Debug settings	
	•



Create New Project: S32DS Project

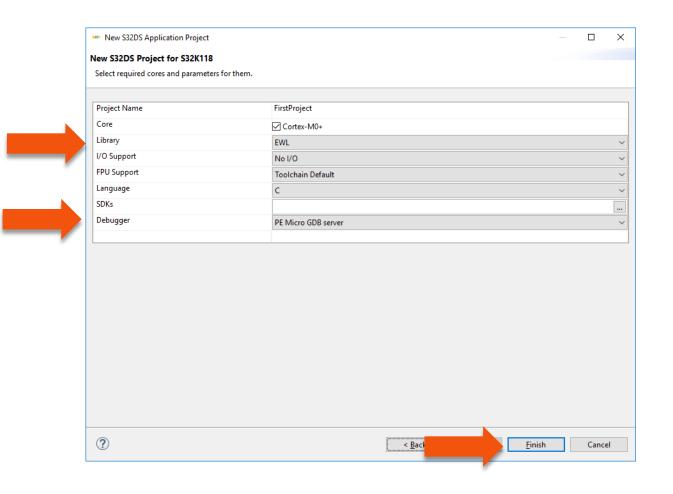
- Project Name:
 - Example: FirstProject
- Project Type:
 - Select from inside executable or library folder
- Next

Project name:				
FirstProject				
Use default location				
Location: C:\Users\nxa12689\workspaceS32DS.	ARM\FirstProject			Brow
Processors :	ToolChain Selec	tion:		
type filter text	Core Kind	Name	Toolchain	
 >>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	M0plus	Cortex-M0+	Standard S32DS toolchain for ARM	
S32K116	Description :			
> 🧽 Family MAC57D5xx > 淕 Family S32V	GCC toolchair	is selected		



Create New Project: S32DS Project

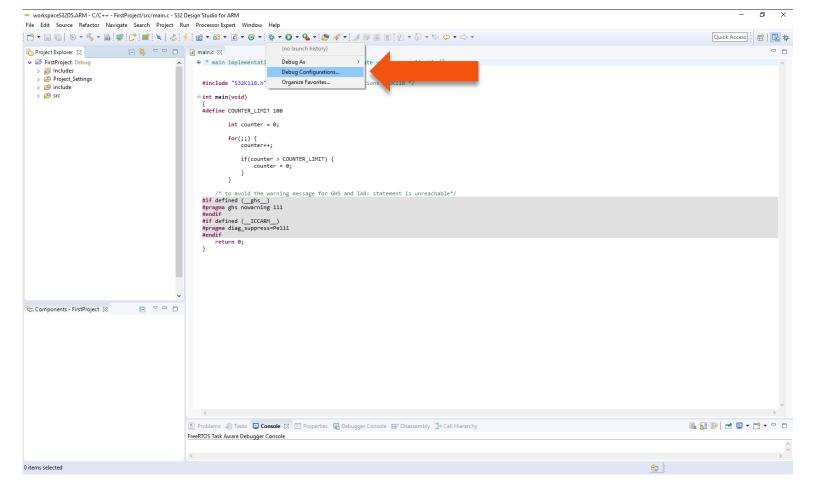
- Select Debugger Support and Library Support
- Click Finish





OpenSDA Configuration

- To Debug your project with OpenSDA, it is necessary to select the OpenSDA in the Debug Configuration.
- Select your project, and click on debug configuration



28 EXTERNAL USE

OpenSDA Configuration

- Select the Debug configuration under GDB PEMicro Interface Debugging
- Click on Debugger tab

Create, manage, and run configura	tions
Image: Second Secon	Name: FirstProject_Debug Main Debugger Source Common DS OS Awareness Project: FirstProject Brown Specify the number of additional ELF Files you wish to program: 0 Generate ELF Fields
	C/C++ Application: Debug/FirstProject.elf Build (if required) before launching Build Configuration: Debug © Enable auto build © Disable auto build © Disable auto build
< Ⅲ ► Filter matched 9 of 11 items	Revert



OpenSDA Configuration

- Select OpenSDA as the interface, if your board is plugged should appear in the Port field.
- Click Apply and debug to finish.

〕 🗎 🗙 🖻 ‡} ▼	
	Name: FirstProject_Debug
ype filter text	Main 🕸 Debugger 🕟 Startup 🦆 Source 🔳 Common 🖉 OS Awareness
C/C++ Application C/C++ Remote Application	Software Registration
C GDB Hardware Debugging	Please register your software to remove this message.
GDB PEMicro Interface Debugging FirstProject_Debug	Register now
FirstProject_Debug_RAM	PEMicro Interface Settings
FirstProject_Release	Interface: USB Multilink, USB Multilink FX, Embedded OSBDM/OSJTAG - USB Port
 GDB SEGGER J-Link Debugging Launch Group 	Port: USB1 - Multilink Universal Rev C (PEMBDD71E)
•	
	Select Device Vendor: NXP 2K1xx Target: S32K118F256M4
	Core: M0 V
	Specify IP Specify Network Car.
	Additional Options
	Emergency Kinetis Device Recovery by Full Chip Erase 🗹 Use SWD protocol
	Advanced Options
	Hardware Interface Power Control (Voltage> Power-Out Jack)
	Provide power to target Regulator Output Voltage Power Down Delay 250 ms
	Dower off target upon software evit 2V Power Up Delay 1000 mc V
lter matched 9 of 25 items	Revert Apply

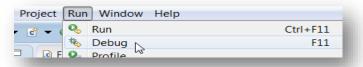


DEBUG BASICS



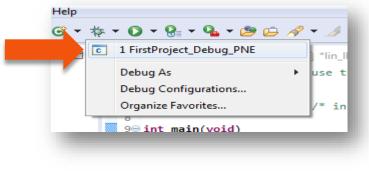
Debug Basics: Starting the Debugger

- Debug configuration is only required once. Subsequent starting of debugger does not require those steps.
- Three options to start debugger:
 - If the "Debug Configuration" has not been closed, click on "Debug" button on bottom right
 - Select Run Debug (or hit F11)



Note: This method currently selects the desktop target (*project.elf*) and gives an error. Do not use until this is changed.

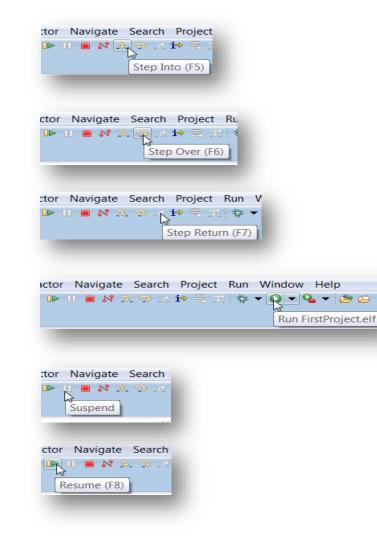
<u>Recommended Method</u>: Click on pull down arrow for bug icon and select ..._debug.elf target





Debug Basics: Step, Run, Suspend, Resume

- Step Into (F5)
- Step Over (F6)
- Step Return (F7)
- Run
- Suspend
- Resume (F8)





Debug Basics: View & Alter Variables

- View variables in "Variables" tab.
- Click on a value to allow typing in a different value.

Name	Туре	Value
⊗= counter	int	8
		2



Debug Basics: View & Alter Registers

- View CPU registers in the "Registers" tab
- Click on a value to allow typing in a different value
- View peripheral registers in the EmbSys Registers tab

N	Name	Value
-	General Registers	
	1919 rO	3
	3838 r1	5
	1919 r2	536866944
	1919 r3	8
	1919 -4	0

Arch: cor	tex-m0 Vendor: Freescale Cl	hip: SKEAZ1284	Board: none					
egister		Hex	Bin	Reset	Access	Address	Description	
🔺 🗁 IRQ							Interrupt	
⊿ 🔐	SC SC	0x00	0000000	0x00	RW	0x40031000	Interrupt Pin Request Status and Co	
	IRQMOD (bit 0)	0x0	0				0: IRQ event is detected only on f	
	IRQIE (bit 1)	0x0	0				🕲 0: Interrupt request when IRQF se	
	IRQACK (bit 2)	0x0	0				IRQ Acknowledge	
	IRQF (bit 3)	0x0	0				😳 0: No IRQ request	
	IRQPE (bit 4)	0x0	0				O: IRQ pin function is disabled.	
	IRQEDG (bit 5)	0x0	0				O: IRQ is falling-edge or falling-e	
	IRQPDD (bit 6)	0x0	0				O: IRQ pull device enabled if IRQI	
	RESERVED (bit 7)	0x0	0				no description available	
- CDC			III				Coulie Destandance Charle	



Debug Basics: View & Alter Memory

Add Memory Monitor

📮 Console	Tasks	🖹 Problems	Executables	🚺 Memory 🔀
Monitors		Add Memor	y Monitor	

Select Base Address
 to Start at : 0x2000000

Monitor Me	emory		×
Enter address of 0x20000000	or expression	on to monit	tor:
	OK	Can	cel

View Memory

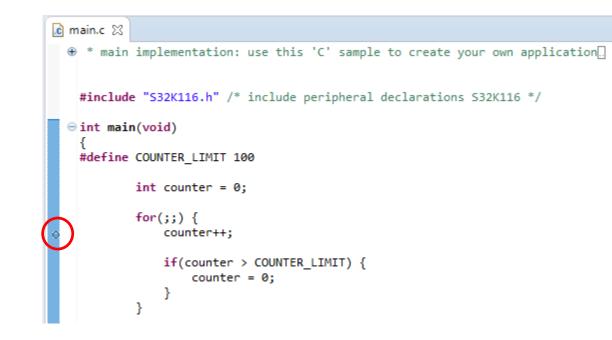
📃 Console 🧔 Tasks 🖹 Problems	🜔 Executables 🛛 🙀 Debugger Co	onsole 📋 Men	nory 🛛 🔚 Oi	utline	
Monitors	💠 🗶 💥 0x20000000:	0x20000000 <he< td=""><td>x> 🛛 🔶 N</td><td>lew Renderings.</td><td></td></he<>	x> 🛛 🔶 N	lew Renderings.	
Ox20000000	Address	0 - 3	4 - 7	8 - B	C - F
	2000000	00000000	00000000	00000000	0000000
	20000010	00000000	00000000	00000000	0000000
	20000020	00000000	00000000	00000000	0000000



Debug Basics: Breakpoints

Add Breakpoint: Point and Click

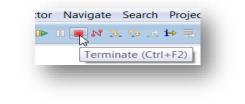
light blue dot represents debugger breakpoint





Debug Basics: Reset & Terminate Debug Session

- Reset program counter
- Terminate Ctl+F2()



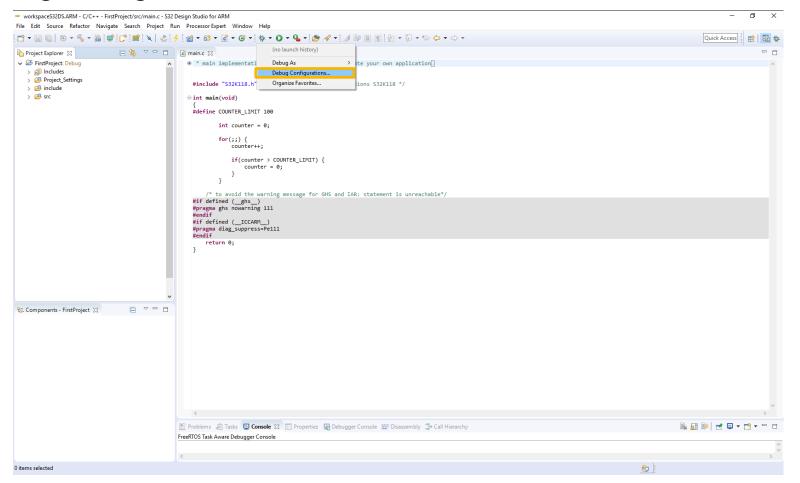


CREATE A P&E DEBUG CONFIGURATION (OPTIONAL)



New P&E debug configuration

Click in debug configurations

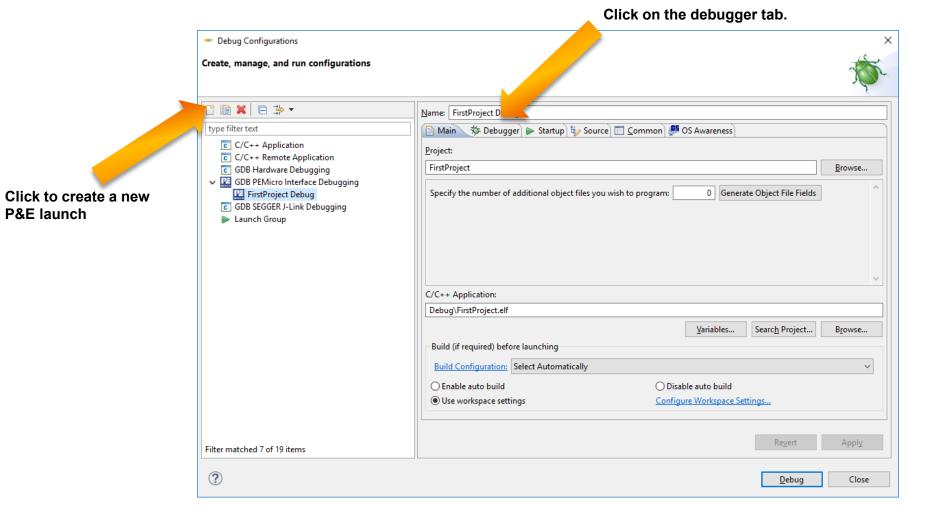




40 EXTERNAL USE

New P&E debug configuration

Create a new P&E launch configuration





New P&E debug configuration

• Select the device

₩ Debug Configurations	×
Create, manage, and run configurations ① Plugin has not been registered. Some functionality may not be available.	- TANK
Image: Image	Name: FirstProject_Debug Main ** Debugger Startup ** Source Software Registration ** Select Target Device Please register your software to removet ** Match 0 or more characters: ** Match 0 or more characters: ** Match 0 or more characters: ** Match 0 or more characters: ** Match 0 or more characters: ** Match 0 or more characters: ** Match 0 or more characters: ** Match 0 or more characters: ** Match 0 or more characters: ** Match 0 or more characters: ** Match 0 or more characters: ** Match 0 or more characters: ** Match 0 or more characters: ** Match 0 or more characters: ** Match 0 or more characters: ** Match 0 or more characters: ** Match 0 or more characters: ** Deprint USB Multilink Universal Rev C Select Device Vendor: NXP Core: M0 Sepecify IP Specify Additional Options SizeXitafF12M15 SizeXitafF12M15 SizeXitafF12M115 SizeXitafF12M11 SizeXitafF12M0M11 SizeXitafF12M0M11 SizeXitafF12M0M11 SizeXitafF12M0M11 SizeXitafF12M0M11 SizeXitafF12M0M11 SizeXitaf
?	<u>D</u> ebug Close

• Click Apply and debug your application



42 EXTERNAL USE

USEFUL LINKS



Useful Links

- <u>Cookbook application note</u>. This application note contains a bunch of simple examples of how to use different peripherals.
- <u>S32K1xx community</u>. Visit this site for request support on the S32K1xx products, you can also look for threads that may contain the answer that you are looking for.





SECURE CONNECTIONS FOR A SMARTER WORLD