

Quick Start Guide

MC56F83000 Evaluation Kit



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GET TO KNOW THE MC56F83000-EVK

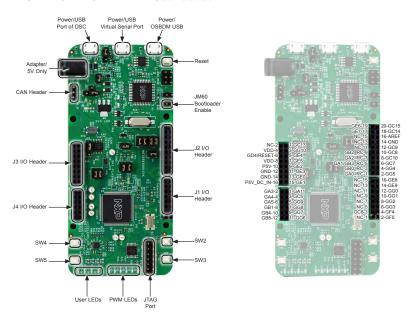


Figure 1: MC56F83000-EVK Callouts

Figure 2: MC56F83000-EVK Pin-Outs

ATTENTIONS

- MC56F83000-EVK can be powered by either of the three USB connectors (J8, J14, J21), or the adapter input (J24). Beware that only an adapter with 5V output can be used.
- When the board is powered up, a green LED D14 will illuminate, indicating 3.3V is on.
 Notice that Boot ROM code is executed first out of reset, a time of about 3 seconds
 are consumed by ROM bootloader to check the active communication port (I²C, CAN,
 SCI) before application code is executed.
- J8 is the on-board OSBDM (realized by JM60) connecter which can be used to debug/ program 56F83789.
- J14 is the virtual serial port connector which can be used by ROM bootloader. CP210x USB to UART bridge VCP drivers are needed.
- 5. J21 is the USB connector of 56F83789 on chip USB module.
- **6.** J6 is JM60 bootloader enable jumper for OSBDM update.
- J10 is the JTAG connector for 56F83789. Remember to remove the four jumpers on J11
 when this JTAG is used. This is to avoid the impact of on-board OSBDM circuit.
- Explore more out-of-box demos and download software and tools at www.nxp.com/MC56F83000-EVK.

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