# SOT758-1

HVQFN16; plastic, thermal enhanced very thin quad flatpack; no leads; 16 terminals; 0.5 mm pitch; 3 mm x 3 mm x 0.85 mm body

29 September 2025 Package information



### 1 Package summary

Terminal position codeQ (quad)Package type descriptive codeHVQFN16Package type industry codeHVQFN16

Package style descriptive code HVQFN (thermal enhanced very thin quad flatpack; no

leads)

Package body material type P (plastic)

JEDEC package outline code MO-220

Mounting method type S (surface mount)

**Issue date** 21-10-2002

Table 1. Package summary

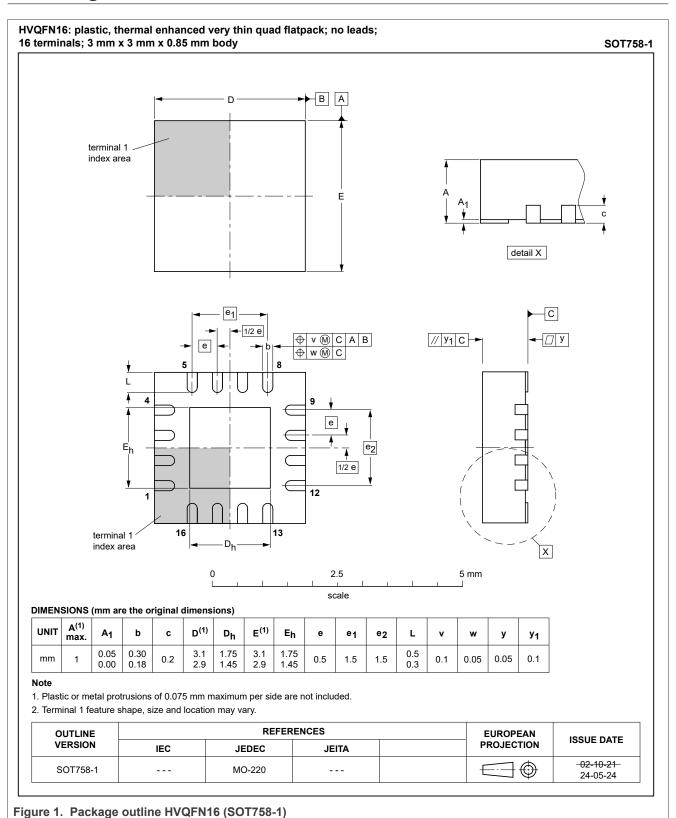
Parameter	Min	Nom	Max	Unit
package length	2.9	3	3.1	mm
package width	2.9	3	3.1	mm
seated height	-	0.85	1	mm
package height	-	0.85	-	mm
nominal pitch	-	0.5	-	mm
actual quantity of termination	-	16	-	



NXP Semiconductors SOT758-1

HVQFN16; plastic, thermal enhanced very thin quad flatpack; no leads; 16 terminals; 0.5 mm pitch; 3 mm x 3 mm x 0.85 mm body

## 2 Package outline

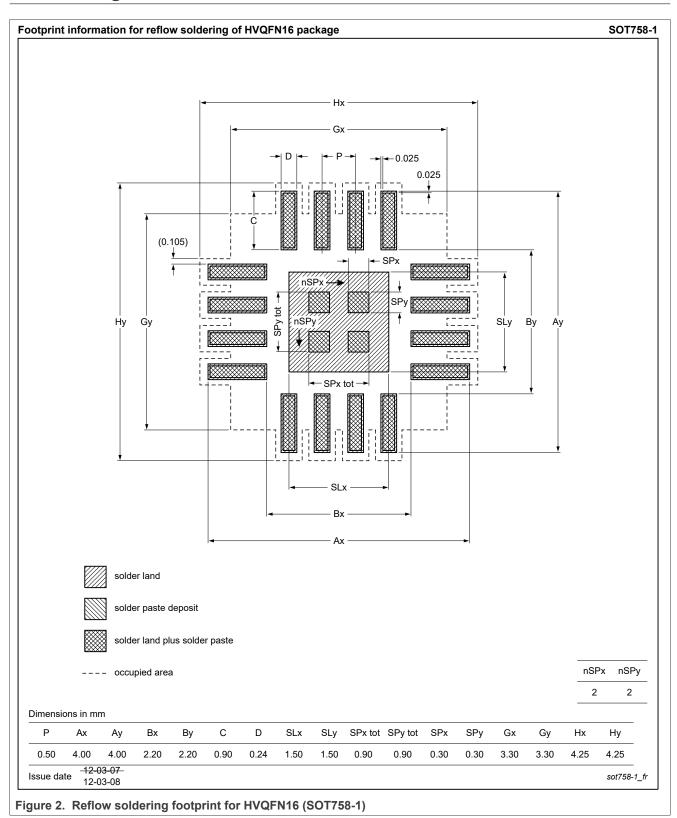


SOT758-1

NXP Semiconductors SOT758-1

HVQFN16; plastic, thermal enhanced very thin quad flatpack; no leads; 16 terminals; 0.5 mm pitch; 3 mm x 3 mm x 0.85 mm body

#### 3 Soldering



NXP Semiconductors SOT758-1

HVQFN16; plastic, thermal enhanced very thin quad flatpack; no leads; 16 terminals; 0.5 mm pitch; 3 mm x 3 mm x 0.85 mm body

#### 4 Legal information

#### **Disclaimers**

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including -without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

**NXP Semiconductors** SOT758-1

HVQFN16; plastic, thermal enhanced very thin quad flatpack; no leads; 16 terminals; 0.5 mm pitch; 3 mm x 3 mm x 0.85 mm body

#### **Contents**

1	Package summary	1
2	Package outline	2
3	Soldering	3
4	Legal information	4

Date of release: 29 September 2025 Document identifier: SOT758-1