

**Package information** 

# **Package summary**

**Terminal position code** Q (quad) **HVQFN56** Package type descriptive code

Package style descriptive code HVQFN (thermal enhanced very thin quad

flatpack; no leads)

Mounting method type S (surface mount)

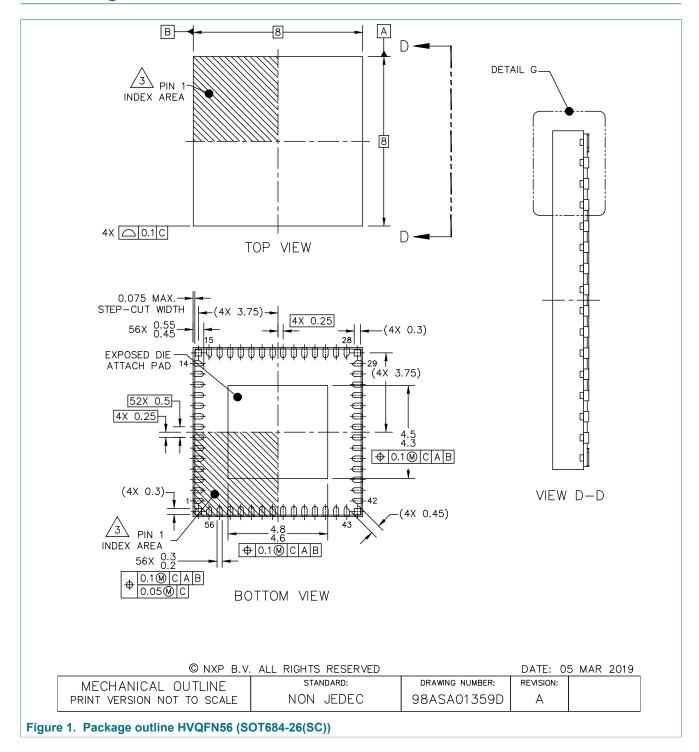
Issue date 05-03-2019 98ASA01359D Manufacturer package code

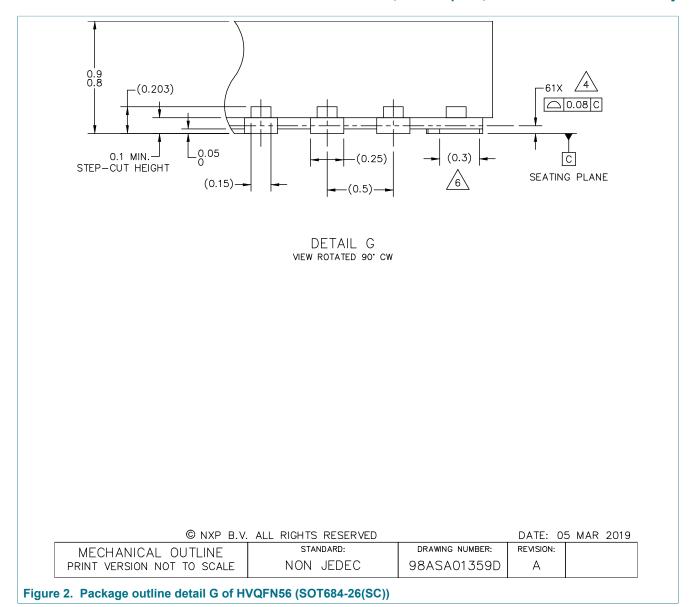
Table 1. Package summary

Parameter	Min	Nom	Max	Unit
package length	-	8	-	mm
package width	-	8	-	mm
seated height	-	0.85	-	mm
nominal pitch	-	0.5	-	mm
actual quantity of termination	-	56	-	



# 2 Package outline





## 3 Soldering

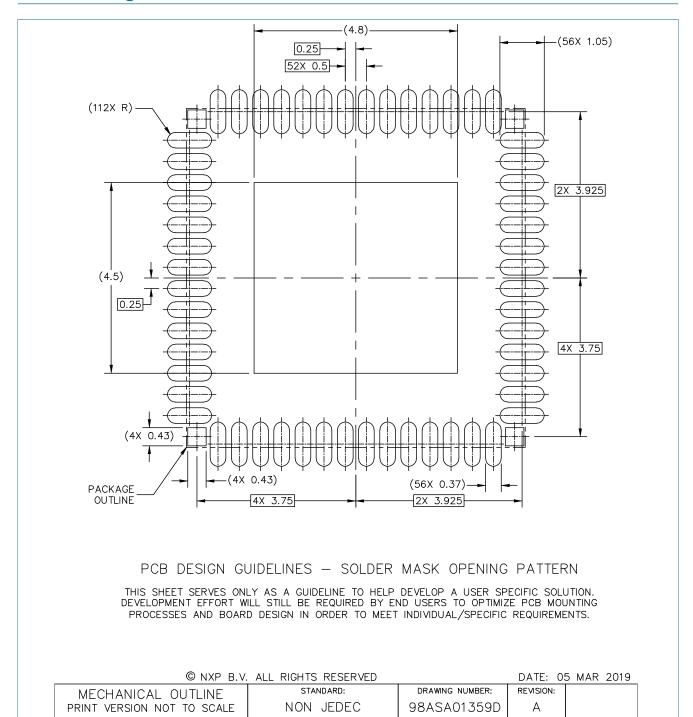


Figure 3. Reflow soldering footprint part1 for HVQFN56 (SOT684-26(SC))

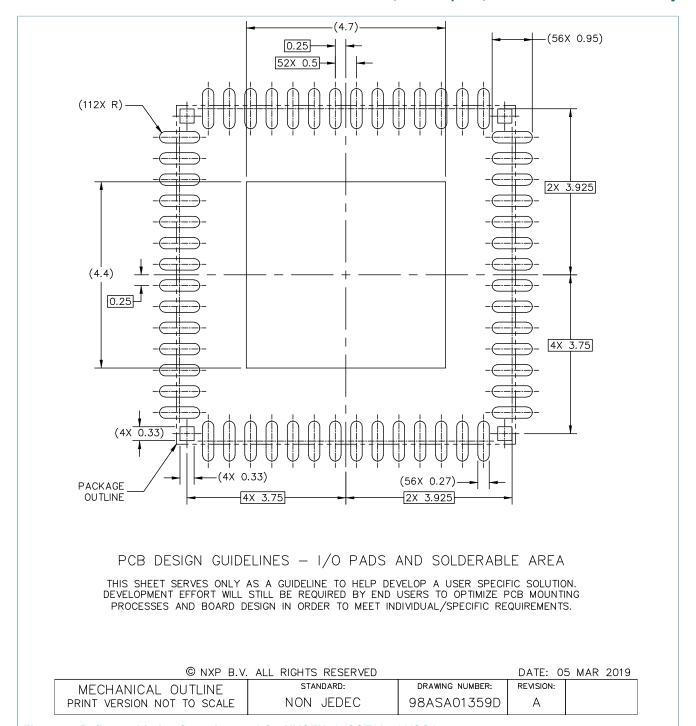
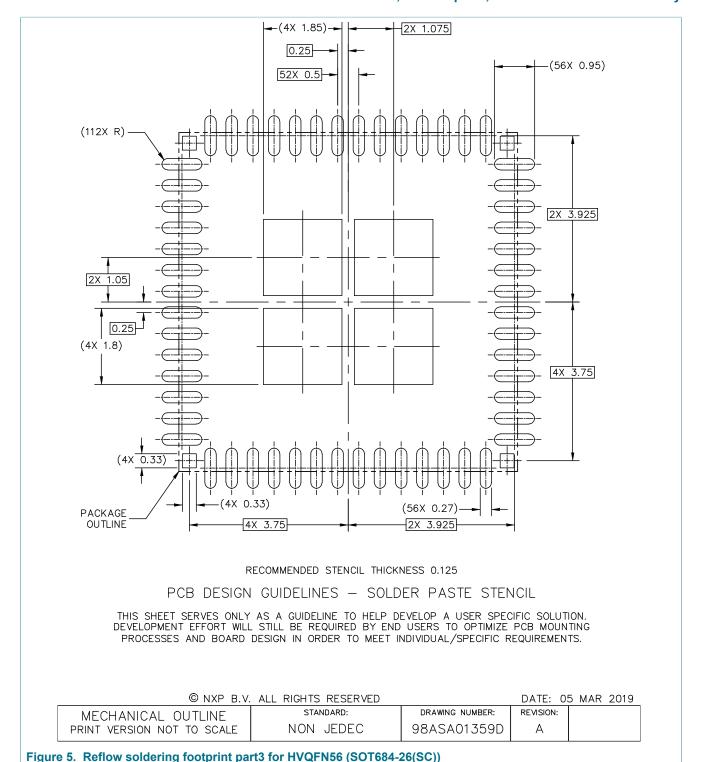


Figure 4. Reflow soldering footprint part2 for HVQFN56 (SOT684-26(SC))



#### NOTES:

- 1. ALL DIMENSIONS ARE IN MILLIMETERS.
- 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.

 $\sqrt{3}$ . PIN 1 FEATURE SHAPE, SIZE AND LOCATION MAY VARY.

 $\sqrt{4}$ . COPLANARITY APPLIES TO LEADS, DIE ATTACH FLAG AND CORNER NON-FUNCTIONAL PADS.

5. MIN. METAL GAP SHOULD BE 0.25 MM.

6. ANCHORING PADS.

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DATE: 05 MAR 2019 DRAWING NUMBER: STANDARD: REVISION: MECHANICAL OUTLINE PRINT VERSION NOT TO SCALE NON JEDEC 98ASA01359D Α

Figure 6. Package outline note of HVQFN56 (SOT684-26(SC))

## 4 Legal information

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### **NXP Semiconductors**

SOT684-26(SC)

HVQFN56, thermal enhanced very thin quad flat package; no leads; step-cut wettable flank; 56 terminals; 0.5 mm pitch, 8 mm x 8 mm x 0.85 mm body

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