

plastic thermal enhanced very thin quad flat package; no leads; 56 terminals; body 8 x 8 x 0.85 mm

21 April 2016

**Package information** 

# 1. Package summary

Terminal position code Q (quad)

Package type descriptive code HVQFN56

Package type industry code HVQFN56

Package style descriptive code HVQFN (thermal enhanced very thin quad

flatpack; no leads)

Package style suffix code NA (not applicable)

Package body material type P (plastic)

JEDEC package outline code --JEITA package outline code ---

Mounting method type S (surface mount)

**Issue date** 13-7-2015

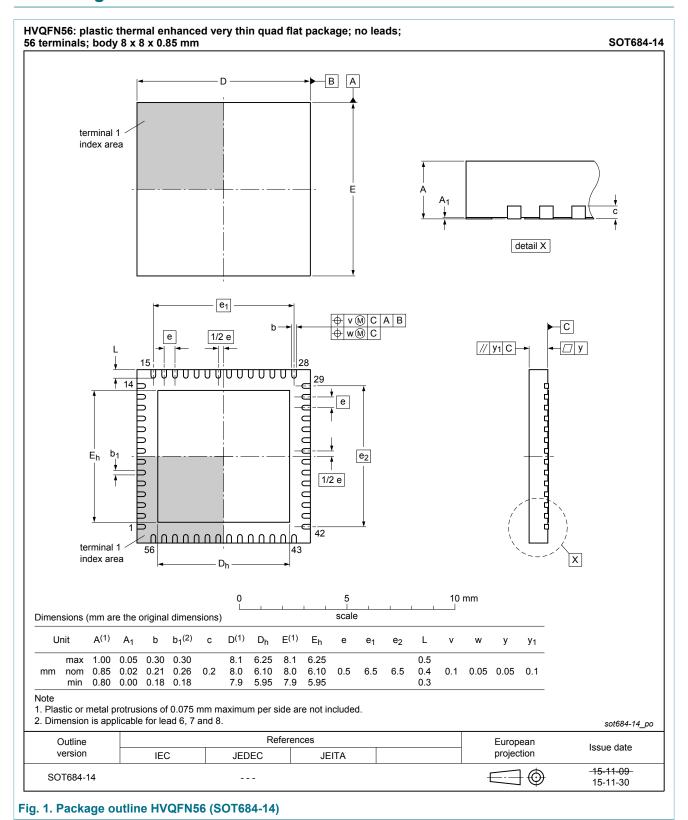
#### **Table 1. Package summary**

Symbol	Parameter	Min	Тур	Nom	Max	Unit
D	package length	7.9	-	8	8.1	mm
E	package width	7.9	-	8	8.1	mm
Α	seated height	0.8	-	0.85	1	mm
A <sub>2</sub>	package height	-	-	-	-	
е	nominal pitch	-	-	0.5	-	mm
n <sub>2</sub>	actual quantity of termination	-	-	56	-	



plastic thermal enhanced very thin quad flat package; no leads; 56 terminals; body 8 x 8 x 0.85 mm

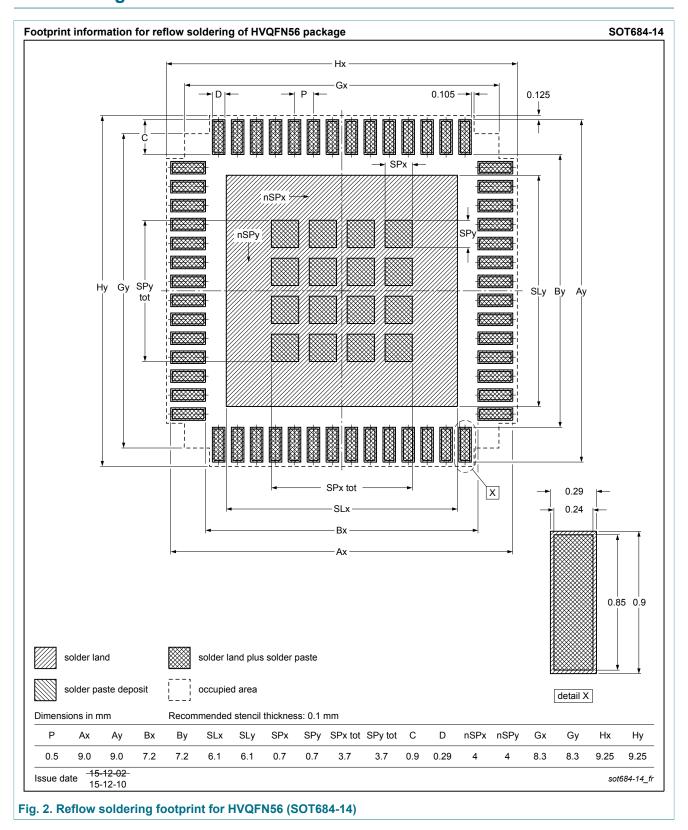
## 2. Package outline



SOT684-14

plastic thermal enhanced very thin quad flat package; no leads; 56 terminals; body 8 x 8 x 0.85 mm

## 3. Soldering



plastic thermal enhanced very thin quad flat package; no leads; 56 terminals; body 8 x 8 x 0.85 mm

### 4. Legal information

#### **Disclaimers**

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

plastic thermal enhanced very thin quad flat package; no leads; 56 terminals; body 8 x 8 x 0.85 mm

### 5. Contents

1.	Package summary	. 1
2.	Package outline	. 2
3.	Soldering	. 3
4.	Legal information	. 4

For more information, please visit: http://www.nxp.com For sales office addresses, please send an email to: salesaddresses@nxp.com Date of release: 21 April 2016

<sup>©</sup> NXP Semiconductors N.V. 2016. All rights reserved