

SOT2089-1(SC)

HWQFN28, thermal enhanced very-very thin quad flat package, no leads, step-cut wettable flank, 0.5 mm pitch, 4.5 mm x 4.5 mm x 0.68 mm body

20 January 2022

Package information

1 Package summary

Terminal position code Q (quad)

Package type descriptive code HWQFN28

Package style descriptive code HWQFN (thermal enhanced very very thin quad

flatpack; no leads)

Package body material type P (plastic)

Mounting method type S (surface mount)

Issue date 25-06-2021

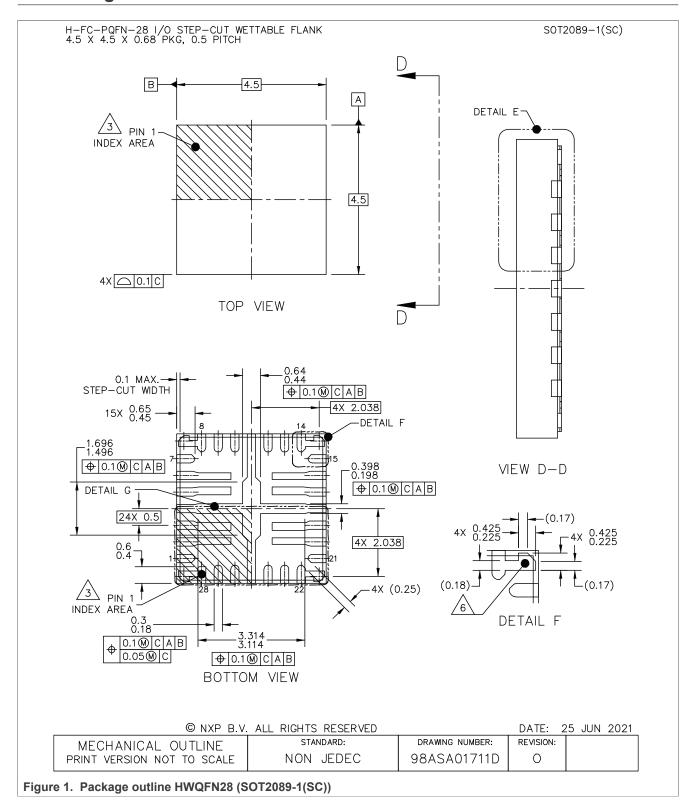
Manufacturer package code 98ASA01711D

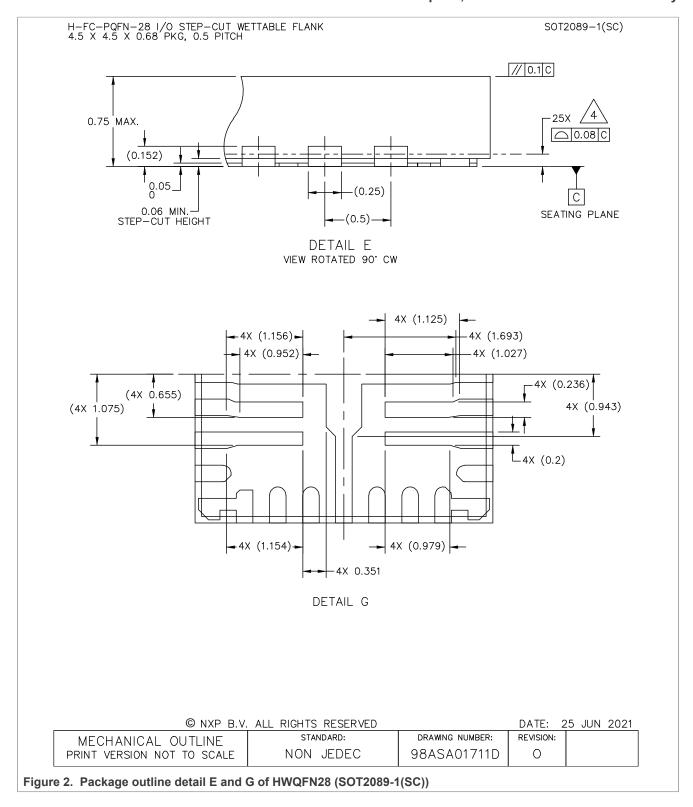
Table 1. Package summary

Parameter	Min	Nom	Max	Unit
package length	-	4.5	-	mm
package width	-	4.5	-	mm
seated height	-	0.68	0.75	mm
nominal pitch	-	0.5	-	mm
actual quantity of termination	-	28	-	

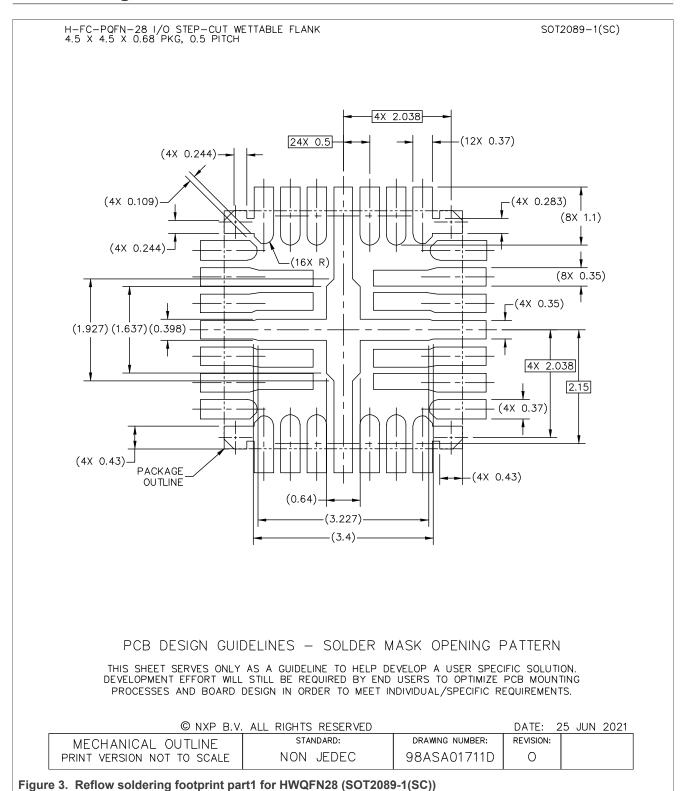


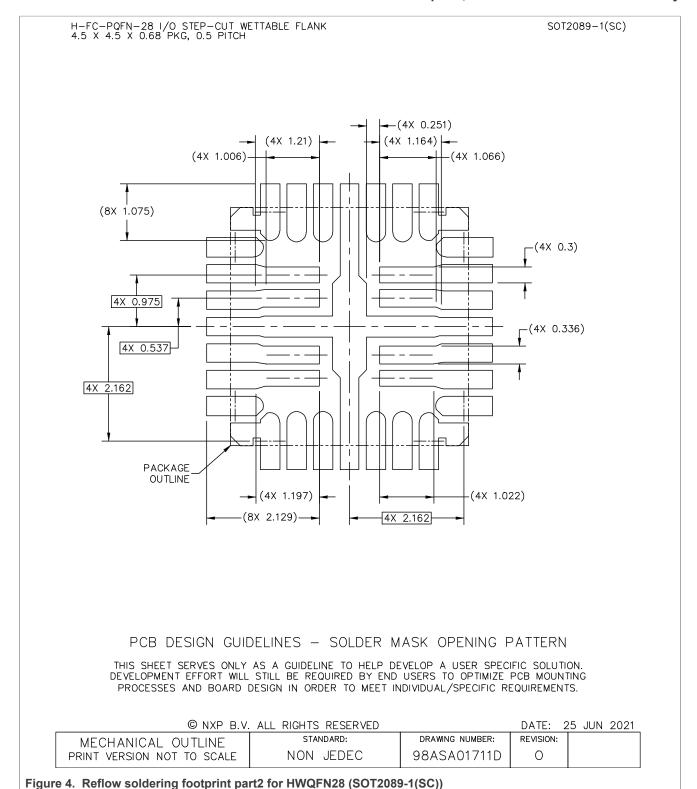
2 Package outline





3 Soldering

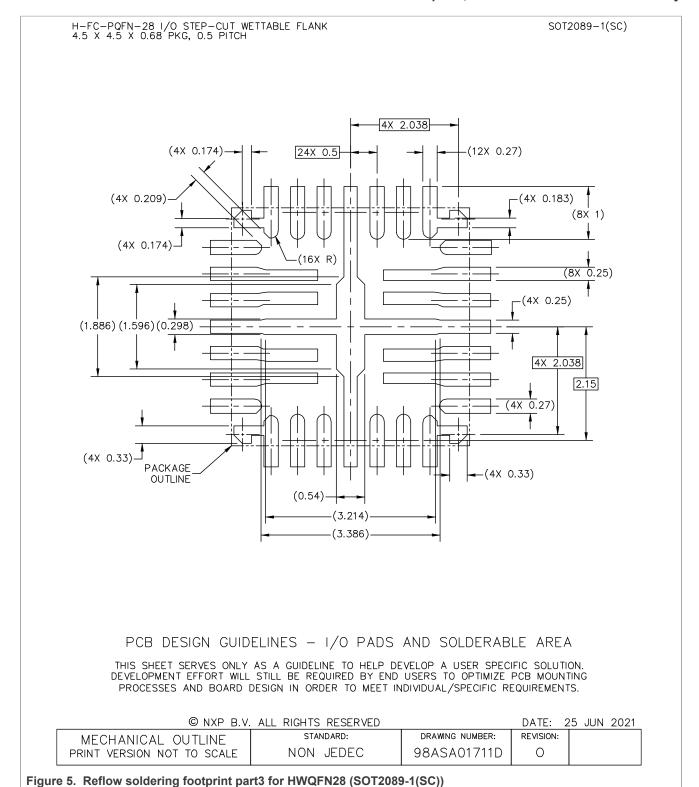


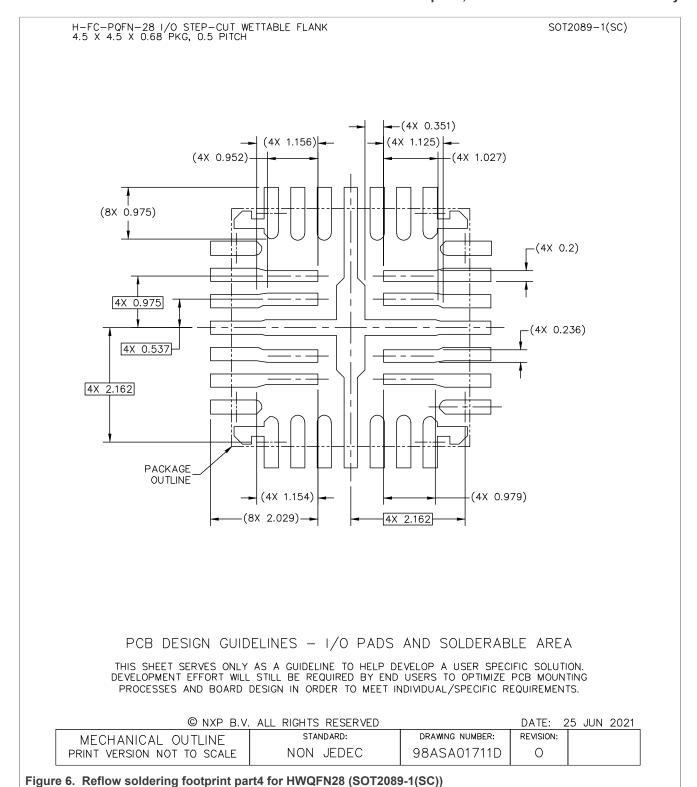


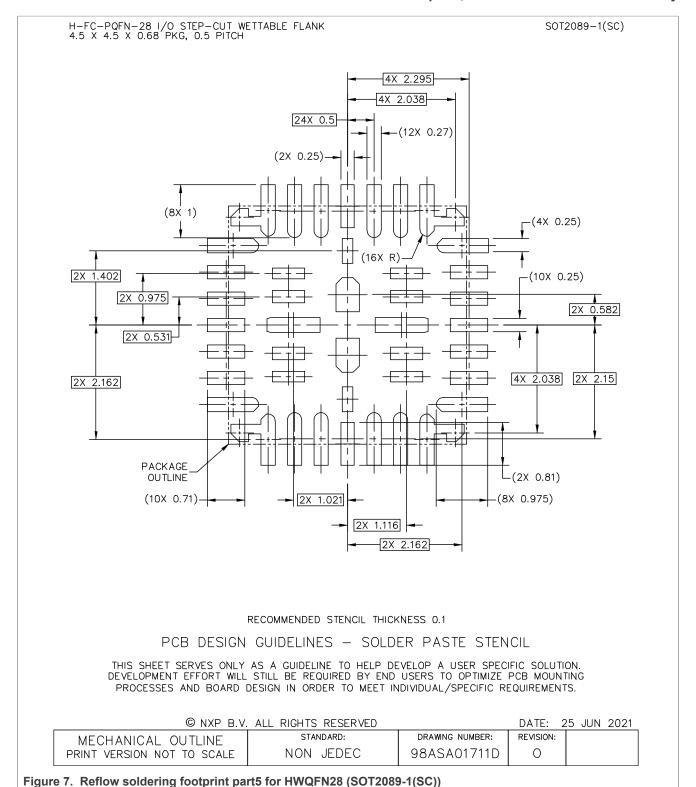
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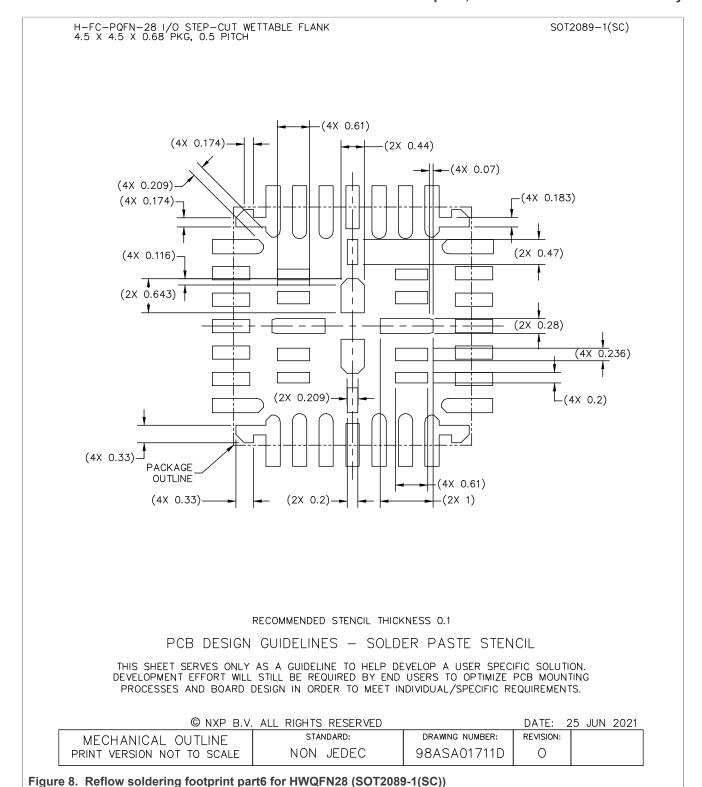
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H-FC-POFN-28 I/O STEP-CUT WETTABLE FLANK 4.5 \times 4.5 \times 0.68 PKG, 0.5 PITCH SOT2089-1(SC) NOTES: 1. ALL DIMENSIONS ARE IN MILLIMETERS. 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994. 3. PIN 1 FEATURE, SHAPE, SIZE AND LOCATION MAY VARY. 4. COPLANARITY APPLIES TO LEADS AND DIE ATTACH PAD. 5. MIN. METAL GAP FOR LEAD TO EXPOSED PAD SHALL BE 0.2 MM. 6. ANCHORING PADS. © NXP B.V. ALL RIGHTS RESERVED DATE: 25 JUN 2021 DRAWING NUMBER: STANDARD: REVISION: MECHANICAL OUTLINE 98ASA01711D 0 PRINT VERSION NOT TO SCALE NON JEDEC

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Figure 9. Package outline note HWQFN28 (SOT2089-1(SC)))

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4 Legal information

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