

WLCSP12, wafer level chip scale package, 12 terminals, 0.35 mm pitch, 1.55 mm x 1.18 mm x 0.455 mm body (backside coating included)

15 April 2021

Package information

1 Package summary

Terminal position code	B (bottom)
Package type descriptive code	WLCSP12
Package style descriptive code	WLCSP (wafer level chip-size package)
Mounting method type	S (surface mount)
Issue date	11-03-2021
Manufacturer package code	98ASA01624D

Table 1. Package summary

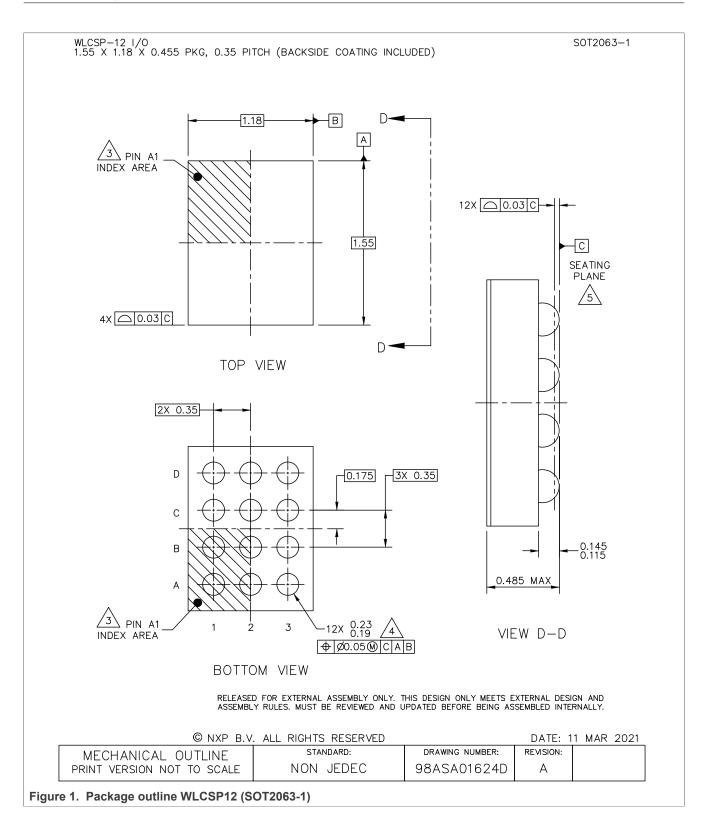
Parameter	Min	Nom	Мах	Unit
package length	1.52	1.55	1.58	mm
package width	1.15	1.18	1.21	mm
seated height	-	0.455	0.485	mm
nominal pitch	-	0.35	-	mm
actual quantity of termination	-	12	-	



SOT2063-1

WLCSP12, wafer level chip scale package, 12 terminals, 0.35 mm pitch, 1.55 mm x 1.18 mm x 0.455 mm body (backside coating included)

2 Package outline

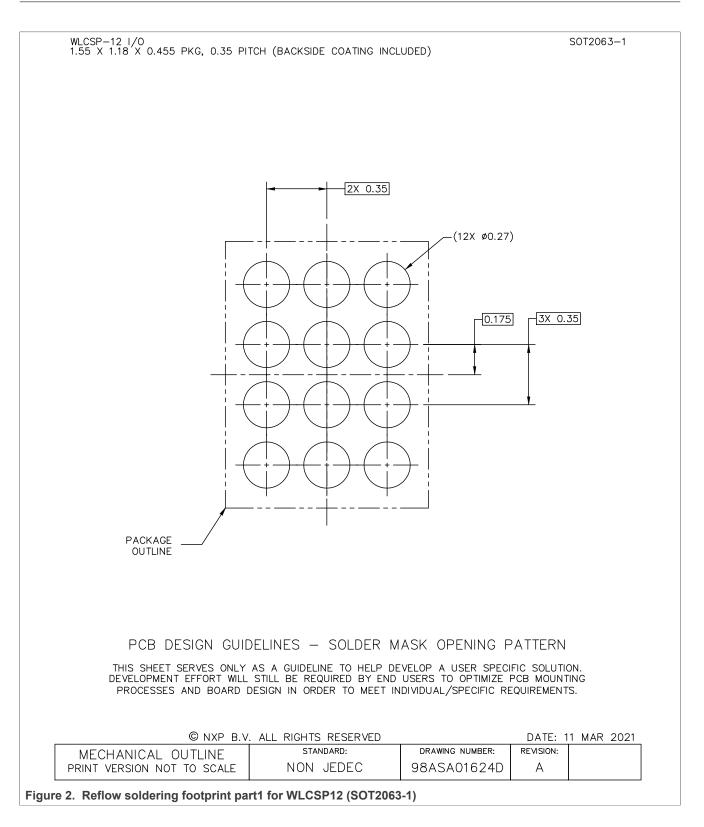


SOT2063-1
Package information

SOT2063-1

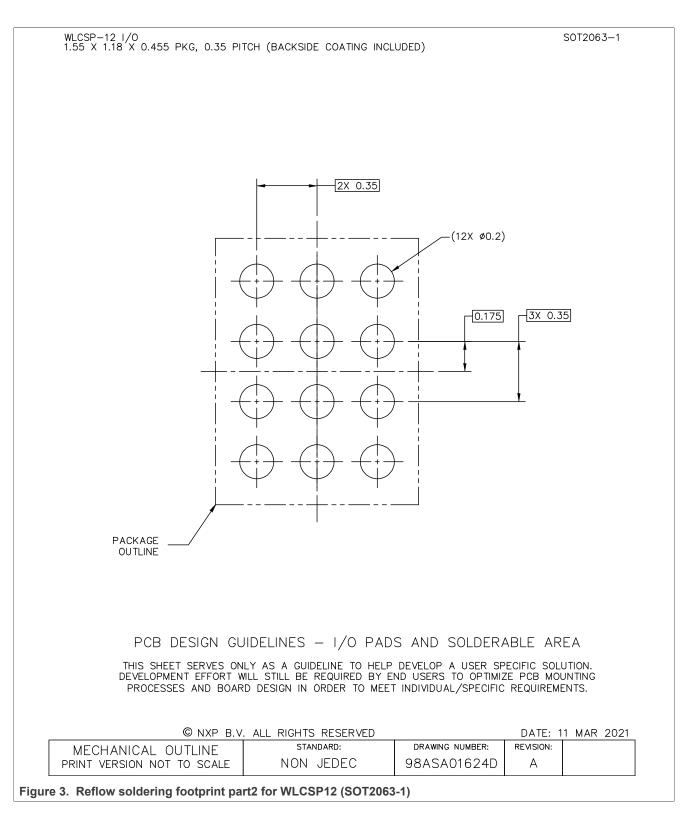
WLCSP12, wafer level chip scale package, 12 terminals, 0.35 mm pitch, 1.55 mm x 1.18 mm x 0.455 mm body (backside coating included)

3 Soldering



NXP Semiconductors

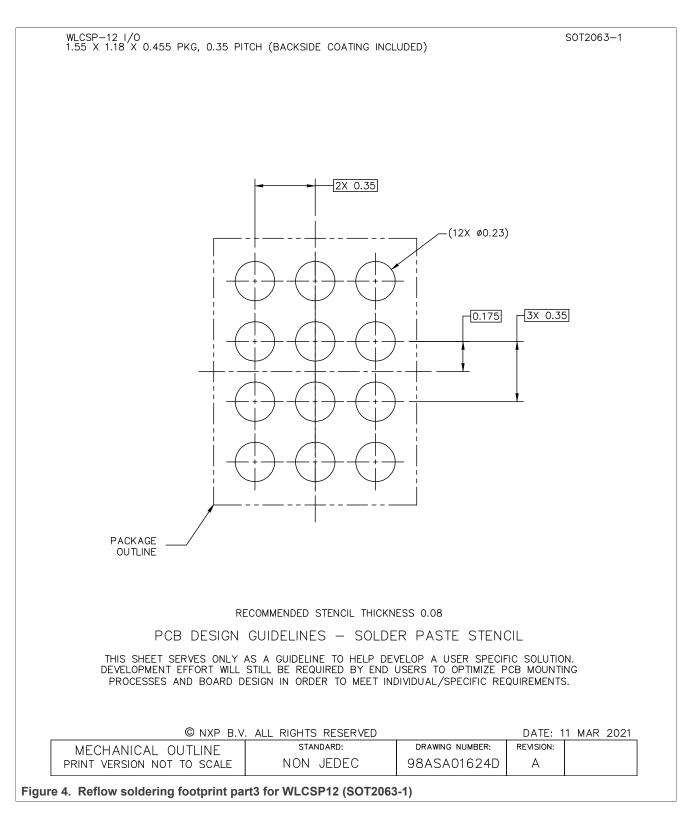
WLCSP12, wafer level chip scale package, 12 terminals, 0.35 mm pitch, 1.55 mm x 1.18 mm x 0.455 mm body (backside coating included)



SOT2063-1

NXP Semiconductors

WLCSP12, wafer level chip scale package, 12 terminals, 0.35 mm pitch, 1.55 mm x 1.18 mm x 0.455 mm body (backside coating included)



SOT2063-1 Package information

WLCSP12, wafer level chip scale package, 12 terminals, 0.35 mm pitch, 1.55 mm x 1.18 mm x 0.455 mm body (backside coating included)

WLCSP-12 I/O 1.55 X 1.18 X 0.455 PKG, 0.35 PITC	H (BACKSIDE COATING IN	CLUDED)	SOT206	3–1
NOTES:				
1. ALL DIMENSIONS IN MILLIMETER	RS.			
2. DIMENSIONING AND TOLERANCI	NG PER ASME Y14.5M-19	994.		
3. PIN A1 FEATURE SHAPE, SIZE	AND LOCATION MAY VAR	RY.		
4. MAXIMUM SOLDER BALL DIAME	TER MEASURED PARALLEI	L TO DATUM C.		
5. DATUM C, THE SEATING PLAN	E, IS DETERMINED BY THE	E SPHERICAL CROWNS O	F THE SOLDER BA	LLS.
6. THIS PACKAGE HAS A BACK S	SIDE COATING THICKNESS	OF 0.025.		
2				
© NXP B.V.	ALL RIGHTS RESERVED STANDARD:	DRAWING NUMBER:	DATE: 11 MAR REVISION:	202
PRINT VERSION NOT TO SCALE	NON JEDEC	98ASA01624D	A	
I	2 (SOT2063-1)		I	

SOT2063-1

WLCSP12, wafer level chip scale package, 12 terminals, 0.35 mm pitch, 1.55 mm x 1.18 mm x 0.455 mm body (backside coating included)

4 Legal information

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

SOT2063-1

WLCSP12, wafer level chip scale package, 12 terminals, 0.35 mm pitch, 1.55 mm x 1.18 mm x 0.455 mm body (backside coating included)

Contents

1	Package summary1
2	Package outline
3	Soldering3
4	Legal information7

© NXP B.V. 2021.

For more information, please visit: http://www.nxp.com For sales office addresses, please send an email to: salesaddresses@nxp.com

Date of release: 15 April 2021