

# SOT1510-2

HLQFP64, plastic, thermal enhanced low profile quad flat package; 64 terminals; 0.5 mm pitch; 10 mm x 10 mm x 1.4 mm body

24 July 2018

**Package information** 

### 1 Package summary

Terminal position code Q (quad)

Package type descriptive code HLQFP64

Package style descriptive code HLQFP (thermal enhanced low profile quad flat

package)

Package body material type P (plastic)

JEDEC package outline code MS-026 BCD

Mounting method type S (surface mount)

Issue date24-07-2018Manufacturer package code98ASA10763D

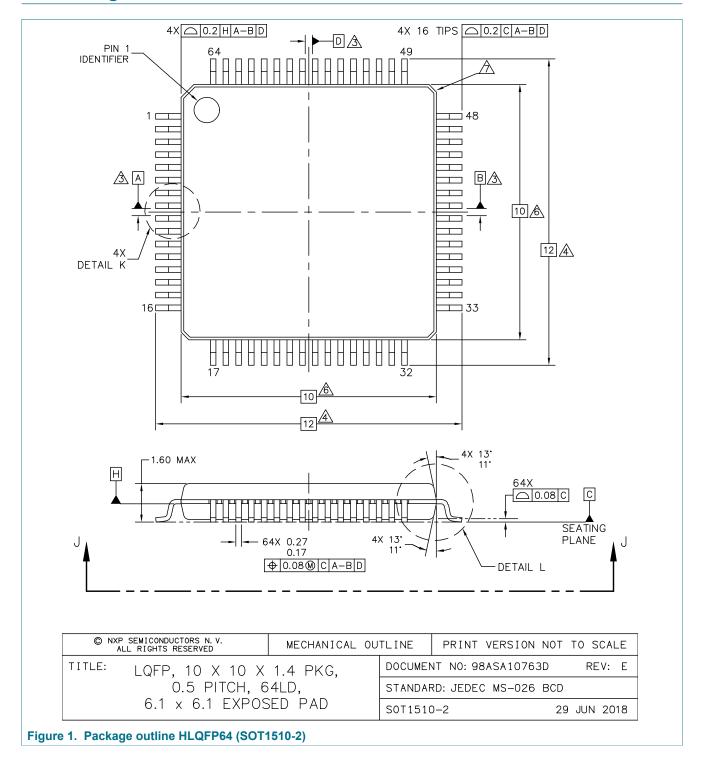
Table 1. Package summary

Parameter	Min	Nom	Max	Unit
package length	-	10	-	mm
package width	-	10	-	mm
package height	-	1.4	-	mm
nominal pitch	-	0.5	-	mm
actual quantity of termination	-	64	-	

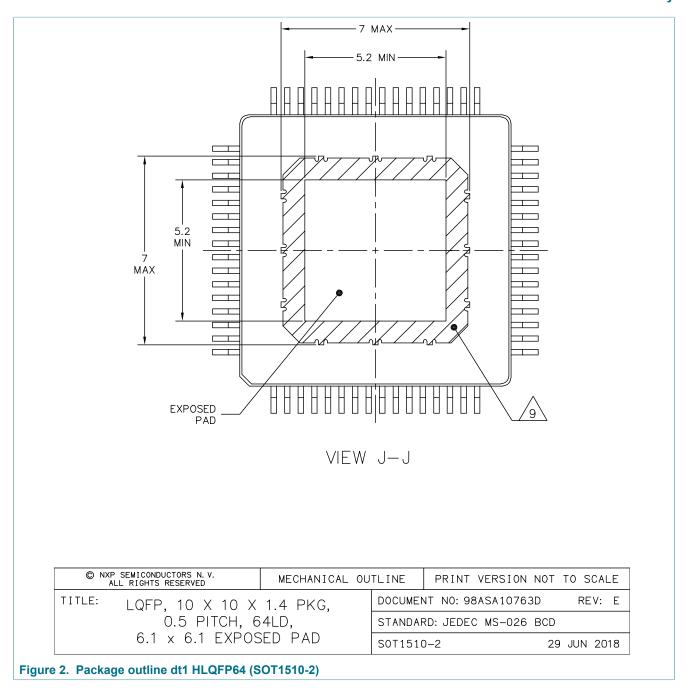


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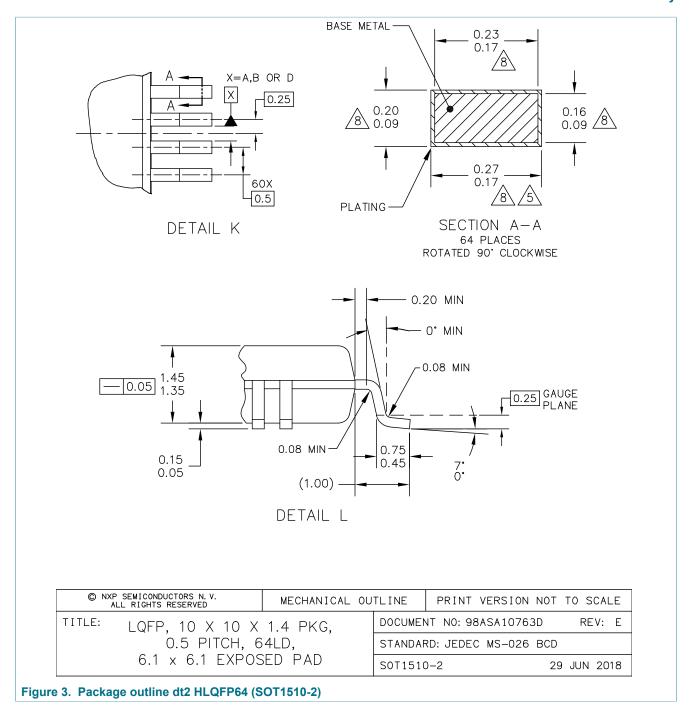
# 2 Package outline



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#### NOTES:

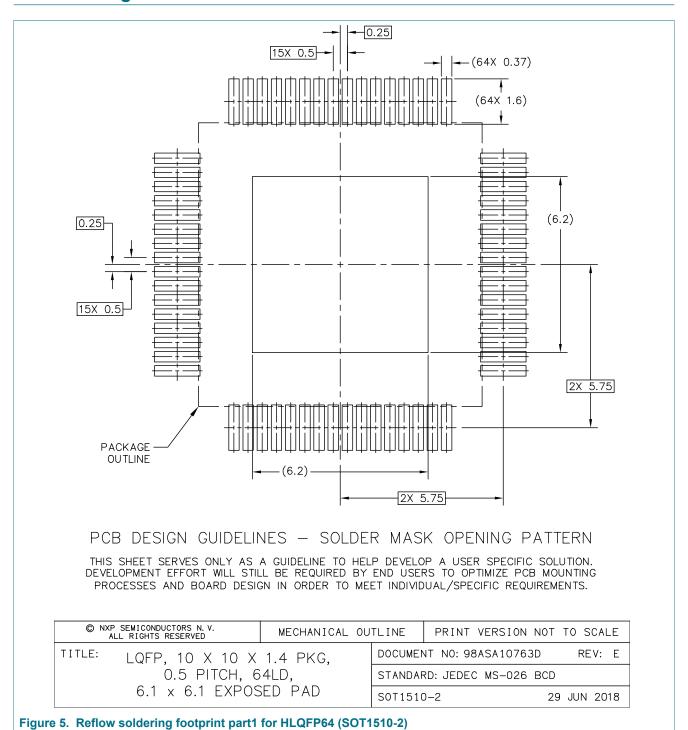
- 1. DIMENSIONS ARE IN MILLIMETERS.
- 2. INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M-1994.
- A DATUMS A, B AND D TO BE DETERMINED AT DATUM PLANE H.
- A. DIMENSIONS TO BE DETERMINED AT SEATING PLANE C.
- DIMENSION DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL NOT CAUSE THE LEAD WIDTH TO EXCEED THE MAXIMUM DIMENSION BY MORE THAN 0.08 MM. DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OR THE FOOT. MINIMUM SPACE BETWEEN PROTRUSION AND ADJACENT LEAD OR PROTRUSION 0.07 MM.
- DIMENSIONS DO NOT INCLUDE MOLD PROTRUSION. ALLOWABLE PROTRUSION IS 0.25 MM PER SIDE. DIMENSIONS ARE MAXIMUM PLASTIC BODY SIZE DIMENSIONS INCLUDING MOLD MISMATCH.
- A EXACT SHAPE OF EACH CORNER IS OPTIONAL.
- THESE DIMENSIONS APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN 0.10 MM AND 0.25 MM FROM THE LEAD TIP.
- A HATCHED AREA REPRESENTS POSSIBLE MOLD FLASH ON EXPOSED PAD.
- KEEP OUT ZONE REPRESENTS AREA ON PCB THAT MUST NOT HAVE ANY EXPOSED METAL (EG. TRACE/VIA) FOR PCB ROUTING DUE TO THE POSSIBILITY OF SHORTING TO TIE BAR/EXPOSED PAD.

© NXP SEMICONDUCTORS N. V. ALL RIGHTS RESERVED	MECHANICAL OUTLINE		PRINT VERSION N	IOT TO SCALE
TITLE: LQFP, 10 X 10 X 1.4 PKG, 0.5 PITCH, 64LD,		DOCUMEN	NT NO: 98ASA10763D	REV: E
		STANDARD: JEDEC MS-026 BCD		
6.1 x 6.1 EXP	SED PAD	S0T1510	0–2	29 JUN 2018

Figure 4. Package outline note HLQFP64 (SOT1510-2)

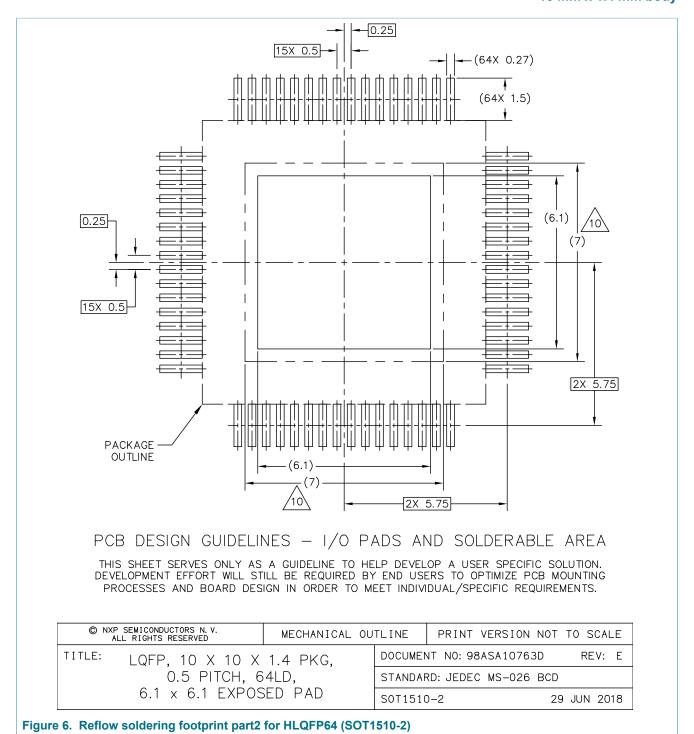
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### 3 Soldering



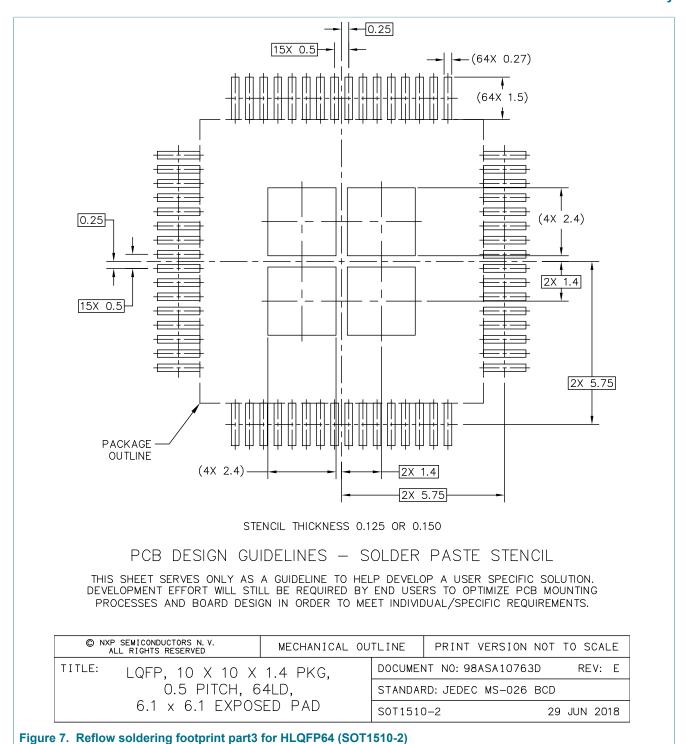
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