

# SOT1172-1

plastic thermal enhanced thin shrink small outline package; 28 leads; body width 4.4 mm; lead pitch 0.65 mm; exposed die pad

8 February 2016

Package information

## 1. Package summary

Terminal position code D (double)

Package type descriptive code HTSSOP28

Package type industry code HTSSOP28

Package style descriptive code HTSSOP (thermal enhanced thin shrink small

outline package)

Package style suffix code NA (not applicable)

Package body material type P (plastic)

IEC package outline code --JEDEC package outline code MO-153

JEITA package outline code ---

Mounting method type S (surface mount)

**Issue date** 31-3-2010

#### **Table 1. Package summary**

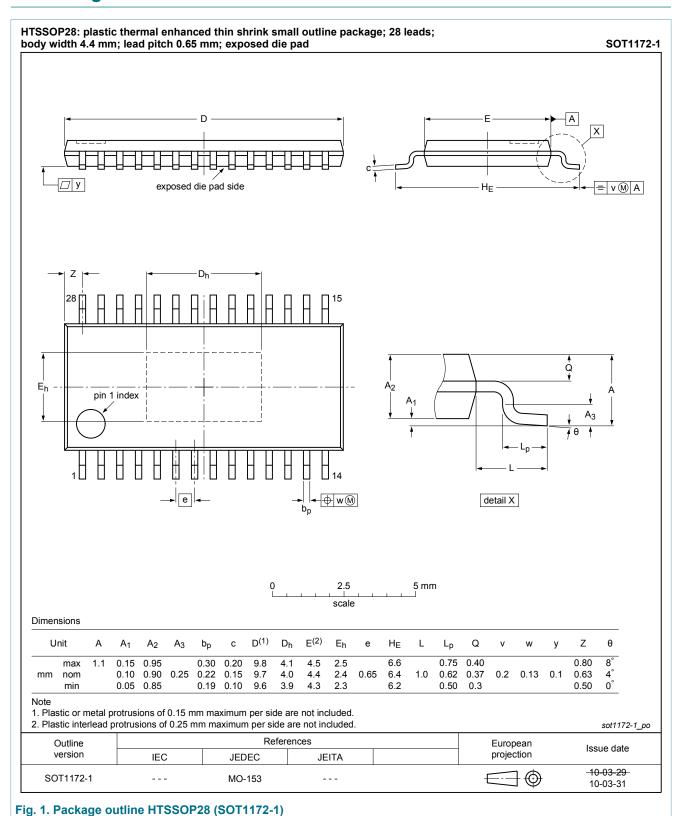
Symbol	Parameter	Min	Тур	Nom	Max	Unit
D	package length	9.6	-	9.7	9.8	mm
E	package width	4.3	-	4.4	4.5	mm
Α	seated height	[tbd]	-	[tbd]	1.1	mm
A <sub>2</sub>	package height	0.85	-	0.9	0.95	mm
е	nominal pitch	-	-	0.65	-	mm
n <sub>2</sub>	actual quantity of termination	-	-	28	-	



NXP Semiconductors SOT1172-1

plastic thermal enhanced thin shrink small outline package; 28 leads; body width 4.4 mm; lead pitch 0.65 mm; exposed die pad

# 2. Package outline



SOT1172-1

NXP Semiconductors SOT1172-1

plastic thermal enhanced thin shrink small outline package; 28 leads; body width 4.4 mm; lead pitch 0.65 mm; exposed die pad

### 3. Legal information

#### **Disclaimers**

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

NXP Semiconductors SOT1172-1

plastic thermal enhanced thin shrink small outline package; 28 leads; body width 4.4 mm; lead pitch 0.65 mm; exposed die pad

### 4. Contents

1.	Package summary	. 1
2.	Package outline	2
3.	Legal information	3
© N	XP Semiconductors N.V. 2016. All rights reserved	_
For	more information, please visit: http://www.nxp.com sales office addresses, please send an email to: salesaddresses@nxp.com a of release: 8 February 2016	

4/4