

Package information

Package summary 1

Terminal position code Q (quad) **HVQFN36** Package type descriptive code

HVQFN (thermal enhanced very thin quad Package style descriptive code

flatpack; no leads)

Package body material type P (plastic)

Mounting method type S (surface mount)

Issue date 29-05-2018

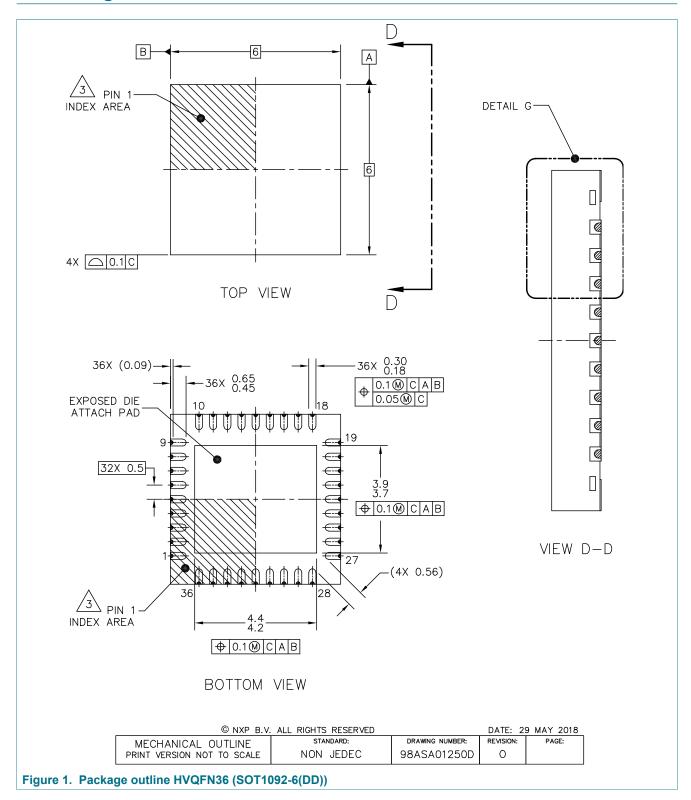
98ASA01250D Manufacturer package code

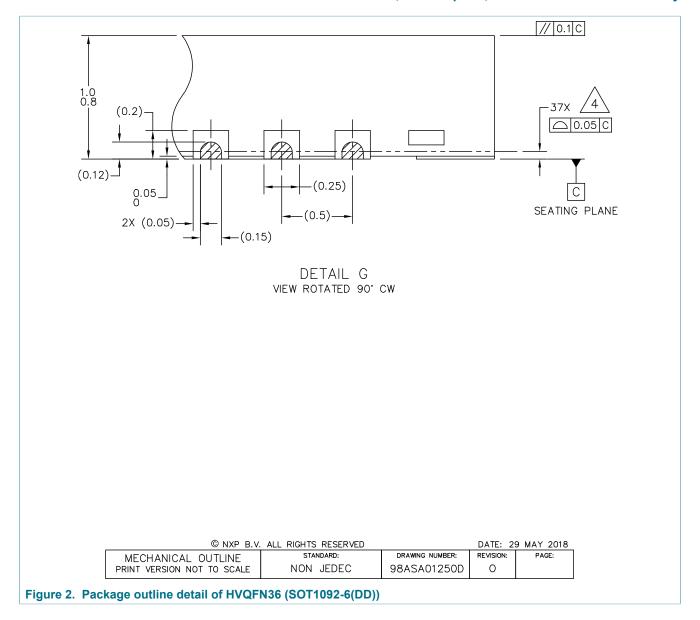
Table 1. Package summary

Parameter	Min	Nom	Max	Unit
package length	-	6	-	mm
package width	-	6	-	mm
package height	-	0.85	-	mm
nominal pitch	-	0.5	-	mm
actual quantity of termination	-	36	-	



2 Package outline





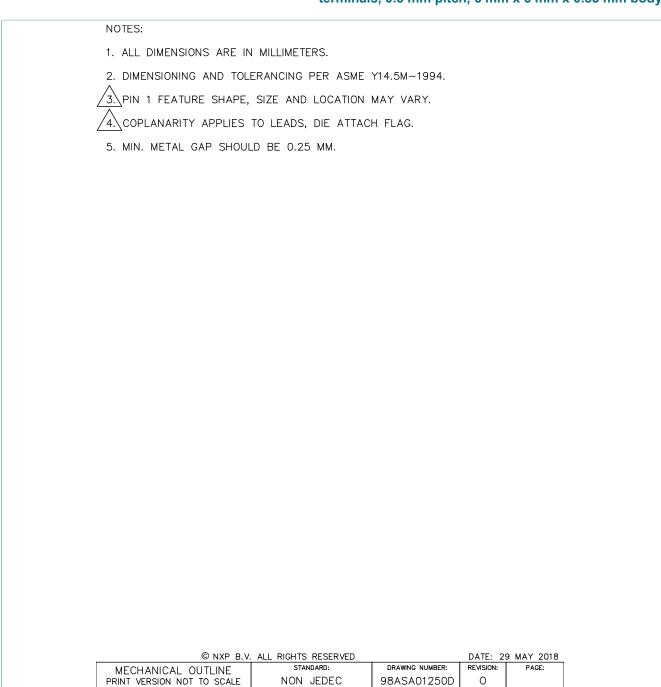


Figure 3. Package outline note of HVQFN36 (SOT1092-6(DD))

NON JEDEC

98ASA01250D

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3 Soldering

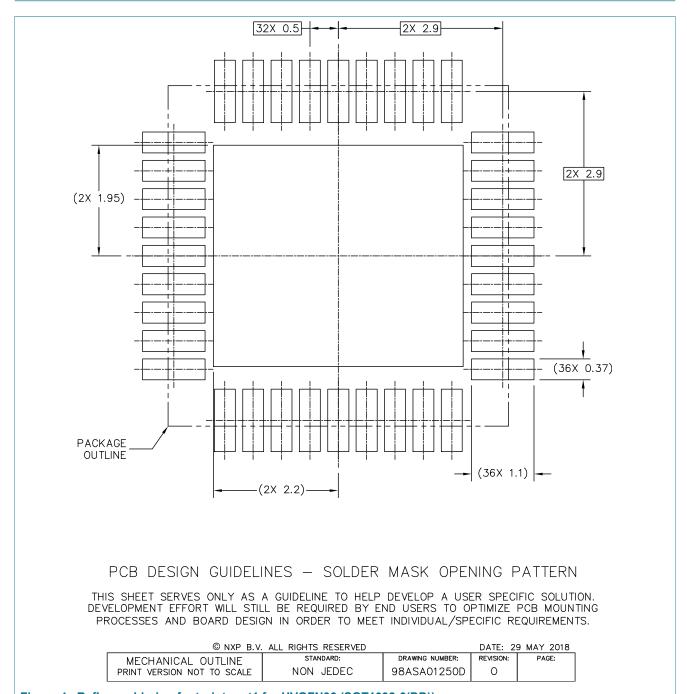
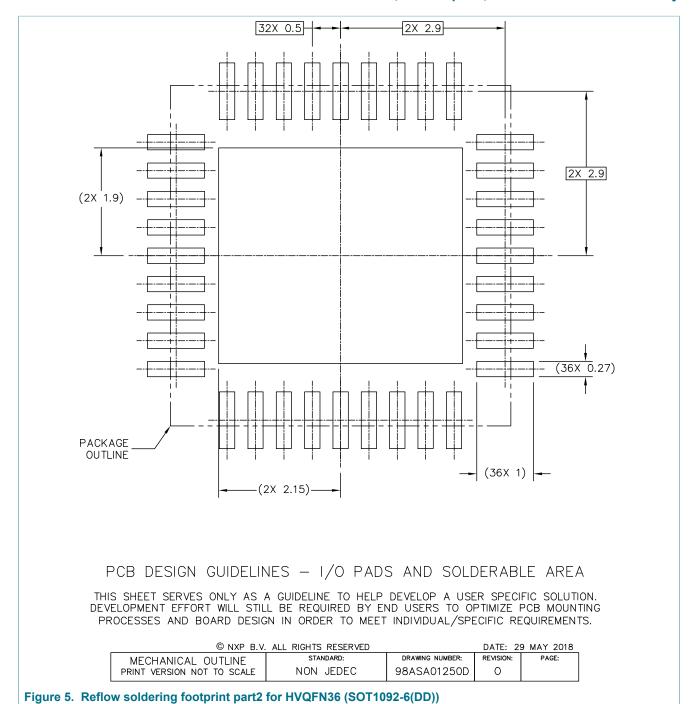


Figure 4. Reflow soldering footprint part1 for HVQFN36 (SOT1092-6(DD))



SOT1092-6(DD)

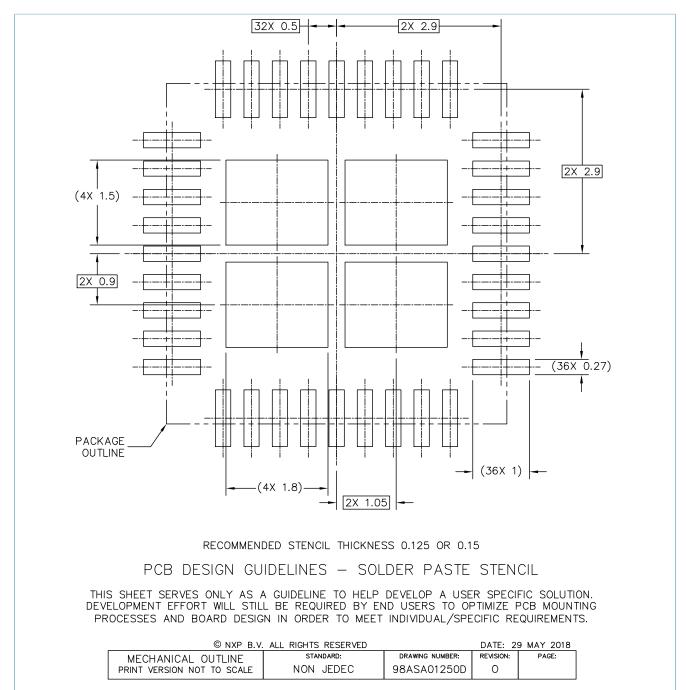


Figure 6. Reflow soldering footprint part3 for HVQFN36 (SOT1092-6(DD))

4 Legal information

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SOT1092-6(DD)

HVQFN36, plastic thermal enhanced very thin quad flat package; no leads; dimple wettable flank; 36 terminals, 0.5 mm pitch, 6 mm x 6 mm x 0.85 mm body

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