
Introduction

This mask set errata applies to this 908JL8 MCU mask set:

- 2L39J

MCU Device Mask Set Identification

The mask set is identified by a 5-character code consisting of a version number, a letter, two numerical digits, and a letter, for example 2L39J. All standard devices are marked with a mask set number and a date code.

MCU Device Date Codes

Device markings indicate the week of manufacture and the mask set used. The date is coded as four numerical digits where the first two digits indicate the year and the last two digits indicate the work week. For instance, the date code “0301” indicates the first week of the year 2003.

MCU Device Part Number Prefixes

Some MCU samples and devices are marked with an SC, PC, or XC prefix. An SC prefix denotes special/custom device. A PC prefix indicates a prototype device which has undergone basic testing only. An XC prefix denotes that the device is tested but is not fully characterized or qualified over the full range of normal manufacturing process variations. After full characterization and qualification, devices will be marked with the MC or SC prefix.

I/O Port Function on OSC2/RCCLK/PTA6/KBI6 Pin

SE65-IO_PORT

An anomaly has been found with the OSC2/RCCLK/PTA6/KBI6 pin when:

- RC oscillator option is selected (OSCSEL = 0) and the pin is selected as PTA6 (PTA6EN = 1).

Under this condition, the PTA6 pin can only function as an output pin; not an input/output pin as stated in the device data sheet.

Because PTA6 is output only:

- The DDRA6 bit has no effect on PTA6.
- The PTAPUE6 bit must not be set to logic 1 to avoid possible current drain if DDRA6 is logic 0.
- The KBIE6 bit has no effect, as keyboard interrupt, KBI6, is not available.

To maintain compatibility with future silicon, the DDRA6 bit should be set to a logic 1 if PTA6 is used as an output port pin.

Minimum Crystal Frequency

SE66-CRYSTAL

A longer than normal start-up time has been identified when the crystal oscillator option is selected (OSCSEL = 1).

When a less than 4-MHz crystal is used, the time from V_{DD} applied to the MCU to crystal frequency stabilization is longer than the 4096 ICLK cycles stated in the data sheet. This crystal stabilization time also depends on the crystal being used, as crystal start-up times vary depending on manufacturers (consult the crystal manufacturers data sheet).

Although this longer start-up time does not cause any known anomaly to the MCU, it is recommended that if a less than 4-MHz crystal is used, the reset pin, \overline{RST} , should be held low for a longer period to allow for the crystal to stabilize.



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