



Preliminary

Release Notes

MC68EZ328 - DragonBall™-EZ Maskset 1J83G

This document covers errata information which relates to the maskset 1J83G of the DragonBall-EZ MC68EZ328.

1. LCD Panning function in gray display mode does not work properly when display data are not stored in DRAM. (1/20/99)

If screen panning function is used in gray-level display mode, please use DRAM for video data storage. If the video data is stored in SRAM/ROM, panning function works only in black/white display mode.


2. OVRUN Status Bit in UART Receiver Register does not function. (1/20/99)

Bit 11 in UART Receiver Register (0xFFF904) does not function. This status bit will not be active when overflow occurs in receiver FIFO. The s/w workaround is to implement a counter in the transmission process to confirm the number of data bytes received.

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