

# ES\_PCA9460

## IC Errata for the PCA9460 A2 silicon

Rev. 1.0 — 26 November 2025

Errata

## 1 Product identification

This errata document applies to PCA9460 product family.

Table 1. Orderable part number identification

Part number	Version	Die ID REV_ID[3:0]	Package	IC marking	FabAssyDate Code
PCA9460AUK	A1 Silicon	0x00	2.86 x 2.46	N9460A	
PCA9460BUK	A1 Silicon	0x00	2.86 x 2.46	N9460B	
PCA9460CUK	A1 Silicon	0x00	2.86 x 2.46	N9460C	

### 1.1 Device part number prefixes

Device samples marked with a P pre-fix indicate prototype. These devices have undergone basic testing only and are not considered qualified. Any other device pre-fix indicates the product is in production, has full characterization, qualification and testing has been performed, unless otherwise noted.

### 1.2 Device build information / date code

The marked trace code is the link between the physically marked materials and the manufacturing lot's system genealogy information. Once the connection between the marked material and system genealogy information is made, traceability reports provide the material's manufacturing/shipping history. All devices listed in the Errata are affected unless specific date codes are provided below.

## 2 Errata overview

Table 2. Functional problem table

Functional problem	Short description	Severity level <sup>[1][2][3][4]</sup>	Solution	Detailed description
ER1	PCA9460 start up issue when there is VSYS residual voltage above of 200mV.	Low	Ensure VSYS fully discharges upon power-off. At power-up, validate that the residual voltage on VSYS does not exceed 200 mV to ensure proper system initialization and prevent latch-up conditions.	<a href="#">Section 3.1</a>

[1] High: Failure mode that severely inhibits the use of the device for all or a majority of intended applications.

[2] Medium: Failure mode that might restrict or limit the use of the device for all or a majority of intended applications.

[3] Low: Unexpected behavior that does not cause significant problems for the intended applications of the device.

[4] Enhancement: Improvement made to the device due to previously found issues on the design.



### 3 Functional problem detail

#### 3.1 ER1: PCA9460 start up issue when there is VSYS residual voltage above 200mV

##### 3.1.1 Severity level

Low

##### 3.1.2 Problem

When VSYS is applied with a residual voltage exceeding 200 mV, the internal LDO responsible for digital supply (VINT) may fail to initialize, resulting in an incomplete or failed power-up sequence.

If the system is designed to cycle power off and on within a short interval, without permitting VSYS to fall below 200 mV, the PCA9460 may experience an incorrect or incomplete power-up sequence.

##### 3.1.3 Work-around

Ensure VSYS fully discharges upon power-off. At power-up, validate that the residual voltage on VSYS does not exceed 200 mV to ensure proper system initialization and prevent latch-up conditions.

##### 3.1.4 Fix plan

New silicon revision, new part numbers:

- PCA9460**AA**
- PCA9460**BA**
- PCA9460**CA**

### 4 Revision history

Document ID	Release date	Description
ES_PCA9460 v.1.0	26 November 2025	Initial version (CIN 202507030I)

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