

ES_P89LPC9321

Errata sheet P89LPC9321

Rev. 02 — 9 March 2010

Errata sheet

Document information

Info	Content
Keywords	P89LPC9321 errata
Abstract	<p>This errata sheet describes both the known functional problems and any deviations from the electrical specifications known at the release date of this document.</p> <p>Each deviation is assigned a number and its history is tracked in a table at the end of the document.</p>



Revision history

Rev	Date	Description
02	20100309	<ul style="list-style-type: none">The format of this errata sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors.Removed DIVM.1
01	20081203	Initial version.

Contact information

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1. Product identification

The P89LPC9321 devices typically have the following top-side marking:

```
P89LPC9321x x
xxxxxxx xx
xxYYWW R
```

The last letter in the last line (field 'R') will identify the device revision. This Errata Sheet covers the following revisions of the P89LPC9321:

Table 1. Device revision table

Revision identifier (R)	Revision description
'L'	Initial device revision

Field 'YY' states the year the device was manufactured. Field 'WW' states the week the device was manufactured during that year.

2. Errata overview

Table 2. Functional problems table

Functional problems	Short description	Revision identifier
-	-	-

Table 3. AC/DC deviations table

AC/DC deviations	Short description	Revision identifier
-	-	-

Table 4. Errata notes

Note	Short description	Revision identifier
-	-	-

3. Functional problems detail

No known errata

4. AC/DC deviations detail

No known errata

5. Legal information

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Date of release: 9 March 2010

Document identifier: ES_P89LPC9321_2