ERRATA SHEET

Date: **2009 Feb 06**
Document Release: **Version 1.0**
Device Affected: **P89CV51Rx2**

This errata sheet describes both the known functional problems and any deviations from the electrical specifications known at the release date of this document.

Each deviation is assigned a number and its history is tracked in a table at the end of the document.

NXP
Semiconductors

founded by Philips

Document revision history

Rev	Date	Description
1.0	2009 Feb 06	First version

Identification

The typical P89CV51Rx2 devices have the following top-side marking:

P89CV51Rx2 xx

xxxxxxx

xxxYYWWVV

The last letter in the third line (field 'VV') will identify the device revision. This Errata Sheet covers the following revisions of the P89CV51Rx2:

Revision Identifier (VV)	Comment
'B2'	Initial device revision
'B3'	Second device revision

Field 'YY' states the year the device was manufactured. Field 'WW' states the week the device was manufactured during that year.

Errata Overview - Functional Problems

Functional Problem	Short Description	Device Revision the problem occurs in
AUXR.1	AUXR register reset value = 0x02 (EXTRAM = “1”) instead of 0x00	B2

Errata Overview - AC/DC Deviations

AC/DC Deviation	Short Description	Device Revision the deviation occurs in
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Errata Notes

Notes	Short Description	Device Revision the note applies to
-	-	-

Functional Problems of P89CV51Rx2

AUXR.1 Auxiliary Function Register (at Memory Location 0x8E) Bit 1 (EXTRAM)

Introduction: External Memory cannot be accessed if EXTRAM bit = 1

Problem: During power on reset, the EXTRAM bit is set to “1”

Work around: To access external memory, software modification is required to set AUXR register to 0.