Mask Set Errata 3

MC68HC711E9 8-Bit Microcontroller Unit

INTRODUCTION

This mask set errata provides information pertaining to the V_{PPE} pin programming current sensitivity applicable to this MC68HC711E9 MCU mask set device:

- H50W

MCU DEVICE MASK SET IDENTIFICATION

The mask set is identified by a 5-character code consisting of a version number, a letter, two numerical digits, and a letter, for example 3J74Y. Slight variations to the mask set identification code may result in an altered version number, for example 4J74Y.

MCU DEVICE DATE CODES

Device markings indicate the week of manufacture and the mask set used. The data is coded as four numerical digits where the first two digits indicate the year and the last two digits indicate the work week. For instance, the date code “9915” indicates the 15th week of the year 1999.

MCU DEVICE PART NUMBER PREFIXES

Some MCU samples and devices are marked with an SC or XC prefix. An SC prefix denotes special/custom device. An XC prefix denotes that the device is tested but is not fully characterized or qualified over the full range of normal manufacturing process variations. After full characterization and qualification, devices will be marked with the MC prefix.

When contacting a Motorola representative for assistance, please have the MCU device mask set and date code information available.

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V_{PPE} PIN PROGRAMMING CURRENT SENSITIVITY

During EPROM programming of the MC68HC711E9 device, the V_{PPE} pin circuitry may be damaged if the input current is not limited to 10 mA.

Programming the EPROM without an input current limit may destroy the programming functionality. This condition has been experienced on various programming systems (including in-circuit programming) where no current limit on the V_{PPE} pin exists.

Workaround:

It has been found that limiting the current to the $\overline{\text{XIRQ}}/V_{PPE}$ pin with a 1-kΩ resistor can protect the EPROM voltage circuit even when the voltage is bouncing on V_{PPE} and will allow enough current to program the EPROM array.

Additional mask set erratas can be found on the World Wide Web at http://mcu.motsps.com/documentation.