

Mask Set Errata 1

68HC705J1A 8-Bit Microcontroller Unit

INTRODUCTION

This errata provides information regarding short oscillator delay that is applicable to the following 68HC705J1A MCU mask set devices:

- F44T
- G58F
- G63P

MCU DEVICE MASK SET IDENTIFICATION

The mask set is identified by a four-character code consisting of a letter, two numerical digits, and a letter (e.g., F44T). Slight variations to the mask set identification code may result in an optional numerical digit preceding the standard four-character code (e.g., 2F44T).

MCU DEVICE DATE CODES

Device markings indicate the week of manufacture and the mask set used. The data is coded as four numerical digits where the first two digits indicate the year and the last two digits indicate the work week. The date code "9115" would indicate the 15th week of the year 1991.

MCU DEVICE PART NUMBER PREFIXES

Some MCU samples and devices are marked with an "SC" or "XC" prefix. An "SC" prefix denotes special/custom device. An "XC" prefix denotes device is tested but is not fully characterized or qualified over the full range of normal manufacturing process variations. After full characterization and qualification, devices will be marked with the "MC" prefix.

Whenever contacting a Motorola representative for assistance, please have the MCU device mask set and date code information available.

Specifications and information herein are subject to change without notice.



SHORT OSCILLATOR DELAY

SOSCD — Short Oscillator Delay Bit

The SOSCD bit controls the oscillator stabilization counter. The normal stabilization delay following reset or exit from stop mode is $4064 t_{cyc}$. Setting SOSCD enables a short oscillator stabilization delay.


1 = Short oscillator delay enabled

0 = Short oscillator delay disabled

NOTE

MCUs with mask set number F44T have a short delay of $16 t_{cyc}$.

MCUs with the mask set numbers G58F and G63P have a short oscillator delay of $128 t_{cyc}$.

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