

Applications Processors - Hardware Accelerated 2D and 3D Graphics, High-Performance, Low-Power, Arm® Cortex®-A8 Core

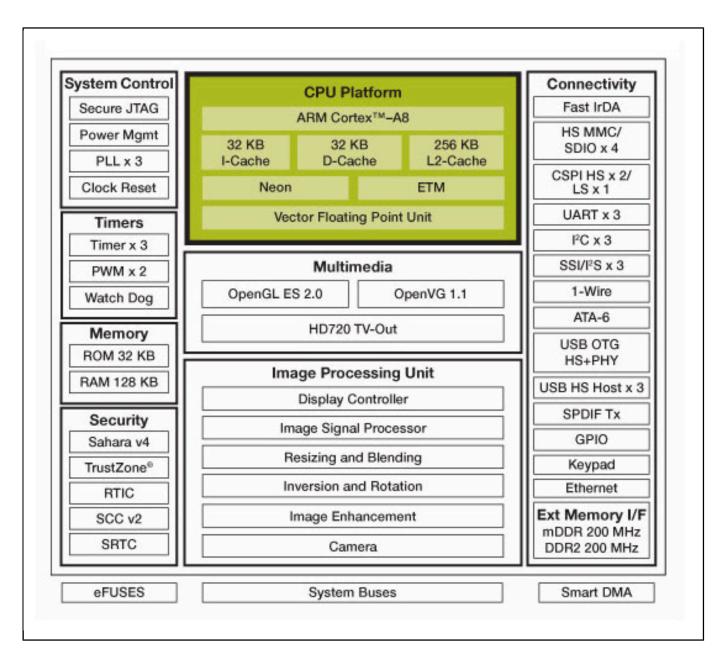
i.MX514

Last Updated: Jan 16, 2023

The i.MX514 multimedia applications processor offers high performance processing optimized for the lowest power consumption combined with hardware accelerated 2D and 3D graphics for navigation, advanced HMI, instrument clusters and telematics. It features Our advanced and power-efficient implementation of the Arm® Cortex®-A8 core, which operates at speeds up to 600 MHz.

The i.MX514 is supported by companion NXP[®] power management ICs (PMIC), MC13892, MC34704, MC34709 and MMPF0100.

For automotive i.MX51 versions, i.MX516 samples are orderable.



i.MX514 Multimedia Applications Processor Block Diagram Block Diagram

View additional information for Applications Processors - Hardware Accelerated 2D and 3D Graphics, High-Performance, Low-Power, Arm® Cortex®-A8 Core.

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.