



Multimedia Applications Processors - Integrated Electronic Paper Display (EPD), High Performance, Arm® Cortex®-A8 Core

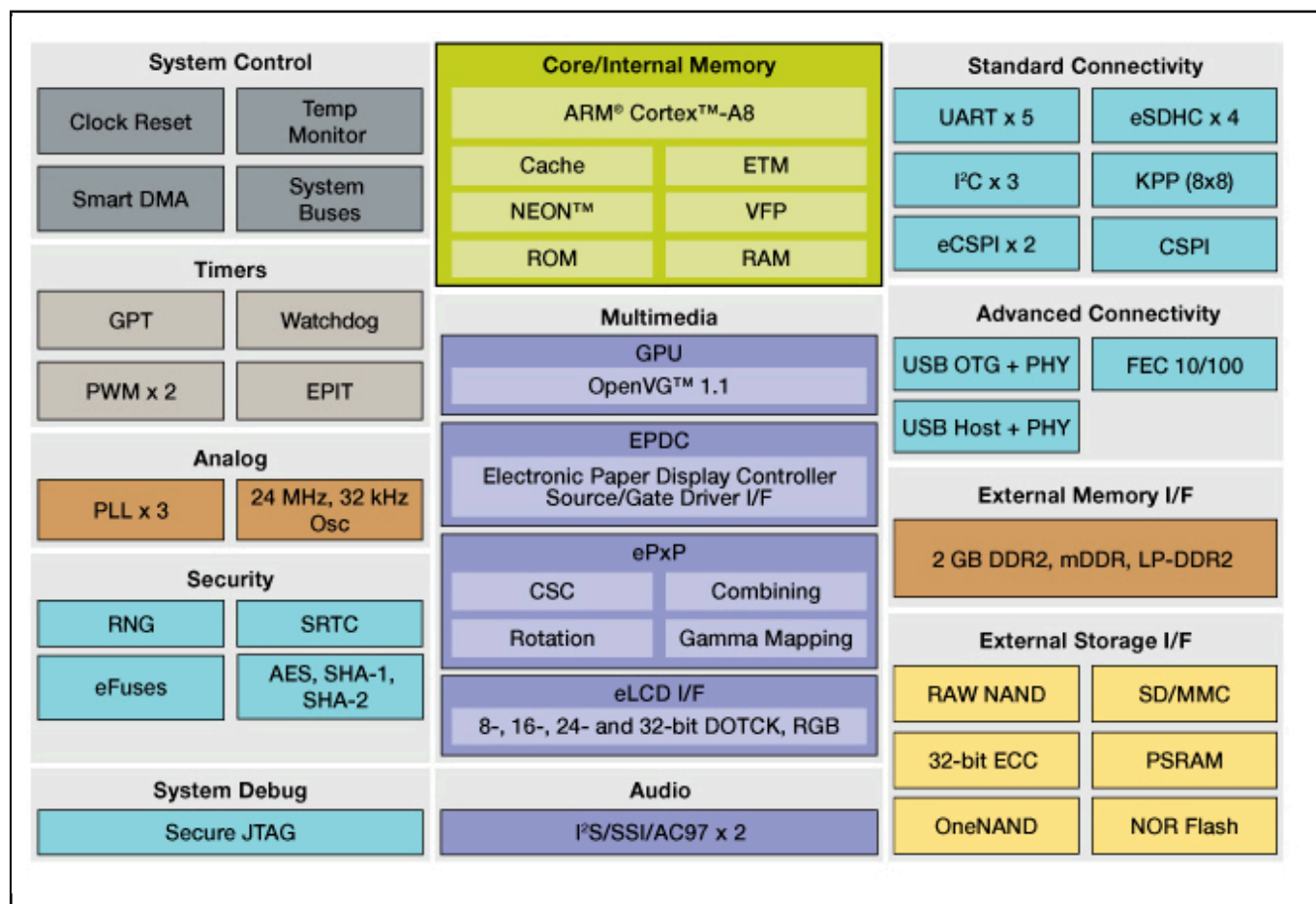
i.MX508

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The i.MX508 processor is the first system-on-chip (SoC) designed specifically for eReaders that incorporates a high performance 800MHz Arm® Cortex®-A8 processor and an integrated hardware Electronic Paper Display (EPD) Controller certified by E Ink® to drive the latest and next generation Pearl™ panels. The i.MX508 enables reduced system cost, longer battery life and higher performance for faster page flips and a better reading experience.

The i.MX508 is supported by a companion NXP® power management IC, MMPF0100.

i.MX508 Multimedia Applications Processor Block Diagram Block Diagram



View additional information for [Multimedia Applications Processors - Integrated Electronic Paper Display \(EPD\), High Performance, Arm® Cortex®-A8 Core](#).

Note: The information on this document is subject to change without notice.

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