



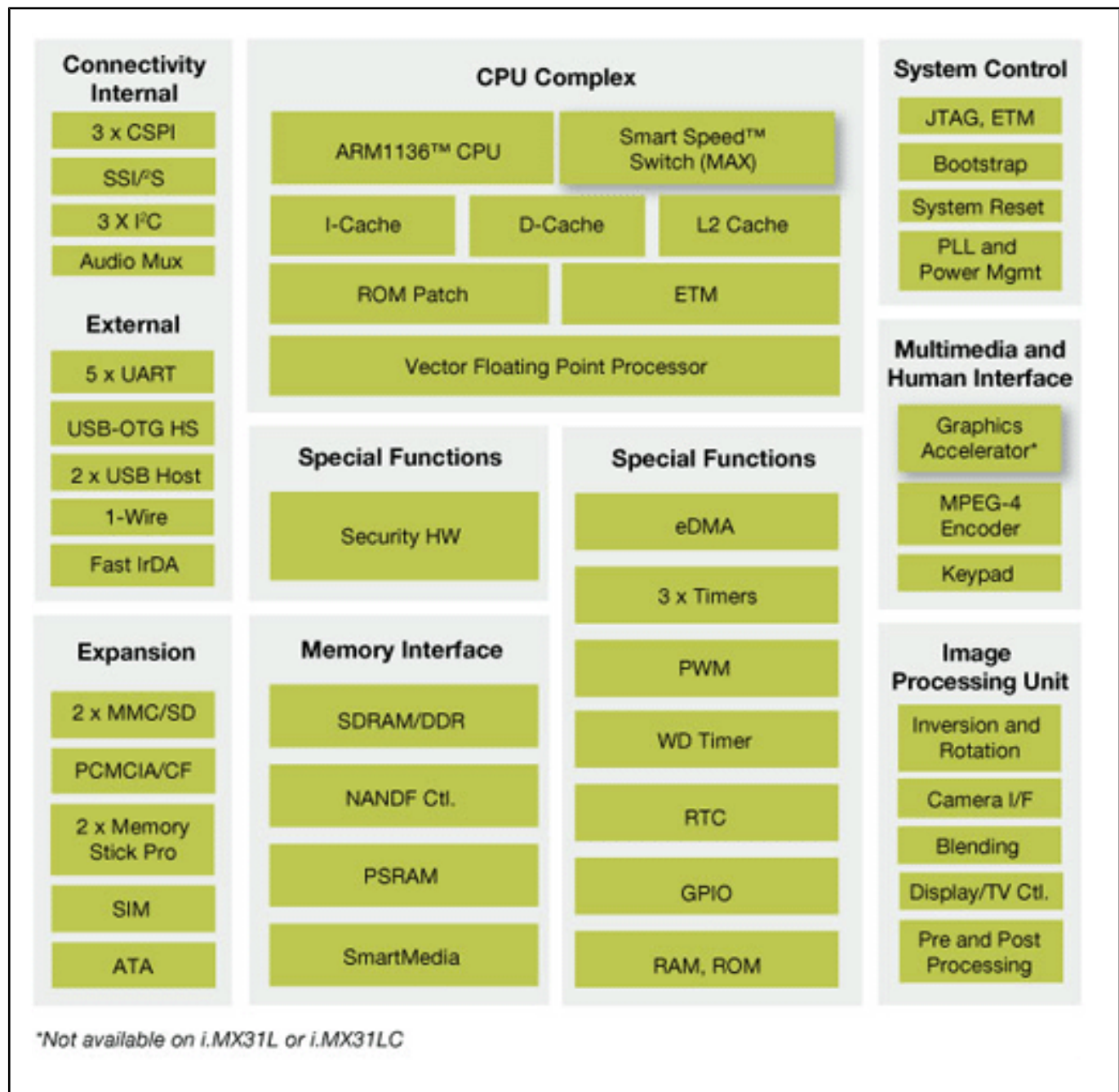
Applications Processors - Integrated Image Processing Unit (IPU), Connectivity , Arm11™ Core

i.MX31

Last Updated: Sep 4, 2025

Millions of i.MX31 processors are powering consumer, industrial, automotive, medical and general embedded devices. With on-chip media accelerators, multiple connectivity interfaces and package options, the i.MX31 and i.MX31L are suitable for a broad spectrum of multimedia oriented applications. As the first in the i.MX portfolio to achieve automotive AEC-Q100 grade 3 qualification, the i.MX31 processor is the engine in the acclaimed Ford SYNC. Design engineers need choices; NXP® delivers.

i.MX31 Multimedia Applications Processor Block Diagram Block Diagram



View additional information for [Applications Processors - Integrated Image Processing Unit \(IPU\)](#), [Connectivity](#) , [Arm11™ Core](#).

Note: The information on this document is subject to change without notice.

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2025 NXP B.V.