Mini High-Speed CAN System Basis Chip with Standby/Sleep Mode and Watchdog

UJA1167ATK

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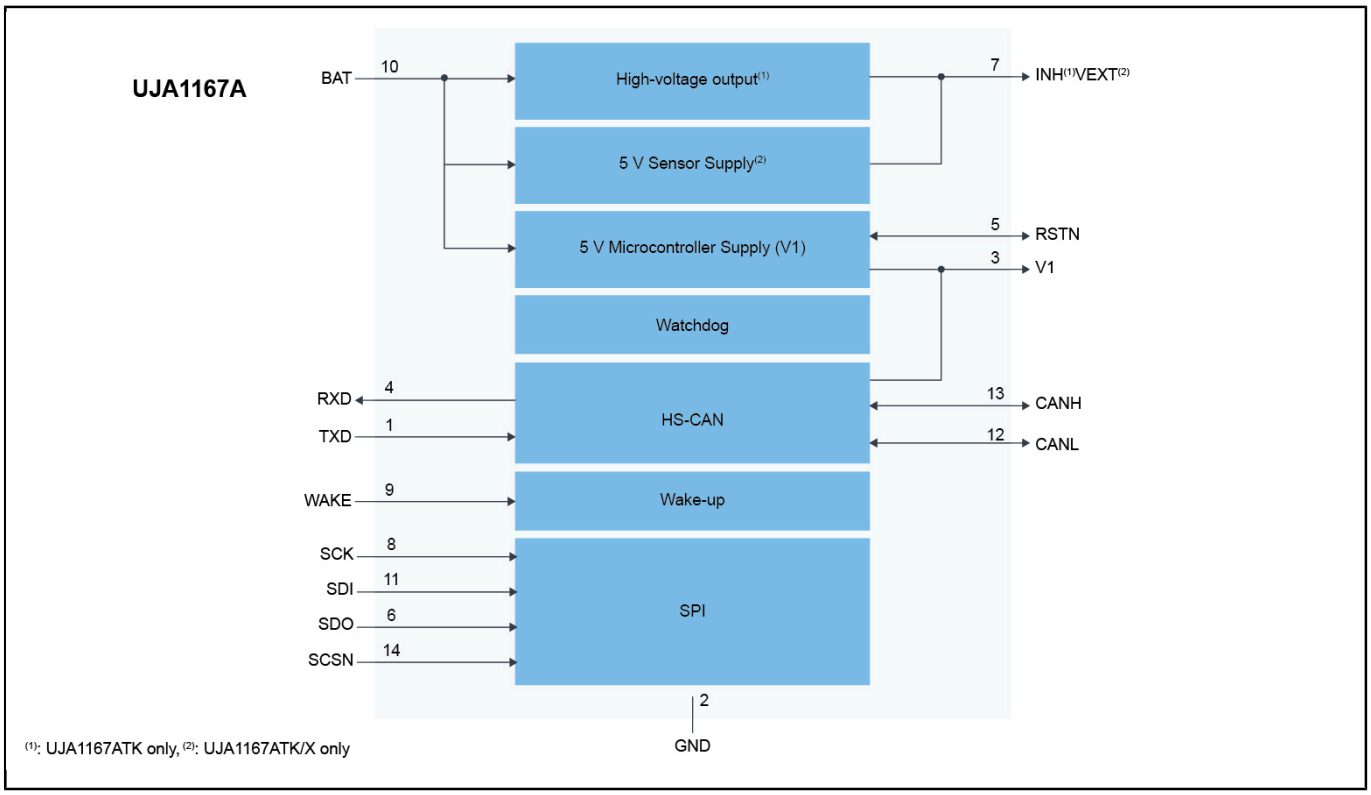
The UJA1167ATK is a mini high-speed CAN system basis chip (SBC) containing an ISO 11898-2:2016 and SAE J2284-1 to SAE J2284-5 compliant HS-CAN transceiver supporting CAN FD up to 5 Mbit/s and an integrated 5 V/100 mA supply for a microcontroller.

It features a watchdog and a serial peripheral interface (SPI). The UJA1167ATK can be operated in very-low-current standby and sleep modes with bus and local wake-up capability and supports ISO 11898-2:2016 compliant autonomous CAN biasing. The microcontroller supply is switched off in sleep mode.

A number of configuration settings are stored in non-volatile memory, allowing the SBC to be adapted for use in a specific application.

UJA1167ATK Mini SBC Block Diagram Block Diagram



View additional information for Mini High-Speed CAN System Basis Chip with Standby/Sleep Mode and Watchdog.

Note: The information on this document is subject to change without notice.