



Mini High-Speed CAN System Basis Chip with Standby Mode

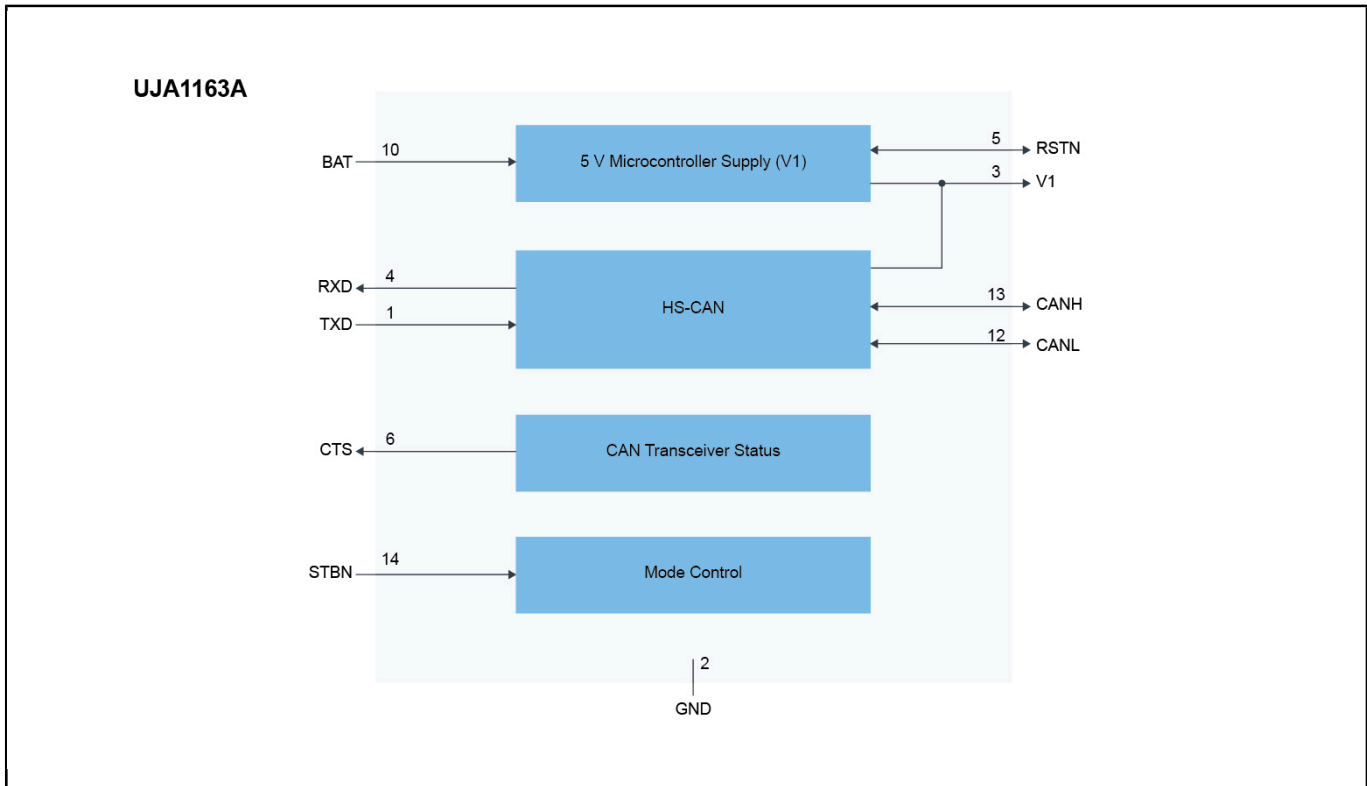
UJA1163ATK

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The UJA1163ATK is a mini high-speed CAN system basis chip (SBC) containing an ISO 11898-2:2016 and SAE J2284-1 to SAE J2284-5 compliant HS-CAN transceiver, supporting CAN FD up to 5 Mbit/s and an integrated 5 V/100 mA supply for a microcontroller.

The UJA1163ATK can be operated in very-low-current standby mode with bus wake-up capability and supports ISO 11898-6 compliant autonomous CAN biasing.

UJA1163ATK Mini SBC Block Diagram Block Diagram



View additional information for [Mini High-Speed CAN System Basis Chip with Standby Mode](#).

Note: The information on this document is subject to change without notice.

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