



I²C-Bus to SPI Bridge

SC18IS602B

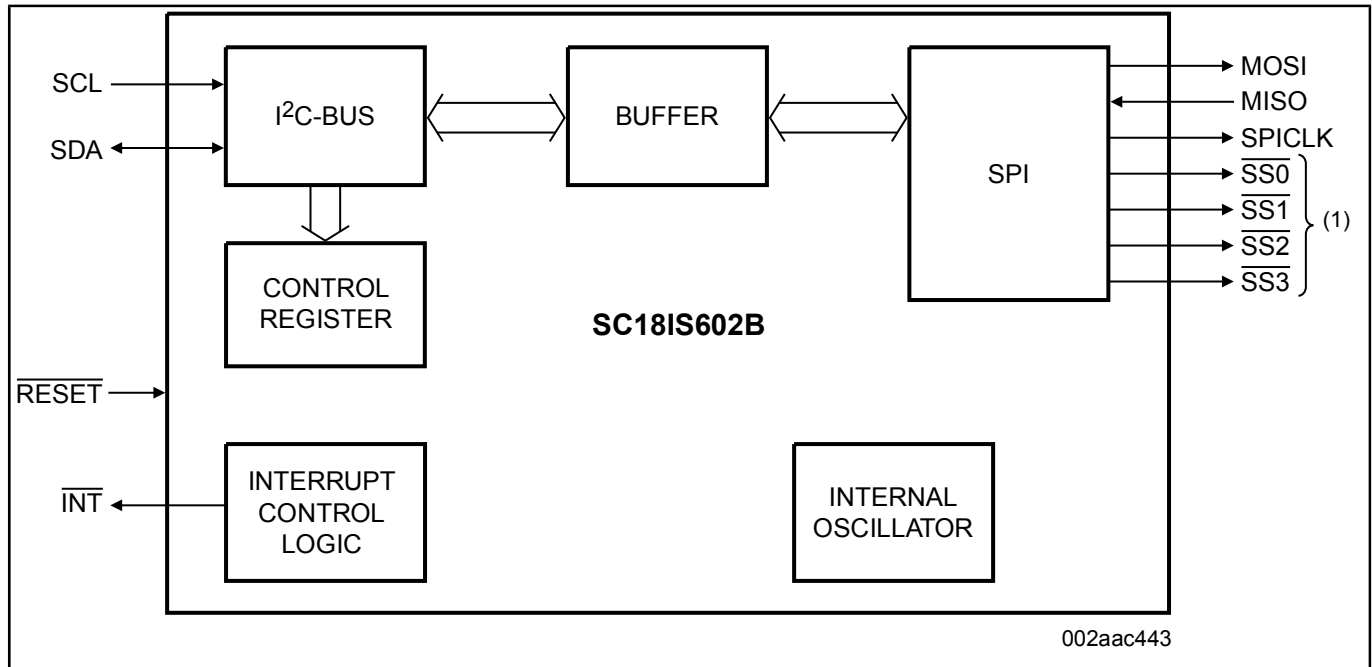
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The SC18IS602B is designed to serve as an interface between a standard I²C-bus of a microcontroller and an SPI bus. This allows the microcontroller to communicate directly with SPI devices through its I²C-bus. The SC18IS602B operates as an I²C-bus target-transmitter or target-receiver and an SPI leader. The SC18IS602B controls all the SPI bus-specific sequences, protocol, and timing. The SC18IS602B has its own internal oscillator, and it supports four SPI chip select outputs that may be configured as GPIO when not used.

SC18IS602B Block Diagram



View additional information for [I²C-Bus to SPI Bridge](#).

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