



# SPI to I<sup>2</sup>C-Bus Interface

## SC18IS600

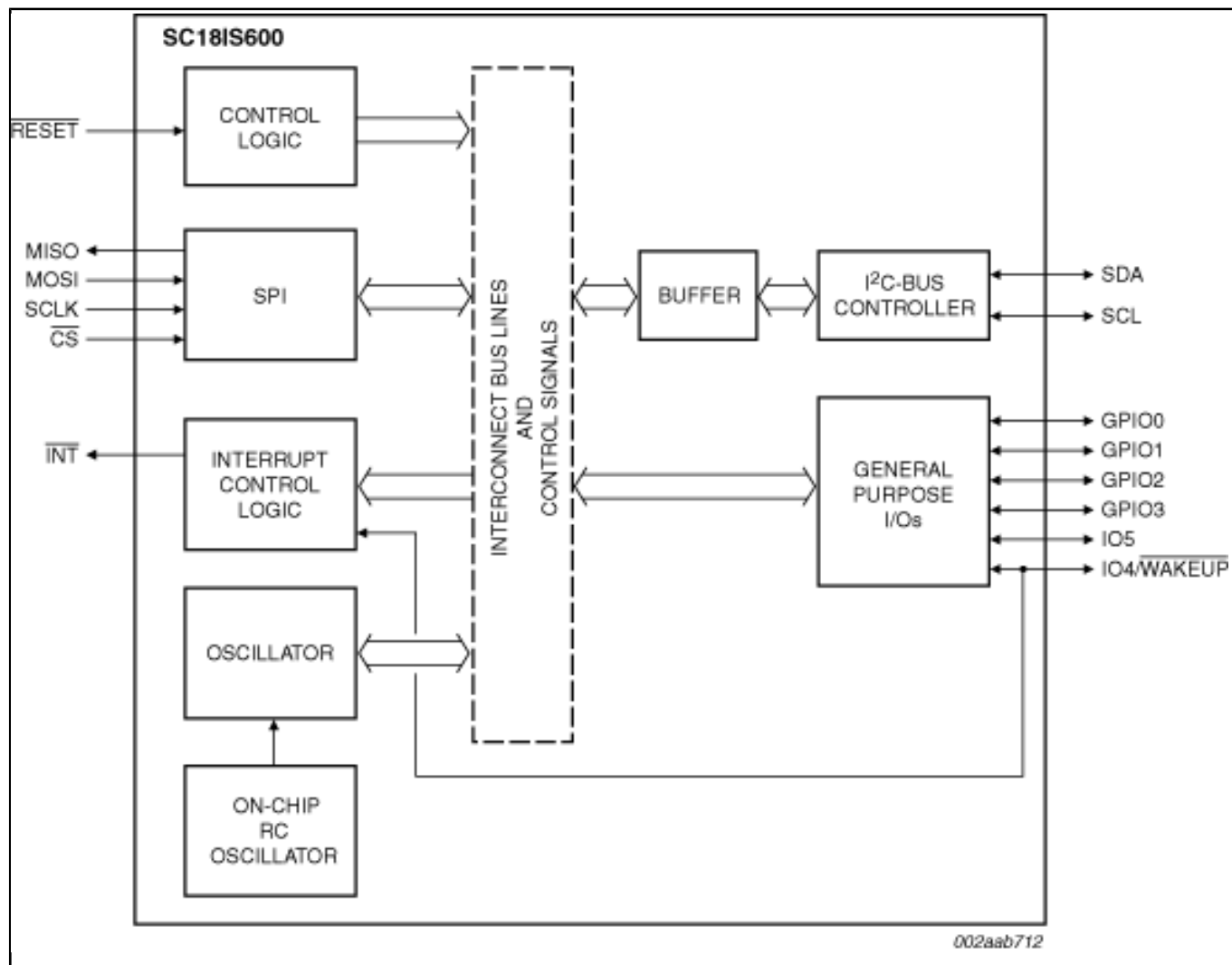
### Archived

このページには、製造中止（生産終了）となった製品の情報が記載されています。本ページに記載されている仕様および情報は、過去の参考情報です。

Last Updated: Sep 13, 2024

The SC18IS600 is designed to serve as an interface between the standard SPI of a host (microcontroller, microprocessor, chip set, etc.) and the serial I<sup>2</sup>C-bus. This allows the host to communicate directly with other I<sup>2</sup>C-bus devices. The SC18IS600 can operate as an I<sup>2</sup>C-bus controller-transmitter or controller-receiver. The SC18IS600 controls all the I<sup>2</sup>C-bus specific sequences, protocol, arbitration and timing.

## SC18IS600IPW, SC18IS601IPW Block Diagram



View additional information for [SPI to I<sup>2</sup>C-Bus Interface](#).

**Note:** The information on this document is subject to change without notice.

**www.nxp.com**

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2025 NXP B.V.