



5 V, 3.3 V and 2.5 V Dual UART, 5 Mbit/s (max.), with 16 B FIFOs

SC16C2552BIA44

アーカイブス

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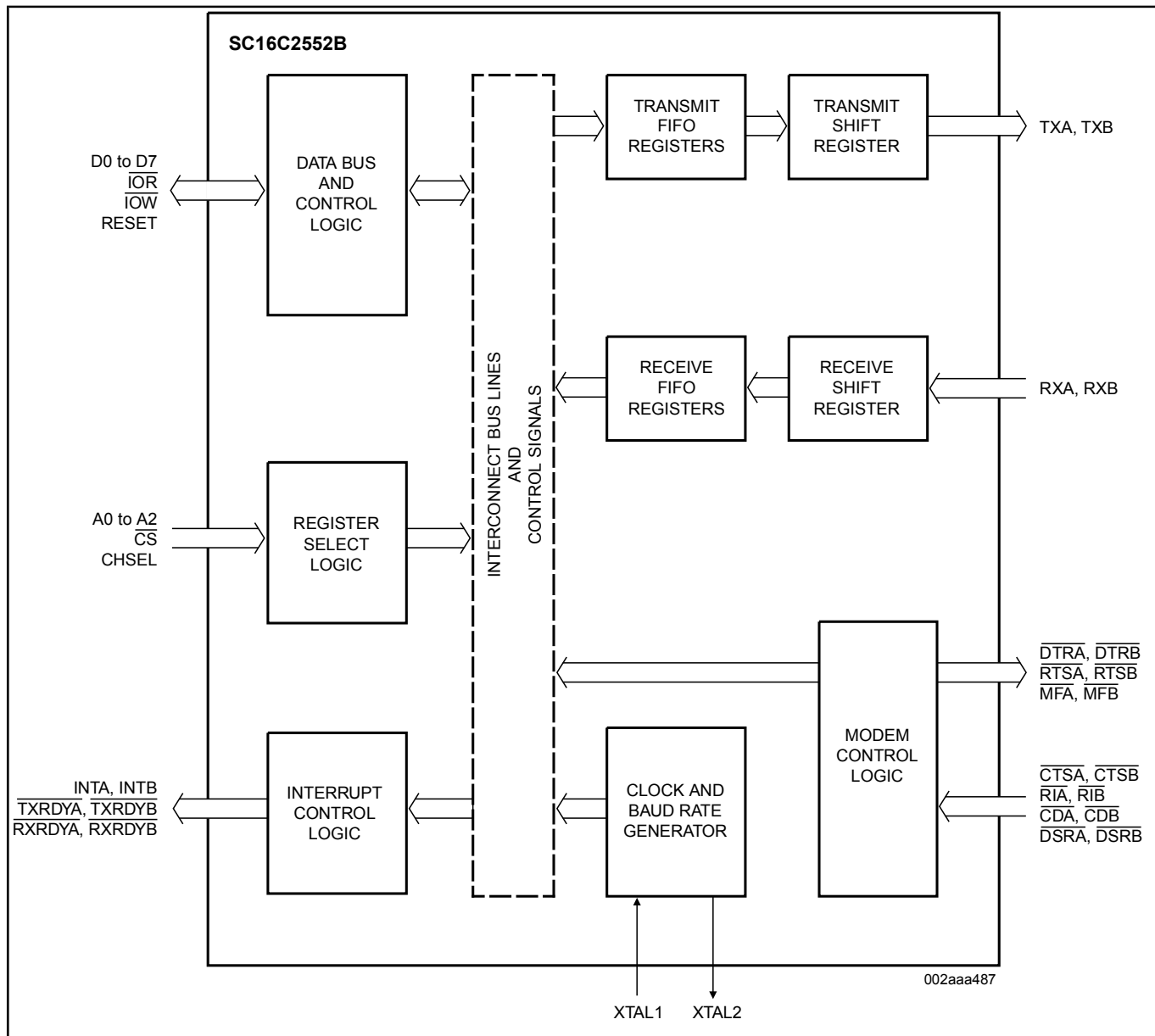
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The SC16C2552B is a two channel Universal Asynchronous Receiver and Transmitter (UART) used for serial data communications. Its principal function is to convert parallel data into serial data and vice versa. The UART can handle serial data rates up to 5 Mbit/s.

The SC16C2552B is pin compatible with the PC16552 and ST16C2552. The SC16C2552B provides enhanced UART functions with 16-byte FIFOs, modem control interface, DMA mode data transfer and concurrent writes to control registers of both channels. The DMA mode data transfer is controlled by the FIFO trigger levels, the RXRDY and TXRDY signals. On-board status registers provide the user with error indications and operational status. System interrupts and modem control features may be tailored by software to meet specific user requirements. An internal loopback capability allows on-board diagnostics. Independent programmable baud rate generators are provided to select transmit and receive baud rates.

The SC16C2552B operates at 5 V, 3.3 V and 2.5 V and the industrial temperature range and is available in a plastic PLCC44 package.

SC16C2552B Block Diagram Block Diagram



View additional information for [5 V, 3.3 V and 2.5 V Dual UART, 5 Mbit/s \(max.\), with 16 B FIFOs.](#)

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