

Safety Processor for the ADAS Domain

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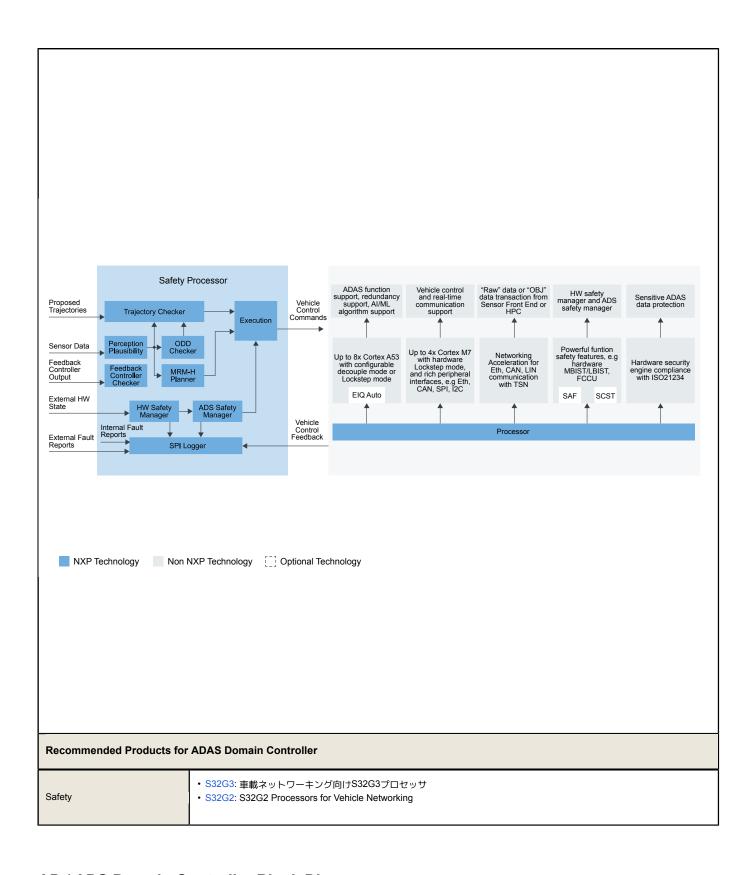
One of the major functions of the AD/ADAS domain controller involve perception and planning. Perception function drives the requirement for very high performance (TOPS) while the subsequent path planning and actuation drives the need for ASIL D safety requirements (DMIPS). ADAS compute may not have ASIL D capabilities, but the ASIL D decision needs to be made in the domain controller. This is achieved by the use of a high-performance ASIL D processor called as safety processor, which acts as a redundant path evaluating the decision made by the ADAS compute.

With advancement of ADAS technology, the implemented level of automation driving becomes increasingly higher. However, a degraded mode to keep basic function of ADAS should be supported if any failure leads to complex ADAS application failure. In this situation, a safety processor is needed, which supports powerful function safety features to detect/handle failure and owns enough resource to support degrade/redundancy.

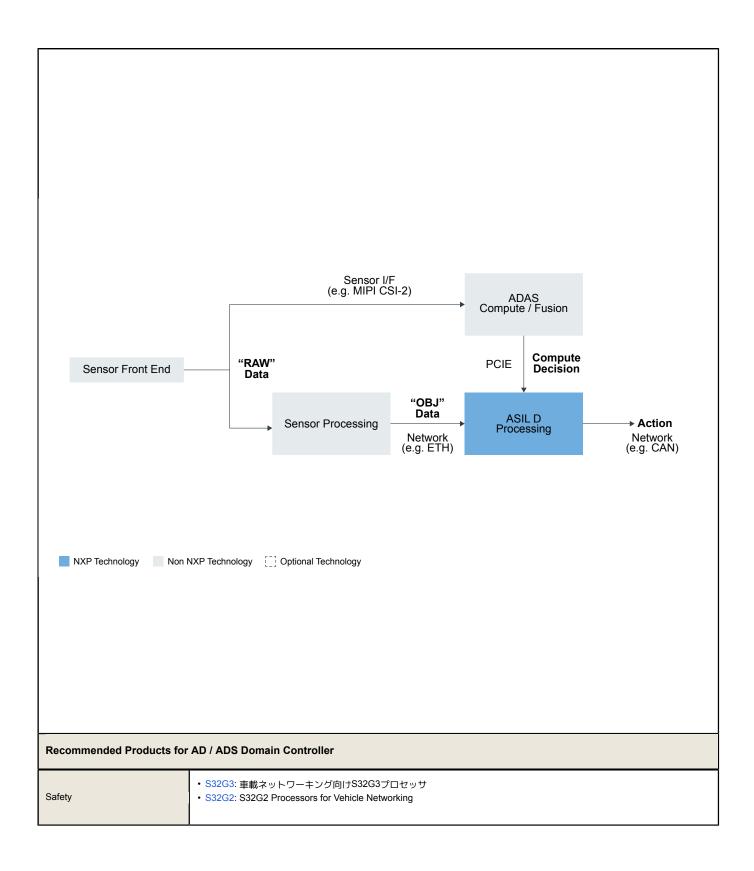
An important feature of ADAS application is that the time-sensitive data or secret data will be transferred between ECU, domain controller and CCU, which drives the requirement that the safety processor in ADAS domain to have excellent networking capabilities for data transactions. It should support processing and forwarding time-sensitive data via time-bounded communication technology, for example, Time-Sensitive-Networking. To protect secret data, accelerated security services based on hardware security engine will be required.

NXP S32G meets all the above-mentioned requirements of ADAS safety co-processor to:

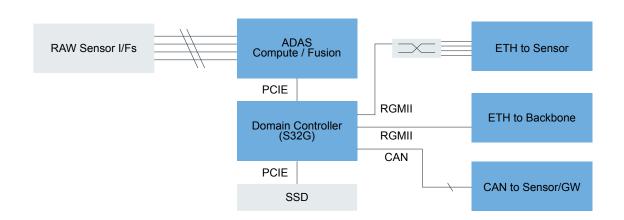
Support ASIL-D applications with powerful function safety features Support degraded mode of ADAS Support requirements of different ADAS levels Provide networking acceleration for CAN and Ethernet with TSN support Protect sensitive ADAS data with HSE Support high-level OS for SOA applications



AD / ADS Domain Controller Block Diagram



ADAS Example Block Diagram



NXP Technology Non NXP Technology [] Optional Technology

Recommended Products for ADAS Example

ADASコンピューティング/フュー ジョン	* LX2162A: Layerscape [®] LX2162A, LX2122A, LX2082A Processors • FS5600: Automotive Dual Buck Regulator and Controller with Voltage Monitors and Watchdog Timer • FS86: ドメイン・コントローラ用セーフティ・システムベーシス・チップ、ASIL BおよびDに適合 * LX2160A: Layerscape [®] LX2160A, LX2120A, LX2080A Processors
ドメイン・コントローラ	 S32G2: S32G2 Processors for Vehicle Networking S32G3: 車載ネットワーキング向けS32G3プロセッサ PF53: AVPとウォッチドッグを備えた12 A/8 A/15 Aコア電源レギュレータ VR5510: Multi-Channel (9) PMIC for S32G Processor – 8 High Power, 1 Low Power, Fit for ASIL D Safety Level
イーサネット	 TJA1103: TJA1103、ASIL B準拠車載イーサネット100BASE-T1 PHYトランシーバ TJA1104: TJA1104, MACsec対応ASIL-B準拠車載用イーサネット100BASE-T1 PHYトランシーバ TJA1120: TJA1120、ASIL B準拠、車載用イーサネット1000BASE-T1 PHYトランシーバ

イーサネット	 TJA1103: TJA1103、ASIL B準拠車載イーサネット100BASE-T1 PHYトランシーバ TJA1104: TJA1104, MACsec対応ASIL-B準拠車載用イーサネット100BASE-T1 PHYトランシーバ TJA1120: TJA1120、ASIL B準拠、車載用イーサネット1000BASE-T1 PHYトランシーバ
CAN	TJA1463: CAN Signal Improvement Capability Transceiver with Sleep Mode TJA1462: CAN Signal Improvement Capability Transceiver with Standby Mode

View our complete solution for Safety Processor for the ADAS Domain.

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