



S32J100 Evaluation Board for Ethernet Switches

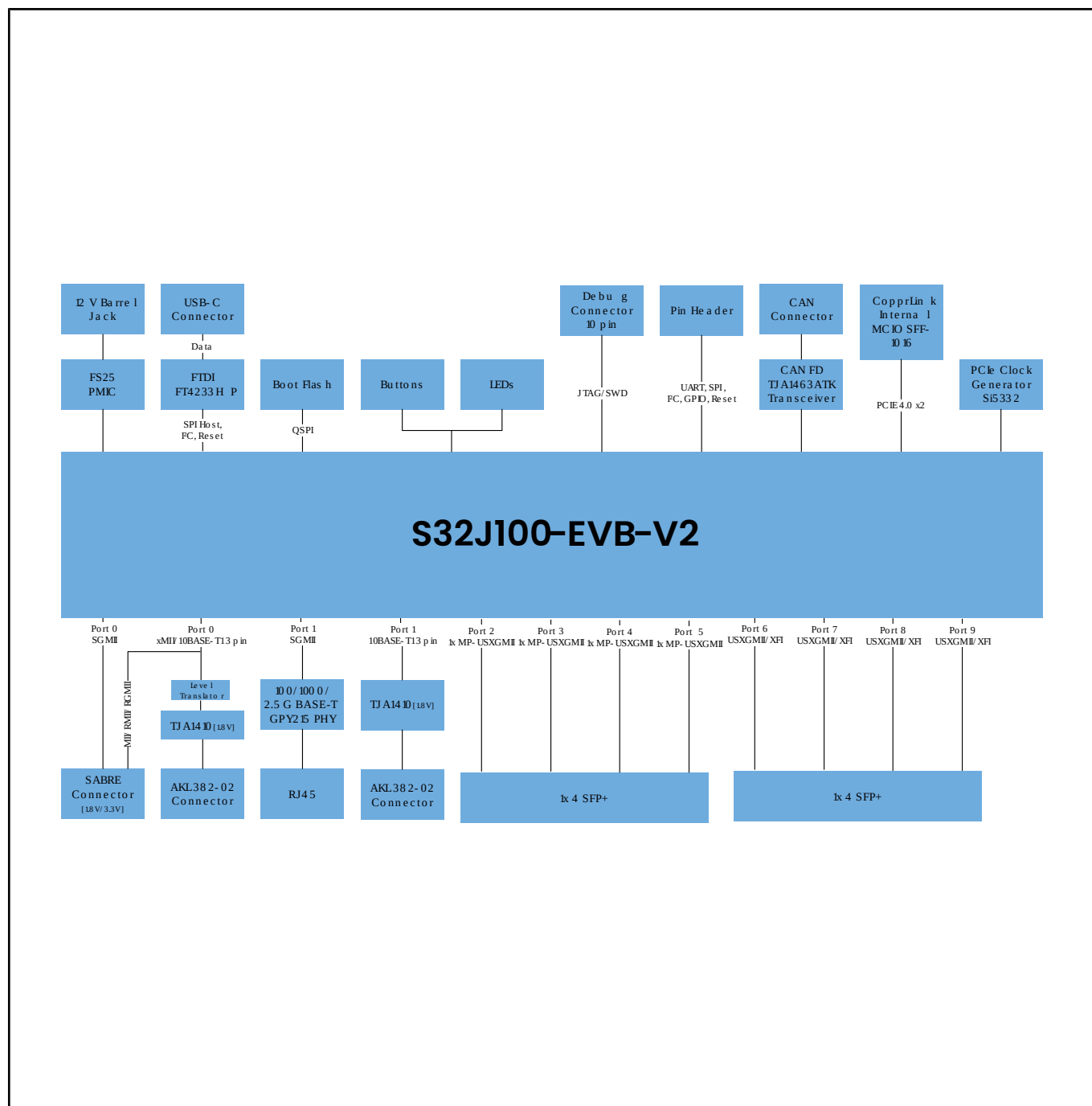
S32J100-EVB-V2 NEW

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The S32J100-EVB-V2 Evaluation Board provides a comprehensive hardware platform for evaluating, prototyping and accelerating software development with the S32J100 high-performance automotive Ethernet switch and network controller. Designed to support next-generation zonal architectures and software-defined vehicles, the board enables developers to explore advanced networking, traffic management, security and system integration capabilities in a real-world development environment. The S32J100 device combines high-bandwidth Ethernet switching, network control and embedded processing to deliver the performance and flexibility required for central compute, zonal controller, ADAS and in-vehicle infotainment applications.

The evaluation board provides access to the device's networking interfaces, processing resources and development tools, allowing engineers to quickly validate network architectures, evaluate Time-Sensitive Networking (TSN) features, test security functions and develop application software. As part of the NXP S32 platform and built on the common NETC networking foundation, the S32J100 helps maximize software reuse across multiple vehicle domains, reducing development effort and accelerating time to market.

S32J100 Evaluation Board Block Features Block Diagram



View additional information for [S32J100 Evaluation Board for Ethernet Switches](#).

Note: The information on this document is subject to change without notice.

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