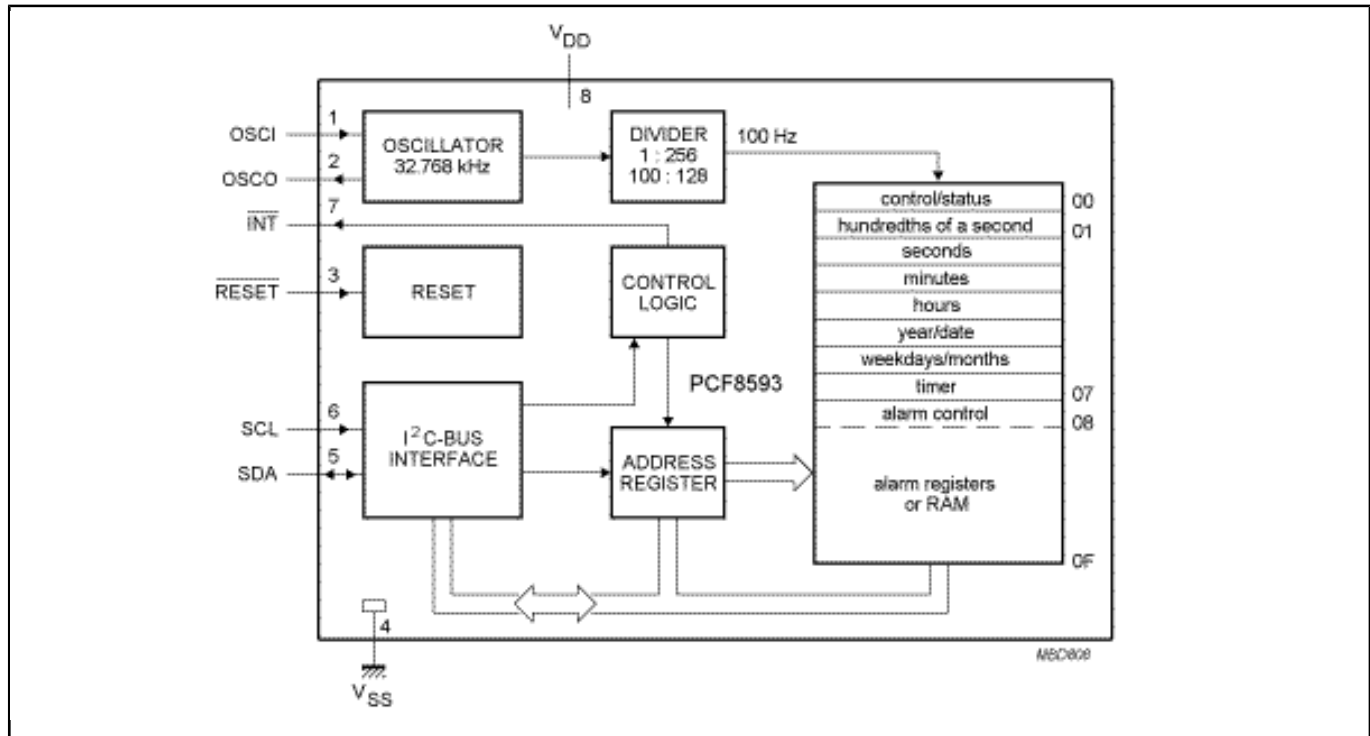# Real-Time Clock and Calendar with 8 B RAM

PCF8593

Last Updated: Apr 11, 2024

The PCF8593 is a CMOS1 clock and calendar circuit, optimized for low power consumption. Addresses and data are transferred serially via the two-line bidirectional I²C-bus. The built-in word address register is incremented automatically after each written or read data byte. The built-in 32.768 kHz oscillator circuit and the first 8 bytes of the RAM are used for the clock, calendar, and counter functions. The next 8 bytes can be programmed as alarm registers or used as free RAM space.

Block diagram: PCF8593P, PCF8593T Block Diagram

View additional information for Real-Time Clock and Calendar with 8 B RAM.

Note: The information on this document is subject to change without notice.