



# Universal LCD driver for low multiplex rates

## PCF8534AU

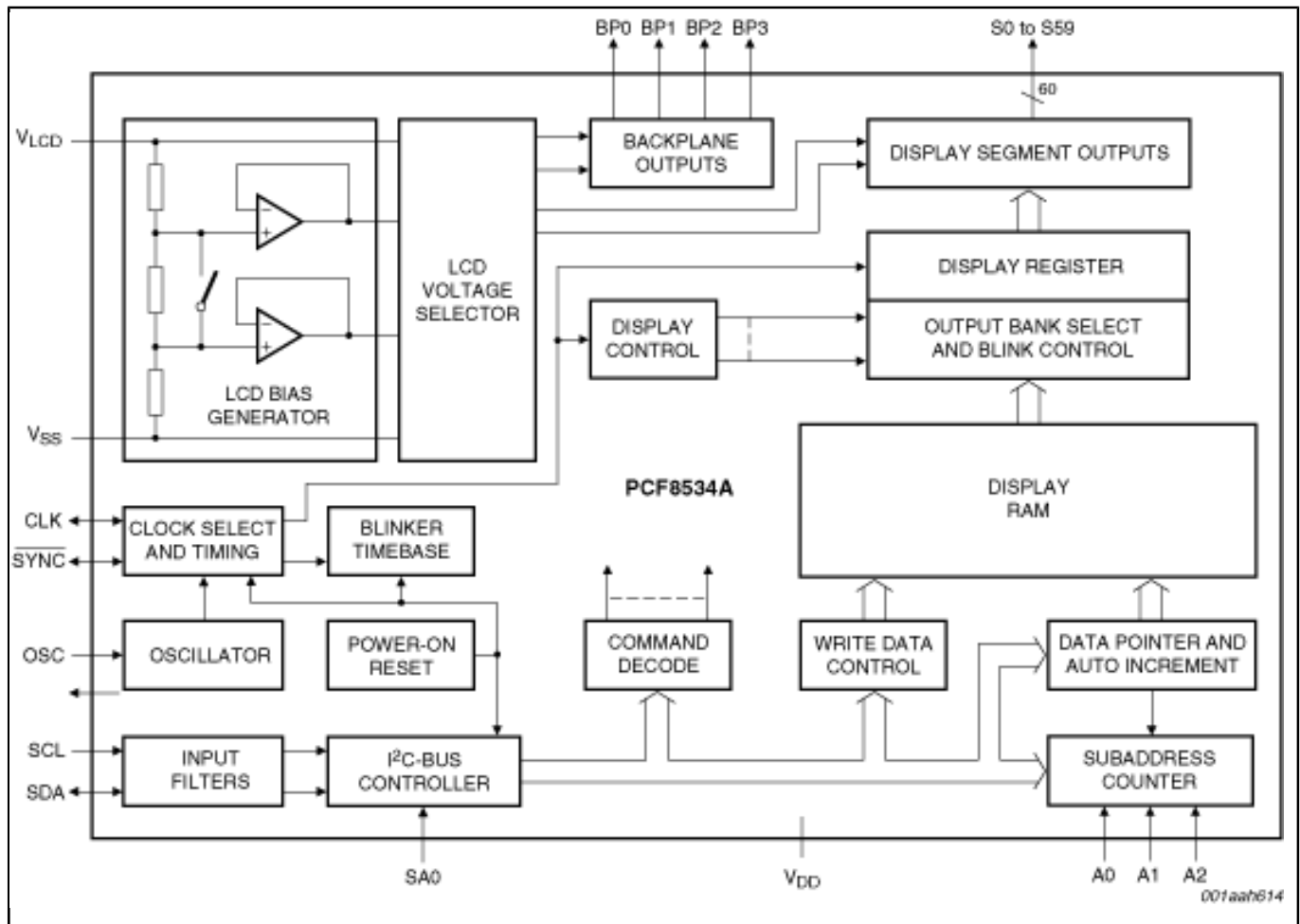
### Not Recommended for New Designs

このページでは、新規設計を推奨しない製品に関する情報を掲載しています。

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The PCF8534A is a peripheral device which interfaces to almost any Liquid Crystal Display (LCD) with low multiplex rates. It generates the drive signals for any static or multiplexed LCD containing up to four backplanes and up to 60 segments. It can be easily cascaded for larger LCD applications. The PCF8534A is compatible with most microcontrollers and communicates via the two-line bidirectional I<sup>2</sup>C-bus. Communication overheads are minimized by a display RAM with auto-incremented addressing, by hardware subaddressing, and by display memory switching (static and duplex drive modes).

## Block diagram: PCF8534AH, PCF8534AHL, PCF8534AU Block Diagram



View additional information for [Universal LCD driver for low multiplex rates](#).

**Note:** The information on this document is subject to change without notice.

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