



# Four-Channel Level Translating I<sup>2</sup>C-Bus/SMBus Repeater

## PCA9519

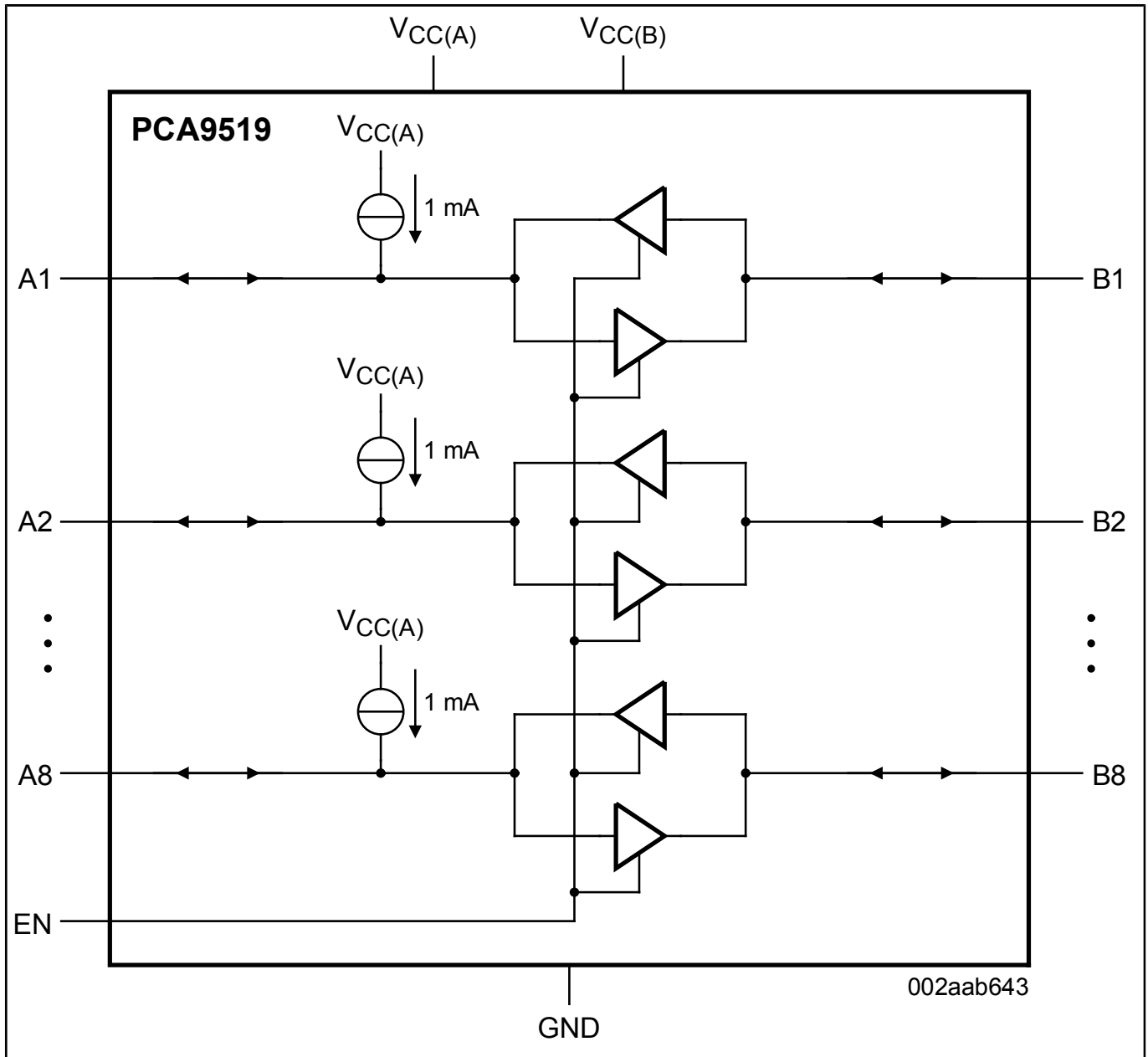
Last Updated: Jun 17, 2025

The PCA9519 is a 4-channel level translating I<sup>2</sup>C-bus/SMBus repeater that enables the processor low voltage 2-wire serial bus to interface with standard I<sup>2</sup>C-bus or SMBus I/O. While retaining all the operating modes and features of the I<sup>2</sup>C-bus system during the level shifts, it also permits extension of the I<sup>2</sup>C-bus by providing bidirectional buffering for both the data (SDA) and the clock (SCL) lines, thus enabling the I<sup>2</sup>C-bus or SMBus maximum capacitance of 400 pF on the higher voltage side. The SDA and SCL pins are over-voltage tolerant and are high-impedance when the PCA9519 is unpowered.

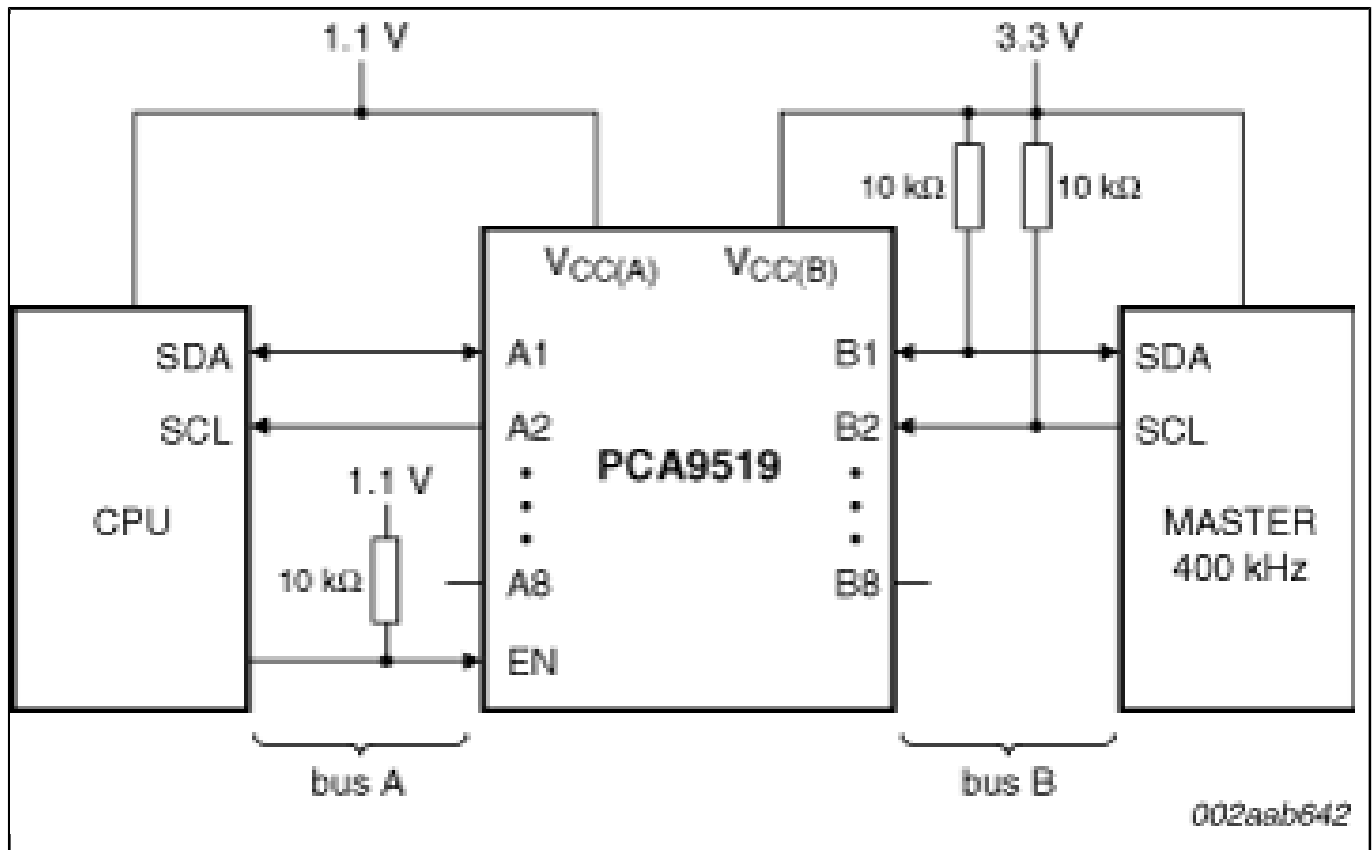
The port B drivers are compliant with SMBus I/O levels, while port A uses a current sensing mechanism to detect the input or output LOW signal which prevents bus lock-up. The port A uses a 1 mA current source for pull-up and a 200  $\Omega$  pull-down driver. This results in a LOW on port A accommodating smaller voltage swings. The output pull-down on the port A internal buffer LOW is set for approximately 0.2 V, while the input threshold of the internal buffer is set about 50 mV lower than that of the output voltage LOW. When the port A I/O is driven LOW internally, the LOW is not recognized as a LOW by the input. This prevents a lock-up condition from occurring. The output pull-down on the port B drives a hard LOW and the input level is set at 0.3 of SMBus or I<sup>2</sup>C-bus voltage level which enables port B to connect to any other I<sup>2</sup>C-bus device or buffer.

The PCA9519 drivers are not enabled unless VCC(A) is above 0.8 V and VCC(B) is above 2.5 V. The enable (EN) pin can also be used to turn the drivers on and off under system control. Caution should be observed to only change the state of the EN pin when the bus is idle.

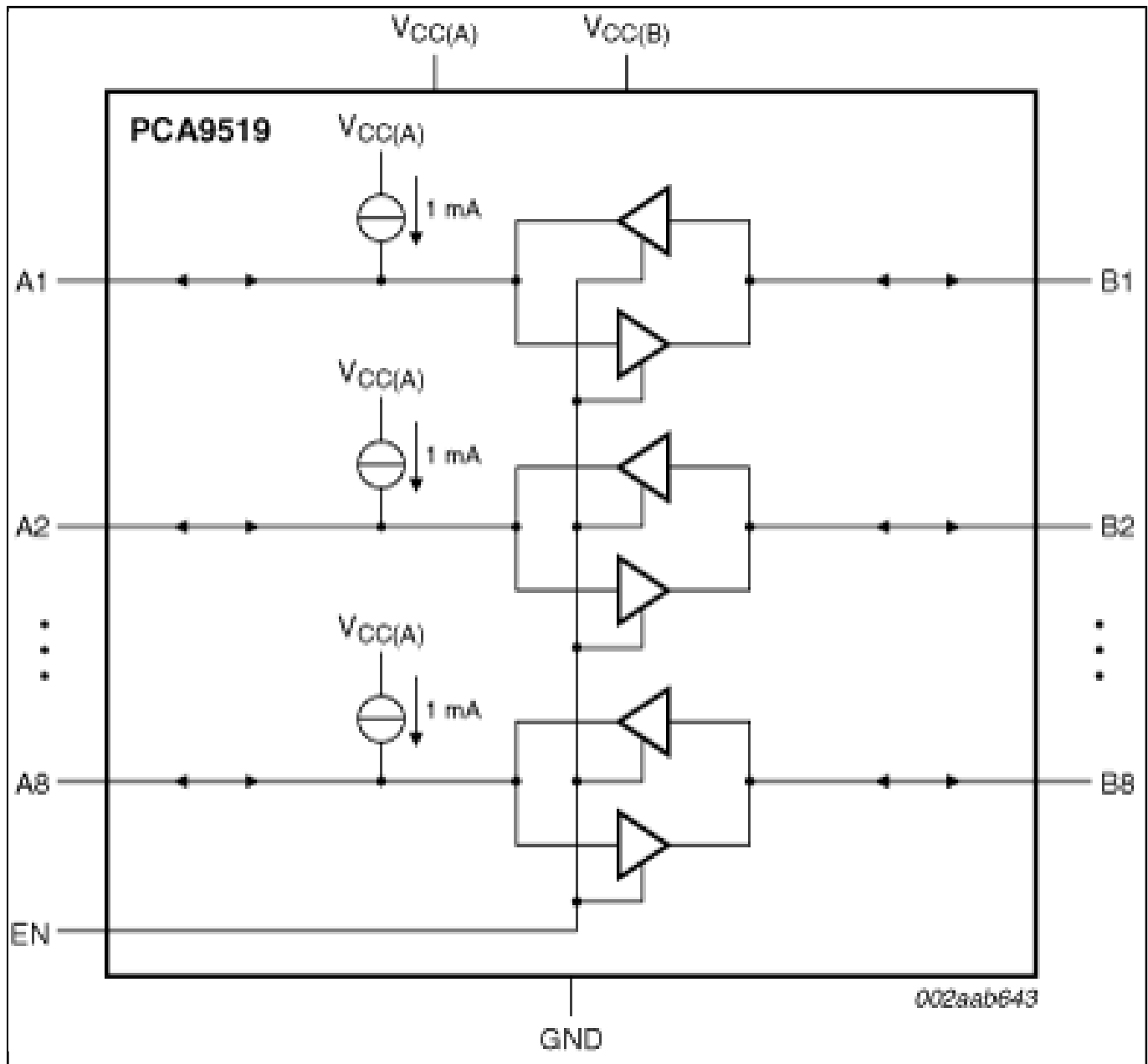
### PCA9519 Block Diagram Block Diagram



**Block diagram: PCA9519BS, PCA9519PW Block Diagram**



Block diagram: PCA9519BS, PCA9519PW Block Diagram



View additional information for [Four-Channel Level Translating I²C-Bus/SMBus Repeater](#).

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