



# QorIQ® P4080/P4040/P4081 Multicore Communications Processors

## P4080

### Not Recommended for New Designs

このページでは、新規設計を推奨しない製品に関する情報を掲載しています。

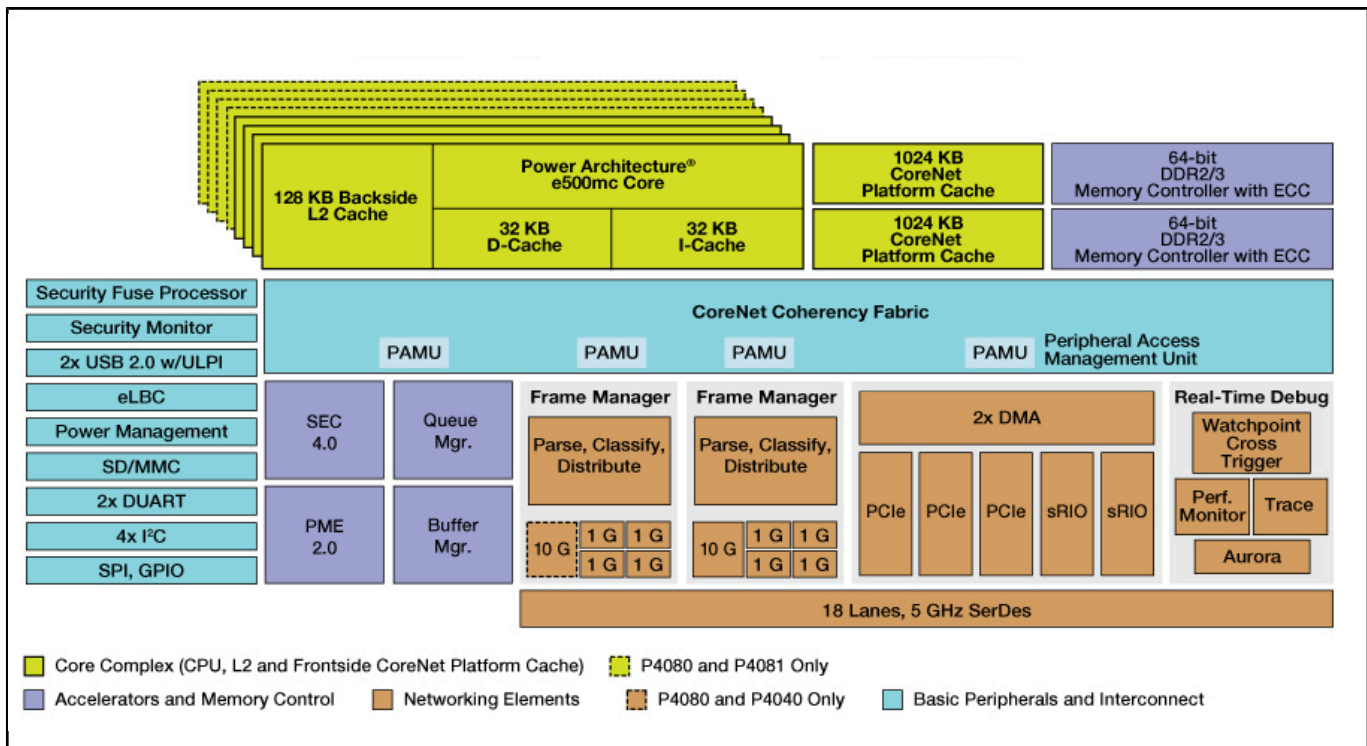
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P4080 and P4040 devices are "Not recommended for new designs", please use the replacement families Power Architecture (T4080), Arm Architecture (LS1046, LS2044, LS2080).

The P4080 was the flagship product of the QorIQ® family when it was first introduced to the market. It provided the foundation upon which the P5, P3, P2 and P1 processor families were built. In addition to those families, P4080 expanded its portfolio with the P4040 and P4081. These processors provide a performance, power and price to meet a broad spectrum of high-performance applications. It combines four to eight Power Architecture® e500mc cores—operating at frequencies up to 1.5 GHz—with high-performance data path acceleration logic and network and peripheral bus interfaces designed for 45 nm technology to deliver high-performance, next-generation networking services in a very low-power envelope.

The QorIQ P4080 processor can be used for combined control, data path and application layer processing. Its high level of integration offers significant performance benefits compared to multiple discrete devices while greatly simplifying board design. The processor is well-suited for applications that are highly compute-intensive, I/O intensive or both, making it ideal for applications such as enterprise and service provider routers, switches, base station controllers, radio network controllers (RNCs), long-term evolution (LTE) and general-purpose embedded computing systems in the networking, telecom/datacom, wireless infrastructure, military and aerospace markets.

### P4080 40 81 BD IMG Block Diagram



View additional information for [QorIQ® P4080/P4040/P4081 Multicore Communications Processors](#).

**Note:** The information on this document is subject to change without notice.

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