



# QorIQ® P1022 and P1013 Dual- and Single-Core Communications Processors

## P1022

新規採用非推奨

このページでは、新規設計を推奨しない製品に関する情報を掲載しています。

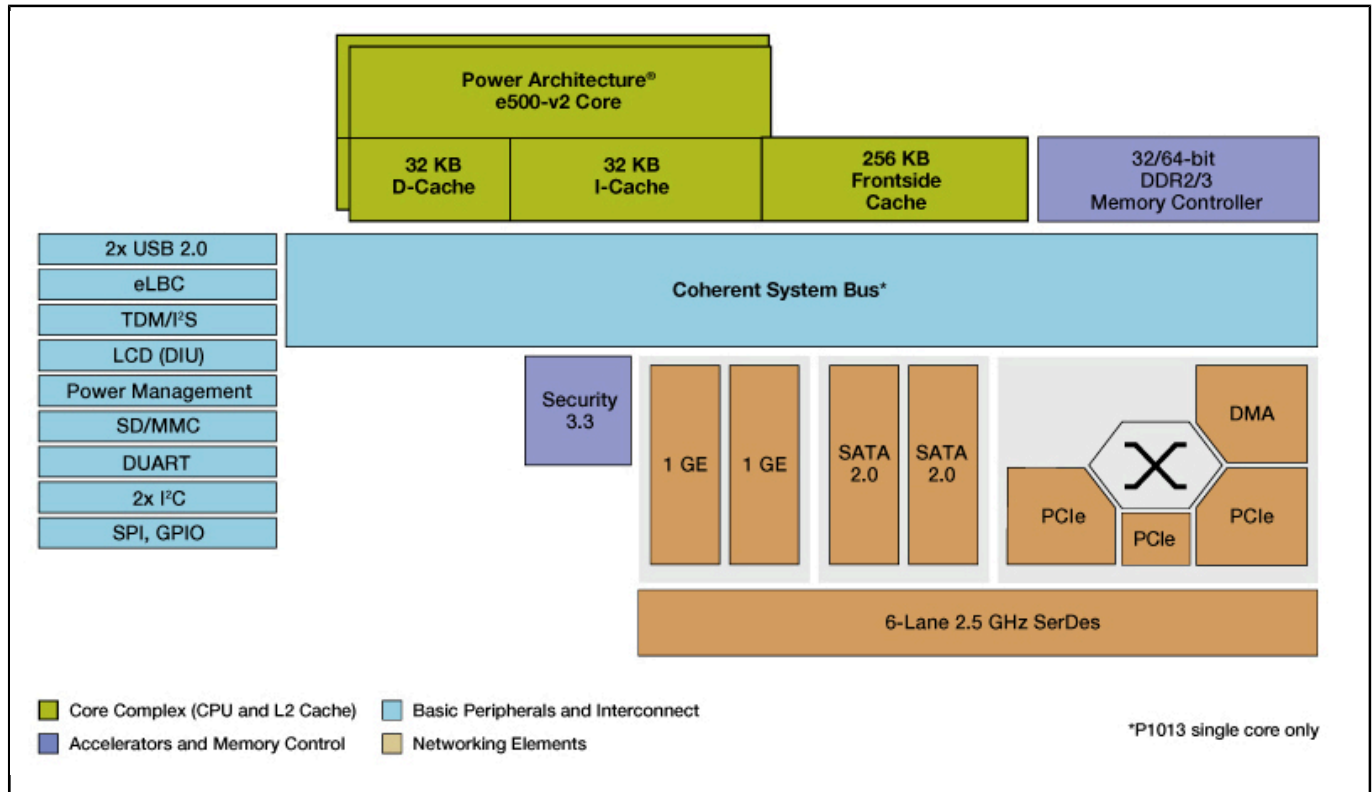
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P1022 device is "Not recommended for new designs", please use the replacement families Power Architecture (T1013, T1023), Arm Architecture (LS1012A, LS1023A).

The QorIQ® P1022 family of processors is designed to deliver complex application processing performance with exceptional feature integration and high-speed connectivity for IP networking and advanced media processing applications. This device combines dual e500 processor cores built on Power Architecture® technology with enhanced system peripherals and interconnects technology to balance processor performance with I/O system throughput. The P1013 is a single-core version of the P1022 family.

Ideally suited for the printing and imaging market, the P1022 processor includes advanced deep sleep power and energy management features that enable developers to design next-generation energy efficiency, environmental and governmental energy regulatory power requirements. This innovative deep sleep function enables <1W system designs where the P1022 processor is able to continue to receive packets and perform fast recovery from an extremely low-power sleep mode.

# NXP QorIQ P1022/13 Communication Processor Block Diagram Block Diagram



View additional information for [QorIQ® P1022 and P1013 Dual- and Single-Core Communications Processors](#).

Note: The information on this document is subject to change without notice.

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