



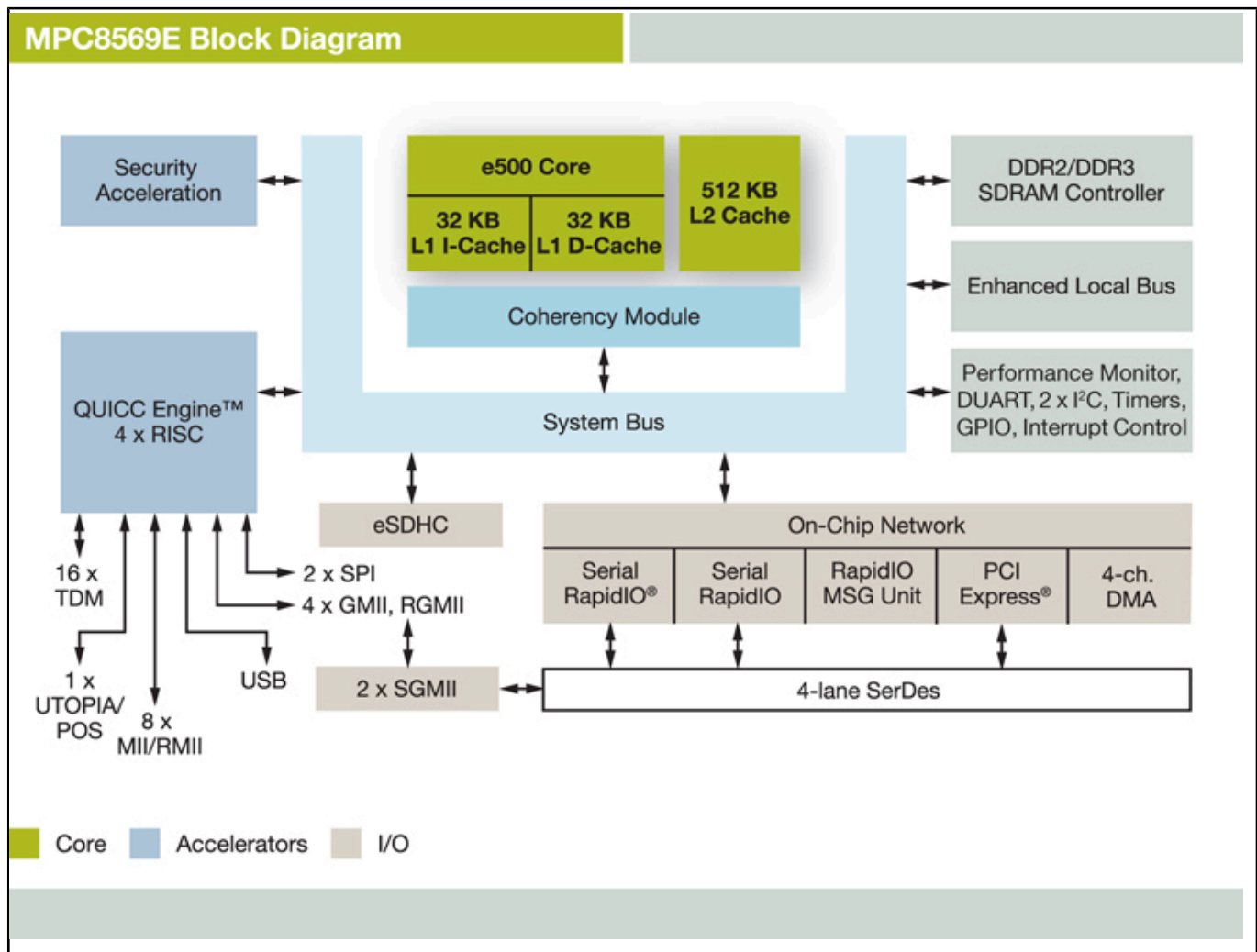
PowerQUICC® III Processor with DDR2/3

MPC8569E

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The MPC8569E PowerQUICC® III family is designed to address the increasing performance requirements for broadband access equipment including 3G/WiMAX/LTE base stations, radio network controllers, gateways and ATM/TDM/IP equipment. The MPC8569E helps facilitate both IP and multi-protocol solutions by combining a high-performance e500 processor core, built on Power Architecture® technology and scaling up to 1.33 GHz, with a flexible communications engine and high-speed system interfaces. The MPC8569E is designed to enable customers to handle many functions in a single-chip solution that otherwise would require multiple devices. This high level of integration provides savings in cost, power and board space.

Freescale PowerQUICC MPC8569E Communications Processor Block Diagram Block Diagram



View additional information for [PowerQUICC® III Processor with DDR2/3](#).

Note: The information on this document is subject to change without notice.

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