



# PowerQUICC® III Processor with TDM, DDR, PCI, 1 GB Ethernet, Security, CPM with UTOPIA

## MPC8555E

### Not Recommended for New Designs

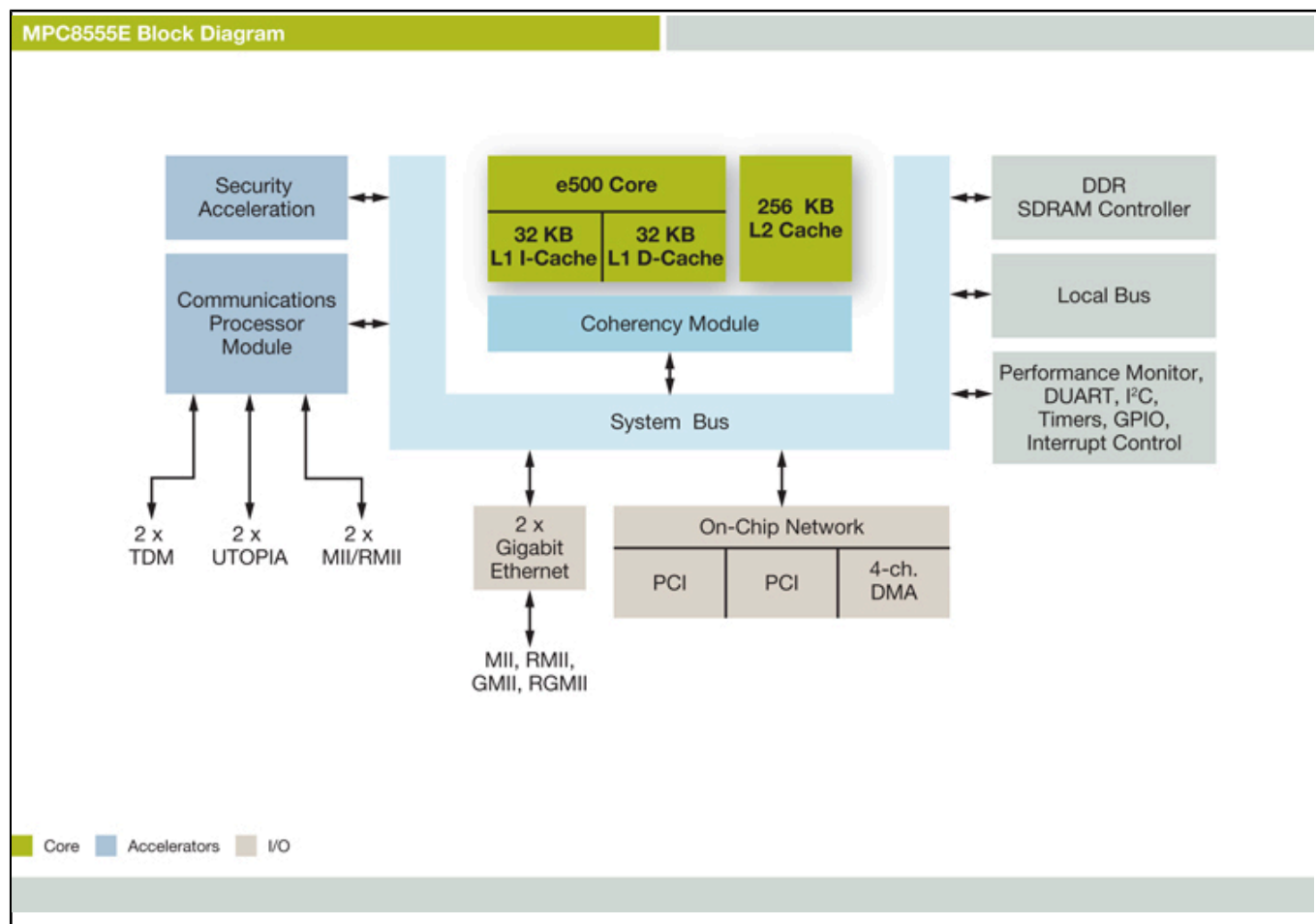
このページでは、新規設計を推奨しない製品に関する情報を掲載しています。

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Our MPC8555E PowerQUICC® III host processor integrates a wide range of advanced NXP® technologies, modular cores and peripherals. Leveraging Our system-on-chip (SoC) PowerQUICC III platform architecture, the MPC8555E combines the powerful e500 core and communications peripheral technology to balance processor performance with I/O system throughput. The processor is designed to offer clock speeds scaling from 533 MHz to 1 GHz.

Access to the errata document for this device requires an NDA. Contact your local NXP Sales Office or NXP Authorized Distributor.

## Freescale PowerQUICC MPC8555E Communications Processor Block Diagram Block Diagram



View additional information for [PowerQUICC® III Processor with TDM, DDR, PCI, 1 GB Ethernet, Security, CPM with UTOPIA](#).

**Note:** The information on this document is subject to change without notice.

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