



32 Bit Microcontroller

MPC566

Not Recommended for New Designs

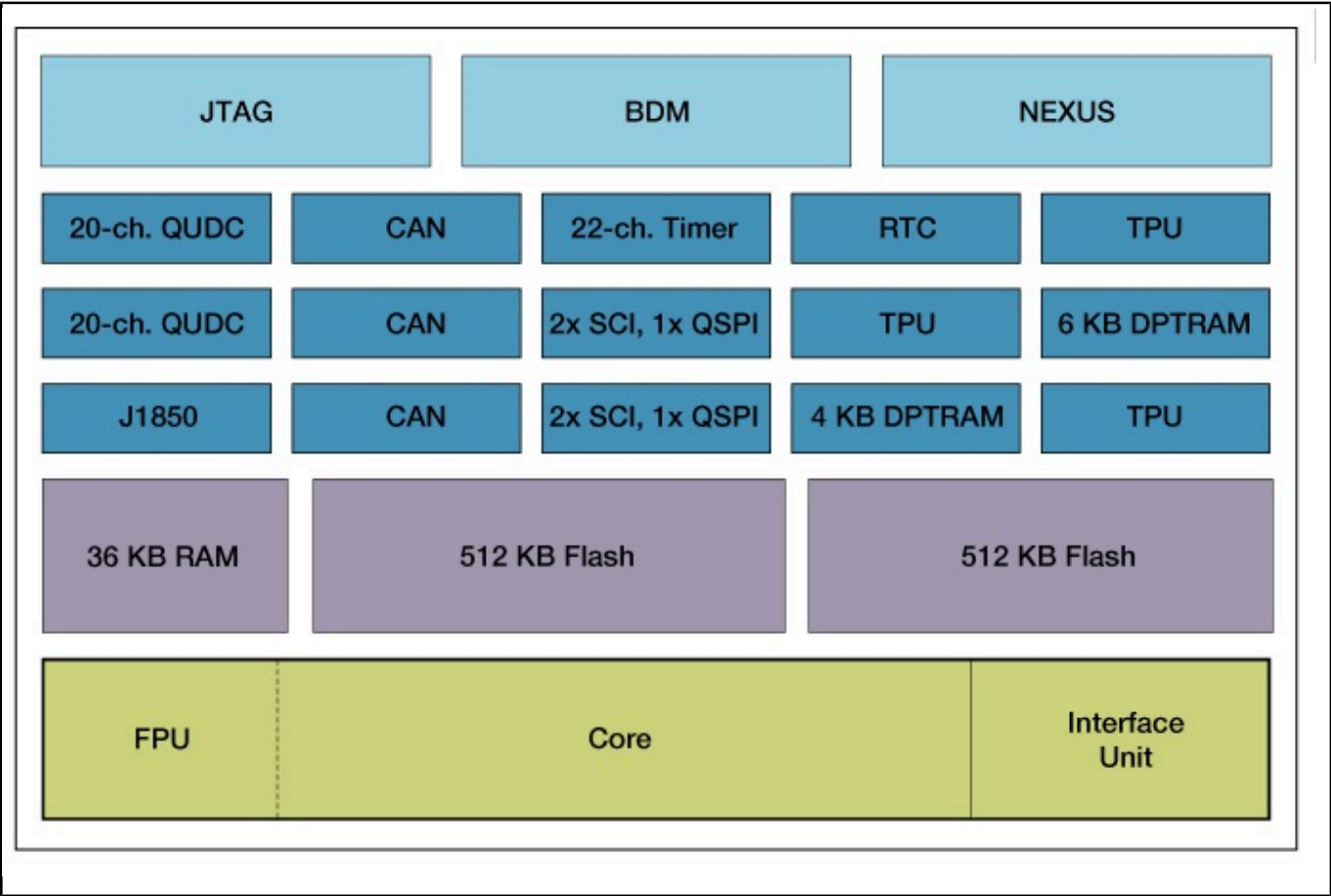
このページでは、新規設計を推奨しない製品に関する情報を掲載しています。

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The MPC566 integrates a Power Architecture® core with a dual precision floating point unit and BCC, 36 KB of RAM and 1 MB of flash memory.

The MPC566, an upgraded version of the MPC565, offers code compression to enable more efficient use of internal or external flash memory. Code compression is optimized for automotive (non-cached) applications and the new scheme increases performance by 40 to 50 percent.

MPC566 Block Diagram Block Diagram



View additional information for [32 Bit Microcontroller](#).

Note: The information on this document is subject to change without notice.