



# **MCX N23x Highly Integrated MCUs with On-Chip Accelerators, Intelligent Peripherals and Advanced Security**

## **MCX-N23X**

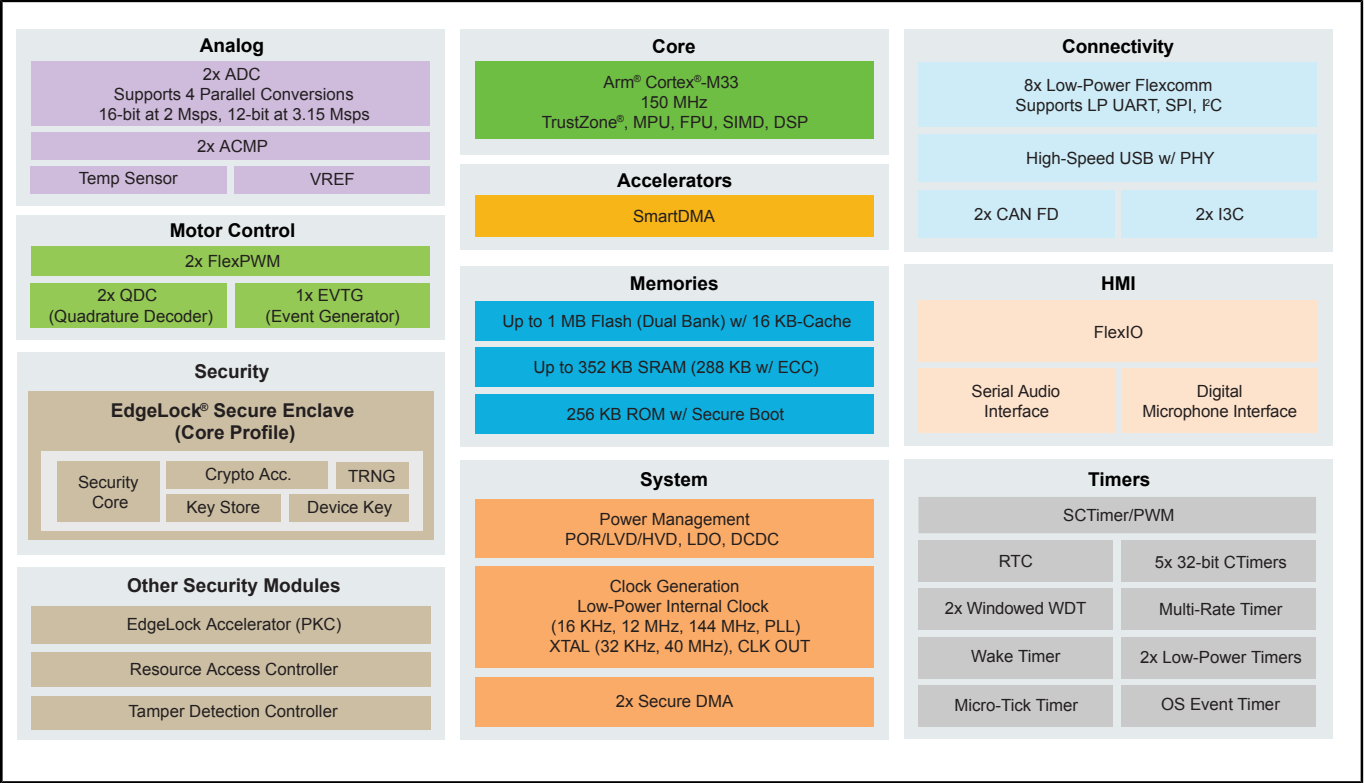
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The MCX N23x is based on a high-performance Arm® Cortex®-M33 running up to 150 MHz, with 1 MB of Flash, 352 kB ECC RAM and SmartDMA. The MCX N23x is optimized for cost, memory and system performance and offers a single core option with efficient distribution of workloads to the analog and digital peripherals.

The EdgeLock Secure Enclave on the MCX N23x is a self-contained, on-die hardware security subsystem that has its own dedicated security core, internal ROM, secure RAM and it supports state-of-the-art side-channel attack-resilient symmetric and asymmetric crypto accelerators and hashing functions for security services.

The MCX N23x devices are supported by the MCUXpresso Developer Experience to optimize, ease and help accelerate embedded system development.

# MCX N23x MCUs Block Diagram



View additional information for [MCX N23x Highly Integrated MCUs with On-Chip Accelerators, Intelligent Peripherals and Advanced Security](#).

**Note:** The information on this document is subject to change without notice.