



# **MCX A34 Mixed-Signal, Optimized for Motor Control and High-Performance Analog Peripherals**

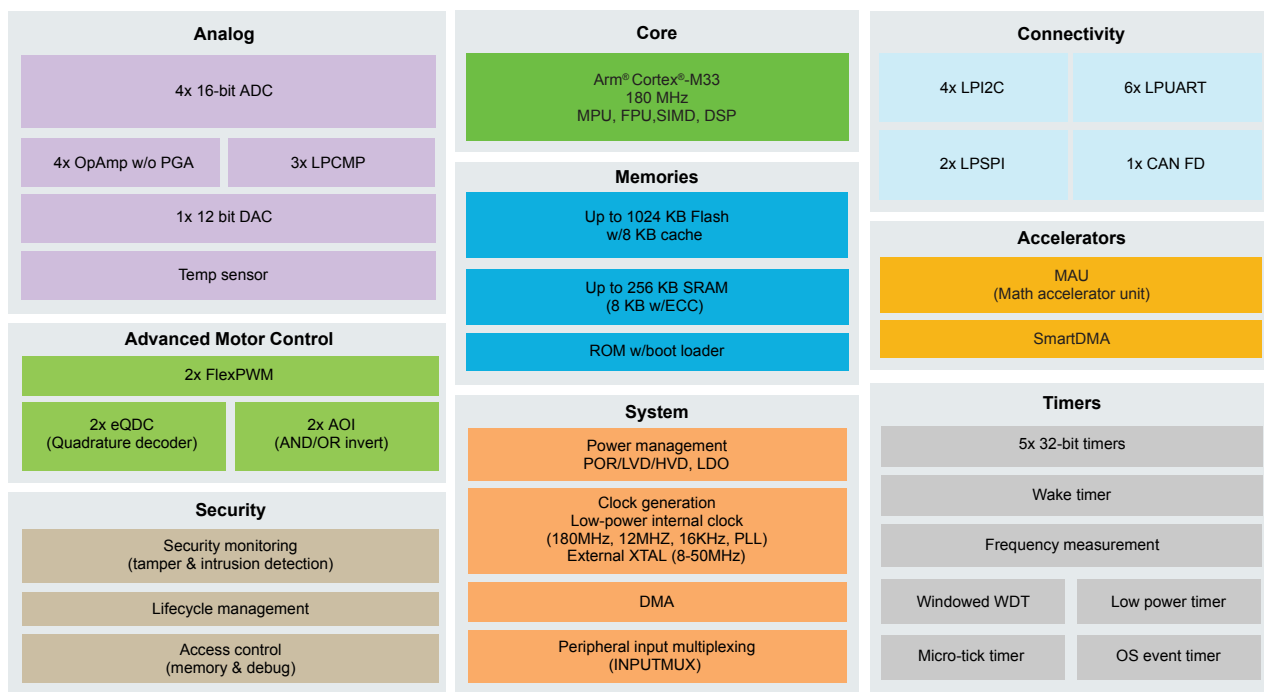
## **MCX-A34X**

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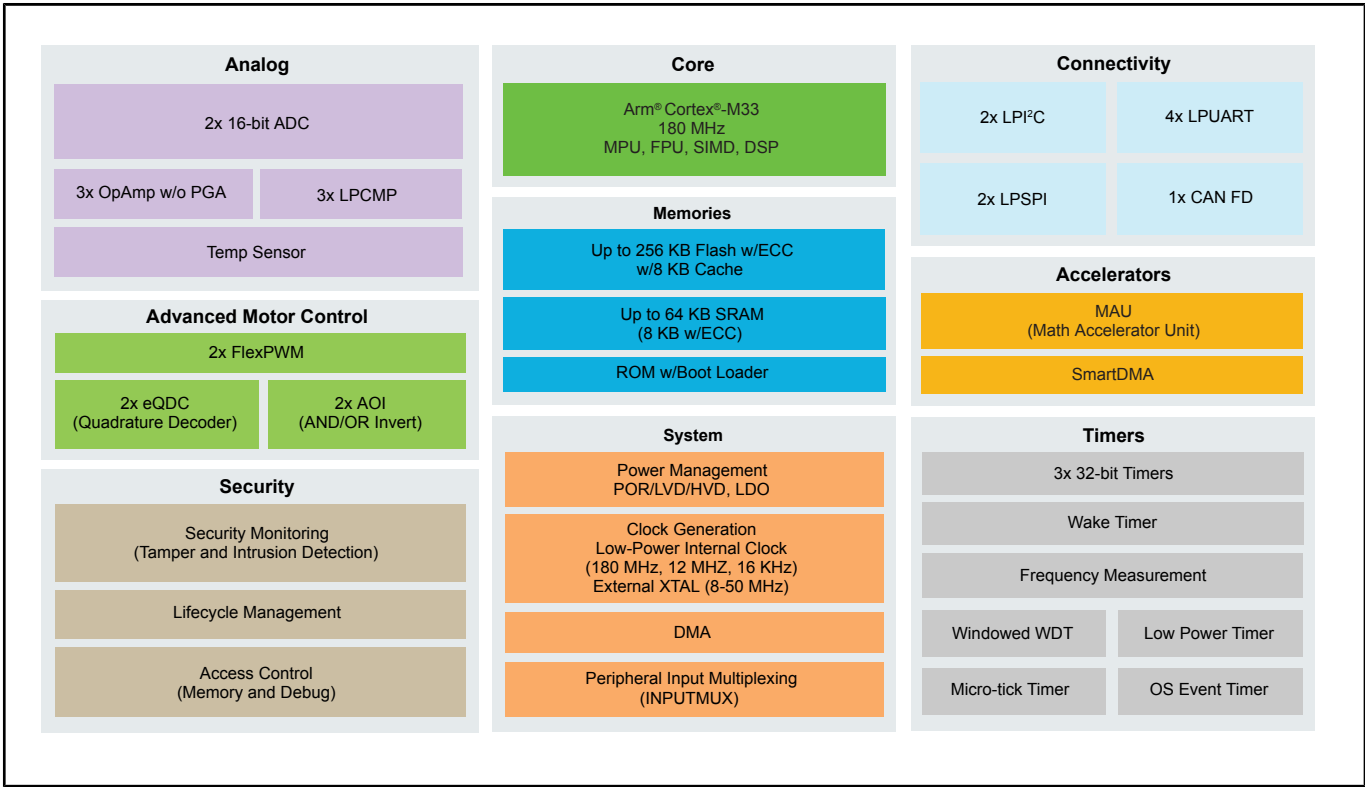
The MCX A34 family of Mixed-Signal MCUs, including the MCX A345, MCX A346, MCXA343, and MCXA344, is purpose-built for motor control applications. Powered by high-performance Arm® Cortex®-M33 cores running up to 180 MHz, these devices offer flexible memory configurations ranging from 256KB to 1MB Flash and 64KB to 256KB RAM. Optimized with the MAU engine, the MCX A34 MCUs integrate 2x FlexPWM modules with 4 submodules, AOI, and up to 4x ADCs, depending on the device. They also feature a rich set of serial peripherals, including SmartDMA, to support complex control and communication needs.

All devices in the family are supported by the MCUXpresso Developer Experience, which streamlines development and accelerates time-to-market for embedded systems.

## MCX A345 and A346 MCUs Block Diagram



## MCX A343 and A344 MCUs Block Diagram



View additional information for [MCX A34 Mixed-Signal, Optimized for Motor Control and High-Performance Analog Peripherals](#).

**Note:** The information on this document is subject to change without notice.

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